Project	IEEE 802.16 Broadband Wireless Access Working Group http://ieee802.org/16 >				
Title	Corrections for AAS preamble PHY Modifier in OFDMA PHY				
Date Submitted	2004-11-12				
Source(s)	Yuval Lomnitz, Hassan Yaghoobi	Intel Corp	yuval.lomnitz@intel.com, hassan.yaghoobi@intel.com		
	Ran Yaniv, Tal Kaitz	Alvarion, Ltd	ran.yaniv@alvarion.com, tal.kaitz@alvarion.com		
	Dave Pechner, Todd Chauvin, John Dogan, Doug Dahlby, Adam Kerr	ArrayComm Inc.	dpechner@arraycomm.com, chauvin@arraycomm.com, dahlby@arraycomm.com		
Re:	IEEE P802.16-REVd/D5				
Abstract	This contribution introduces corrections for AAS preamble PHY Modifier in OFDMA PHY				
Purpose	Adopt into P802.16d/D5 corrigenda				
Notice	This document has been prepared to assist IEEE 802.16. It is offered as a basis for discussion and is not binding on the contributing individual(s) or organization(s). The material in this document is subject to change in form and content after further study. The contributor(s) reserve(s) the right to add, amend or withdraw material contained herein.				
Release	The contributor grants a free, irrevocable license to the IEEE to incorporate material contained in this contribution, and any modifications thereof, in the creation of an IEEE Standards publication; to copyright in the IEEE's name any IEEE Standards publication even though it may include portions of this contribution; and at the IEEE's sole discretion to permit others to reproduce in whole or in part the resulting IEEE Standards publication. The contributor also acknowledges and accepts that this contribution may be made public by IEEE 802.16.				
Patent Policy and Procedures	The contributor is familiar with the IEEE 802 16 Patent Policy and Procedures				

Corrections for AAS Preamble PHY Modifiers in OFDMA PHY

1 Problems with the current AAS Preamble definition

The definition of AAS Preambles contains ambiguities and contradictions that need to be resolved:

- 1. The preamble definition of equation (100) omits the time shift.
- 2. There are several errors in table PHY_MOD_DL_IE (Table 284).
- 3. There are several errors in table PHY MOD UL IE (Table 300).

2 Proposed Text Change: Equation (100)

Section 8.4.5.3.11:

[Modify the text beginning at line 16 on page 532 with the following:]

In the case when the preamble is cyclically delayed in time by $\frac{kK}{k}$ samples, the preamble will contribute a component $\frac{s^2(t)s^2}{n}(t)$ to the transmitted waveform for the n^{th} preamble symbol as defined below:

[Replace equation (100) with the following:]

$$s'_{n}(t) = \text{Re}\left\{e^{j2\pi f_{c}t} \sum_{m=-(Nused-1)/2}^{(Nused-1)/2} c_{m} \times e^{j2\pi m\Delta f(t-T_{g}-K/F_{s})} e^{j2\pi nM}\right\},$$
(100)

[Append this text to the end of the paragraph containing equation (100)]

The subscript n is the AAS preamble symbol number (0...L-1) to which the waveform is applied where L is the number of symbols occupied by the preamble (as defined in the AAS IE). The parameter M is the 'Preamble Symbol Shift Index' from either the PHY_MOD_DL_IE (Table 284) or the PHY_MOD_UL_IE (Table 300) as appropriate.

[Editorial improvements to equation (101)]

- 1) Replace 'N_{Used-subcarriers'} with 'N_{used}"
- 2) Replace the period at the end of equation (101) with a comma
- 2) Lowercase the 'Where' in line 40 on page 532.

3 Proposed Text Change: PHY_MOD_DL_IE

[Replace Table 284 with the following:]

Table 284—OFDMA DL-MAP Physical Modifier IE format

Extended DIUC Length 4 bits Length = 0x03 Preamble Modifier Type 1 bit 0 -frequency shifted preamble 1 - time shifted Preamble if (Preamble Frequency Shift Index 4 bits Indicates the value of K in equation (101) selse { Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 - 0 sample cyclic shift 1 - Nfft/14 sample cyclic shift 1 - Nfft/14*13 sample cyclic shift 14-15 - reserved For AMC permutation, 0 - 0 sample cyclic shift 1 - Nfft/9 sample cyclic shift 1 - Nfft/9 sample cyclic shift 1 - Nfft/9 sample cyclic shift			
Preamble Modifier Type I bit O –frequency shifted preamble 1 – time shifted Preamble Indicates the value of K in equation (101) Preamble Time Shift Index Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 – 0 sample cyclic shift 1 – Nfft/14 sample cyclic shift 1 – Nfft/14*13 sample cyclic shift 14-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift	Extended DIUC	4 bits	PHYMOD = 0x08
1 - time shifted Preamble	Length	4 bits	Length = $0x03$
if (Preamble Modifier Type == 0) { Preamble Frequency Shift Index } else { Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 - 0 sample cyclic shift 1 - Nfft/14 sample cyclic shift 1 - Nfft/14*13 sample cyclic shift 14-15 - reserved For AMC permutation, 0 - 0 sample cyclic shift	Preamble Modifier Type	1 bit	0 –frequency shifted preamble
Preamble Frequency Shift Index } else { Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 - 0 sample cyclic shift 1 - Nfft/14 sample cyclic shift 13 - Nfft/14*13 sample cyclic shift 14-15 - reserved For AMC permutation, 0 - 0 sample cyclic shift			1 – time shifted Preamble
Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 – 0 sample cyclic shift 1 – Nfft/14 sample cyclic shift 13 – Nfft/14*13 sample cyclic shift 14-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift	if (Preamble Modifier Type == 0) {		
Preamble Time Shift Index 4 bits Specifies the cyclic time shift in equation (10 For PUSC, 0 – 0 sample cyclic shift 1 – Nfft/14 sample cyclic shift 13 – Nfft/14*13 sample cyclic shift 14-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift	Preamble Frequency Shift Index	4bits	Indicates the value of K in equation (101)
For PUSC, 0 – 0 sample cyclic shift 1 – Nfft/14 sample cyclic shift 13 – Nfft/14*13 sample cyclic shift 14-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift	,		
	Preamble Time Shift Index	4 bits	0 – 0 sample cyclic shift 1 – Nfft/14 sample cyclic shift 13 – Nfft/14*13 sample cyclic shift 14-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift
8 – Nfft/9*8 sample cyclic shift 9-15 – reserved			
}	}		
(100): 0 – M=0 1 - M=1/L,	Preamble Symbol Shift Index	1 bit	0 – M=0 1 - M=1/L, where L is the number of symbols occupied by the preamble, as defined in the AAS_DL_IE
Reserved 3 2 bits	Reserved	3 2 bits	
}	}		

4 Proposed Text Change: PHY_MOD_UL_IE

Section 8.4.5.4.14:

[Replace Table 300 with the following:]

Table 300—OFDMA UL-MAP Physical Modifier IE format

PHY MOD UL IE() {		
Extended UIUC	4 bits	PHYMOD = 0x05
Length	4 bits	Length = $0x03$
Preamble Modifier Type	1 bit	0 – frequency shifted preamble 1 – time shifted Preamble
if (Preamble Modifier Type == 0) {		
Preamble Frequency Shift Index	4bits	Indicates the value of <i>K</i> in equation (101)
} else {		
Preamble Time Shift Index	4 bits	Specifies the cyclic time shift in equation (100): For PUSC, 0 – 0 sample cyclic shift 1 – Nfft/4 sample cyclic shift 3 – Nfft/4*3 sample cyclic shift 4-15 – reserved For optional PUSC, 0 – 0 sample cyclic shift 1 – Nfft/3 sample cyclic shift 2 – Nfft/3*2 sample cyclic shift 3-15 – reserved For AMC permutation, 0 – 0 sample cyclic shift 1 – Nfft/9 sample cyclic shift 8 – Nfft/9*8 sample cyclic shift 9-15 – reserved
Preamble Symbol Shift Index	1 bit	Specifies the cyclic symbol shift M in equation (100): 0 - M=0 1 - M=1/L, where L is the number of symbols occupied by the preamble, as defined in the AAS_UL_IE (Table 291, Section 8.4.5.4.6)
Reserved	3 2 bits	
Nesei veu	3 2 UIIS	
}		