Modem Architecture for Dynamic Modulation with Concatenated Coding and Interleaving

IEEE 802.16 Presentation Submission Template (Rev. 8)

Document Number:

IEEE 802.16.1pp-00/30 Date Submitted: 2000-05-04 Source: John Liebetreu Voice: 480-607-4830 Fax: 480-607-4806 Sicom, Incorporated 7585 East Redfield Road E-mail: john@sicom.com Scottsdale, Arizona 85260

Venue:

Meeting #7, Gaithersburg, Maryland

Base Document:

none

Purpose:

This document describes a modem architecture that supports dynamic modulation with concatenated Reed-Solomon convolutional coding and interleaving in the downstream link.

Notice:

This document has been prepared to assist IEEE 802.16. It is offered as a basis for discussion and is not binding on the contributing individual(s) or organization(s). The material in this document is subject to change in form and content after further study. The contributor(s) reserve(s) the right to add, amend or withdraw material contained herein.

Release:

The contributor grants a free, irrevocable license to the IEEE to incorporate text contained in this contribution, and any modifications thereof, in the creation of an IEEE Standards publication; to copyright in the IEEE's name any IEEE Standards publication even though it may include portions of this contribution; and at the IEEE's sole discretion to permit others to reproduce in whole or in part the resulting IEEE Standards publication. The contributor also acknowledges and accepts that this contribution may be made public by IEEE 802.16.

IEEE 802.16 Patent Policy:

The contributor is familiar with the IEEE 802.16 Patent Policy and Procedures (Version 1.0) http://ieee802.org/16/ipr/patents/policy.html, including the statement "IEEE standards may include the known use of patent(s), including patent applications, if there is technical justification in the opinion of the standards-developing committee and provided the IEEE receives assurance from the patent holder that it will license applicants under reasonable terms and conditions for the purpose of implementing the standard."

Early disclosure to the Working Group of patent information that might be relevant to the standard is essential to reduce the possibility for delays in the development process and increase the likelihood that the draft publication will be approved for publication. Please notify the Chair <mailto:r.b.marks@ieee.org> as early as possible, in written or electronic form, of any patents (granted or under application) that may cover technology that is under consideration by or has been approved by IEEE 802.16. The Chair will disclose this notification via the IEEE 802.16 web site

http://ieee802.org/16/ipr/patents/letters.

Modem Architecture Description

- Coding structure for downstream link
 - Concatenated coding with interleaver
 - Inner convolutional code
 - Outer Reed-Solomon code
- Advantages
 - High coding gain
 - Supports long block lengths
 - Supports multiple coding strategies in the channel
 - Well-known complexity for subscriber terminal
 - Additional complexity is concentrated at hub

Modem Architecture Description

- Downstream FDD transmission
- Dynamic variation in modulation format
 - Code rate / type (coding strategy)
 - Modulation order
- Supports multiple codes in the channel
 - Code assignment is partitioned by service requirement
 - Supports variability in block lengths
 - Coding gain improvement for all codes
 - Low-latency applications

Modulator Architecture Description



Typical Encoding FEC Processor



Demodulator Architecture Description

