Project	**IEEE 802.16 Broadband Wireless Access Working Group <http://ieee802.org/16>**
Title	**Buncha figures**
Date Submitted	**2002-01-16**
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Re:	802.16, Ballot 4
Abstract	Some complaints on figures in comment cycle, proposed modifications suggested.
Purpose	Decision on figure replacement in 802.16a/D1
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Buncha Figures

Nico van Waes
Nokia
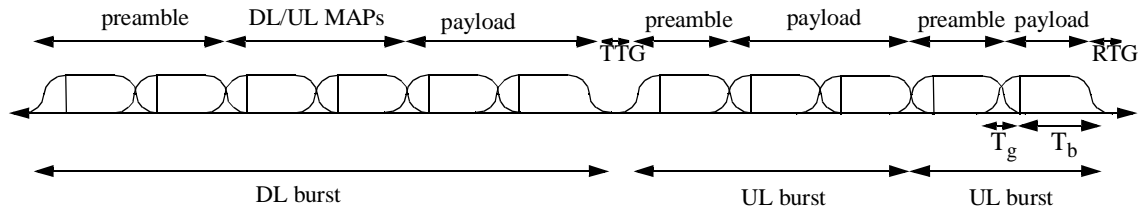
Figure 203—Time Plan - One TDD time frame

OFDMA Symbol Index

| | k-2 | k-1 | k | k+1 | k+2 | k+3 | k+4 | k+5 | k+6 | k+7 | k+8 | k+9 | k+10 | k+11 |

OFDMA Subchannel Index

Numbers n, n+1, n+2,... in the boxes indicate indices on FEC blocks which are transmitted on the indicated subchannel, in the indicated three symbols.
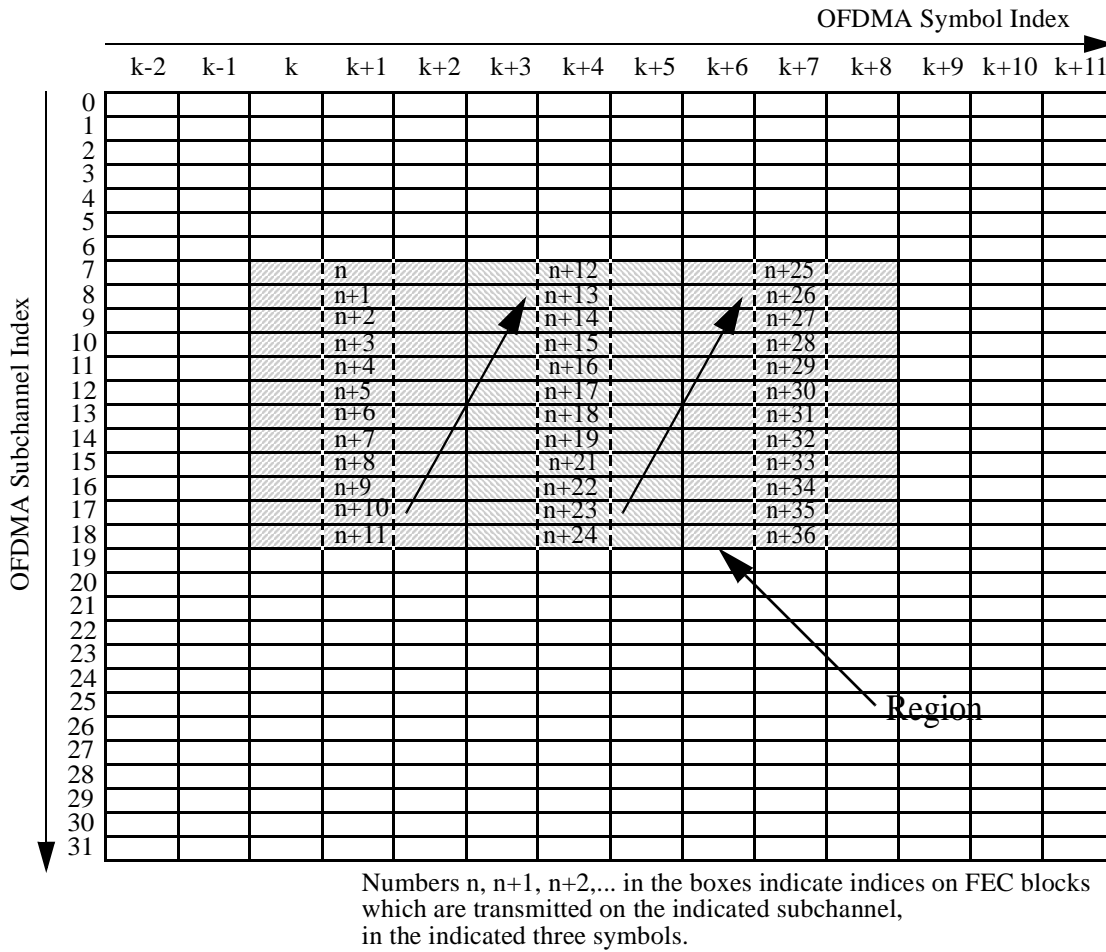
Figure 222—Mapping of FEC Blocks to OFDMA Subchannels and Symbols

8.3.5.3.3.6.1 changes below as agreed in comment 9997 at session #16. Editorial updating was missed as stated in comment 706

8.3.5.3.3.6.1 Scrambling (Randomization) Initialization

The scrambler (see clause 8.3.5.3.2.1.1) is initialized with the following vector.
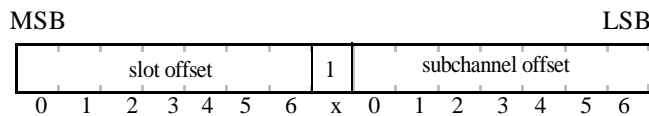
MSB   LSB

| slot offset | 1 | subchannel offset |
| 0 1 2 3 4 5 6 | x | 0 1 2 3 4 5 6 |

Figure 231—OFDMA Randomizer Initialization vector

time

OFDMA symbol period

Example
"Basic Allocation"

Example
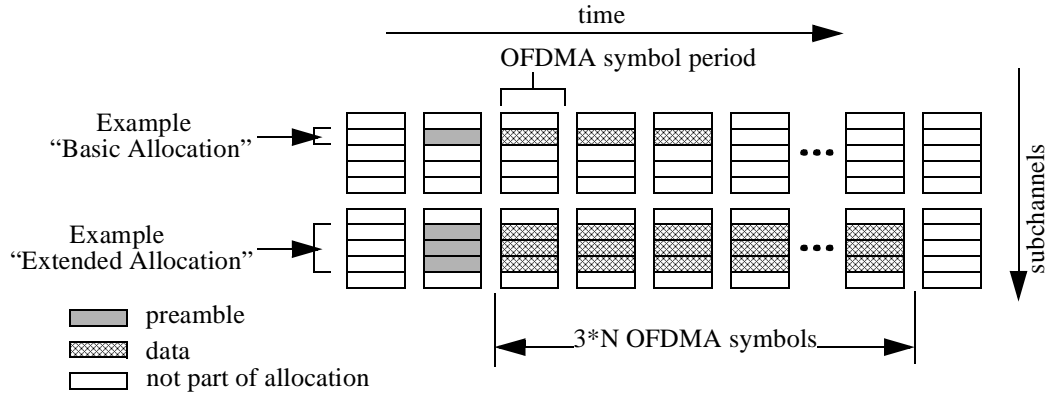"Extended Allocation"

subchannels

preamble

data

not part of allocation

3*N OFDMA symbols

Figure 231—OFDMA UL Bandwidth Allocations