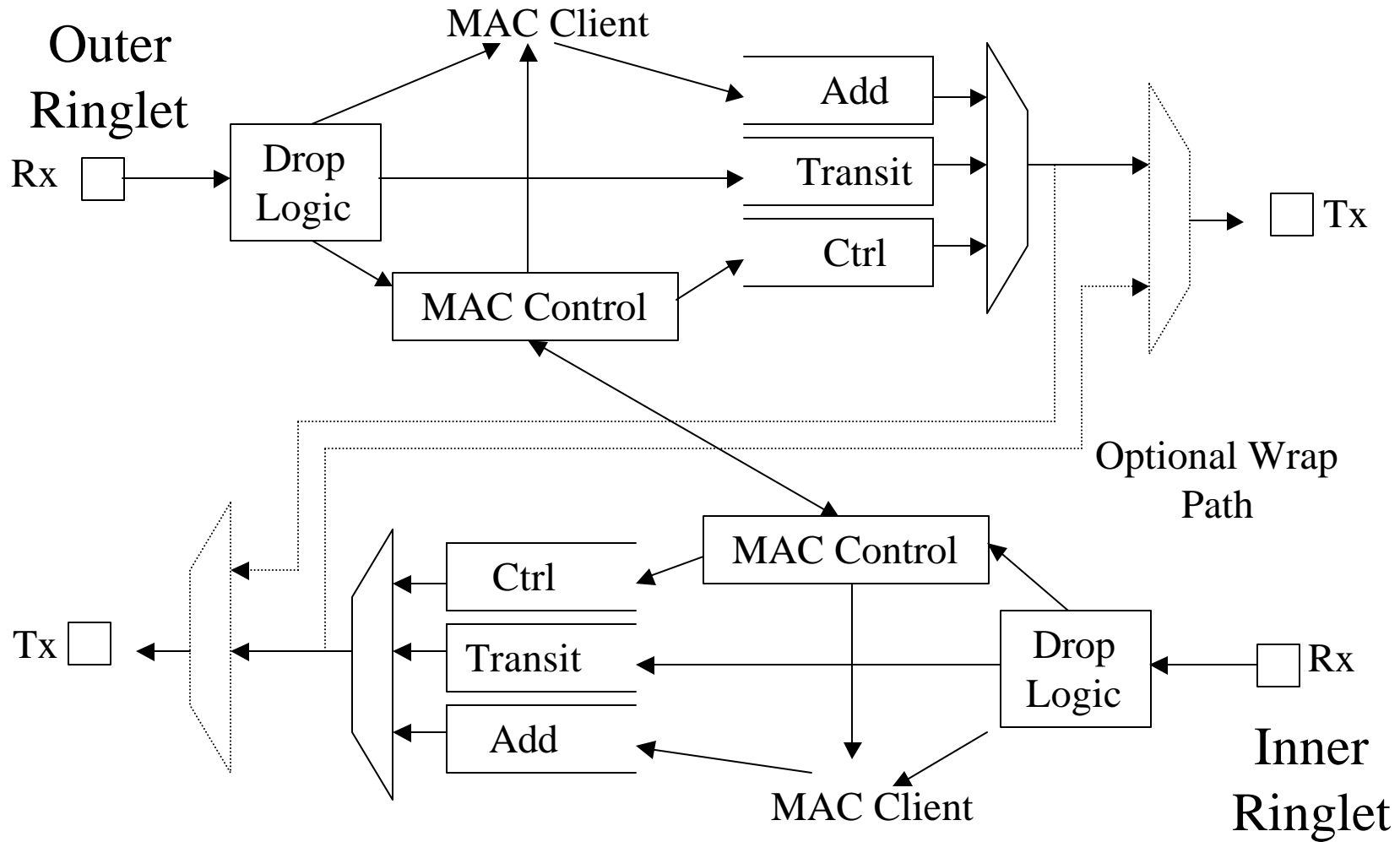


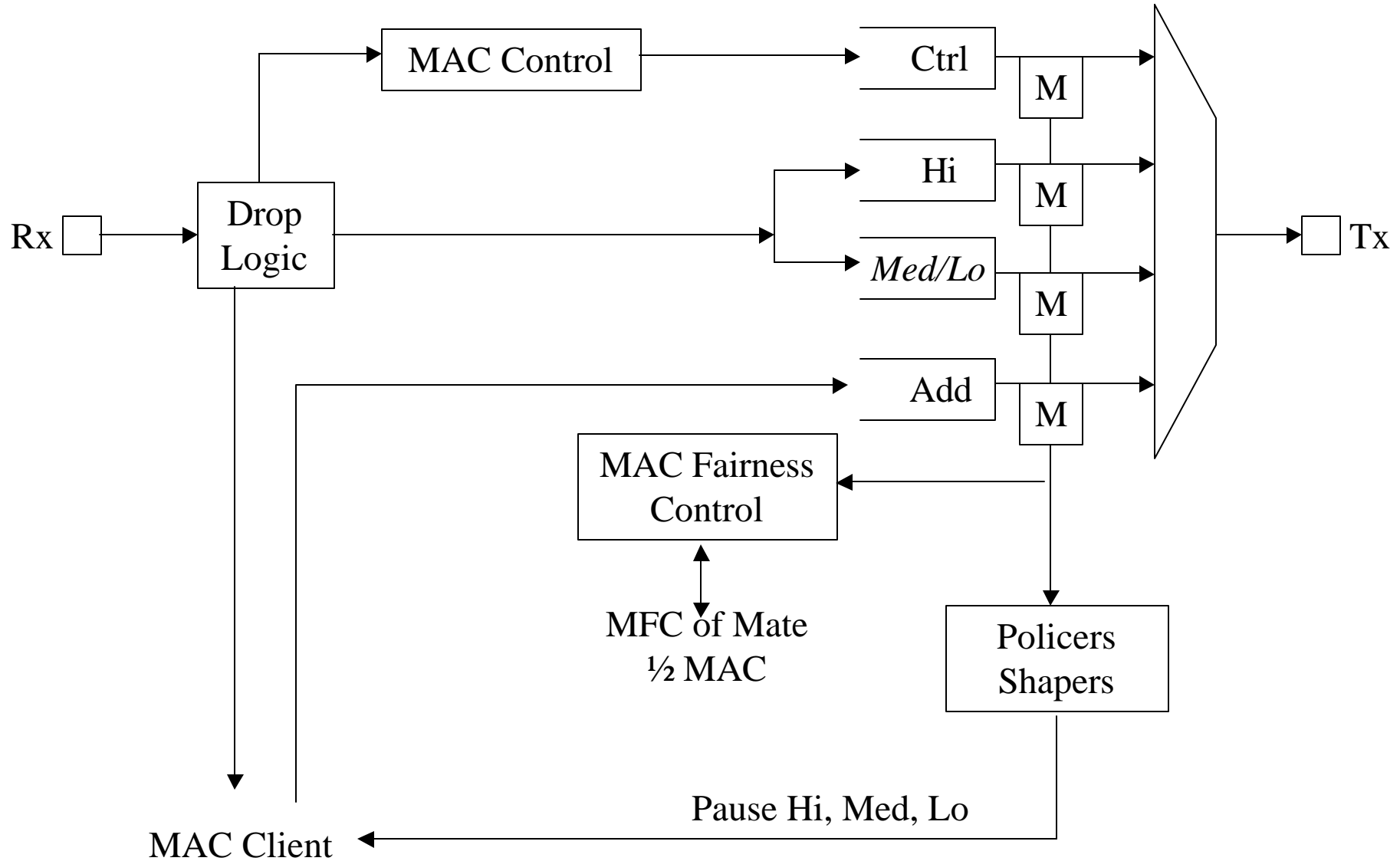
Darwin MAC Architecture

David Meyer

MAC Architectural Model

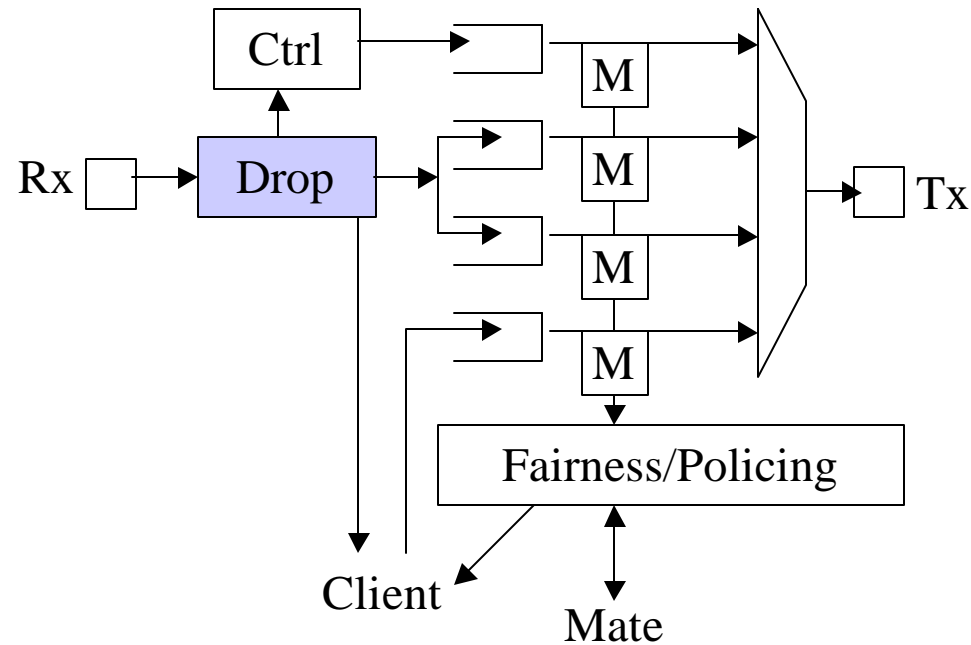


1/2 MAC Model



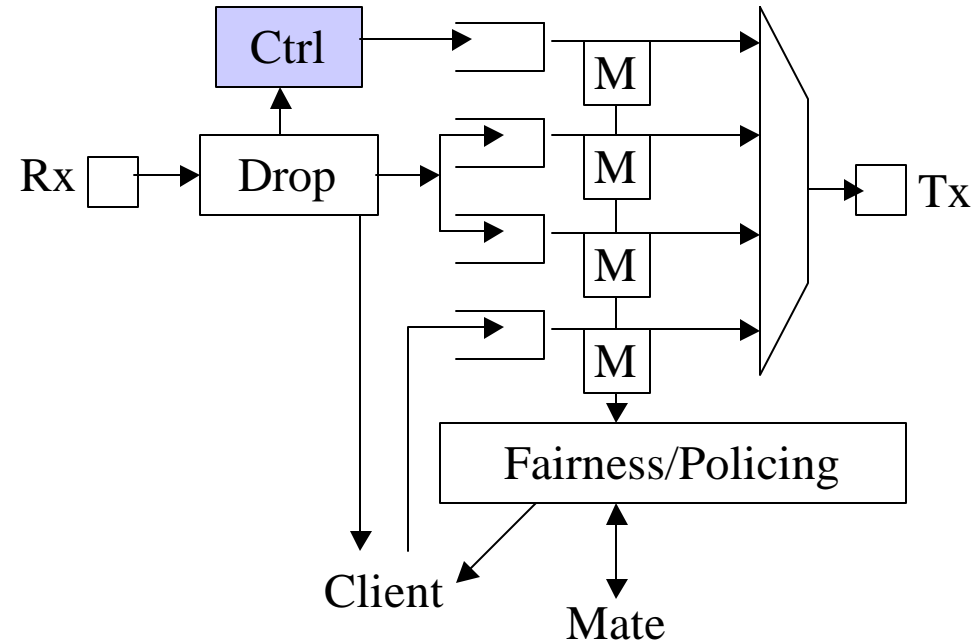
1/2 MAC Model

- HEC checks
- DA unicast strip
- SA multicast strip
- TTL expiration strip
- Forward / Multicast Copy
- Optional CRC checks



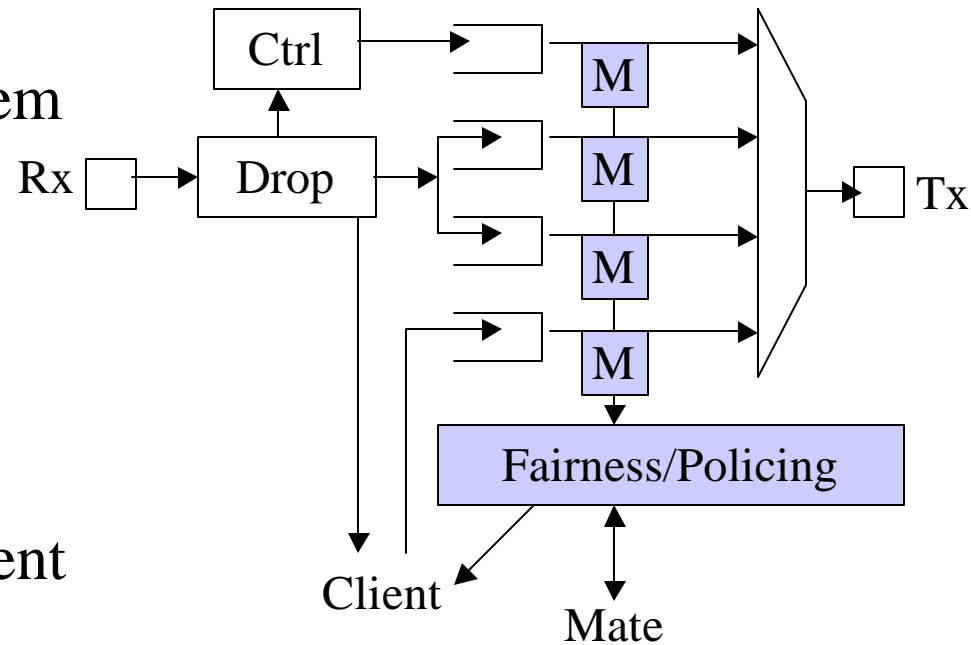
1/2 MAC Model

- Fairness
- Topology
- Protection
- OAM&P
- Broadcast and hop by hop



•Fairness Messaging

- Receive messages, process them and generate new messages using up to date register values of the mate
- Calculate new allow value
- Copy fairness messages to client for VDQ



•Fairness Logic

- Composed of congestion detection, conformance machine and fairness state machine