PHY CRG Summary

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Sitting at an elevation of 4,000 feet on the forested flanks of Mauna Loa, this area is generally cool and wet (recording some 130 inches of rainfall annually). In the village visitors will find small shops, general stores and a few restaurants, plus bed and breakfasts, the oldest hotel in the state, and campgrounds that provide easy access to Hawaii Volcanoes National Park

Summary

- Clause 7
  - Technical non-binding
  - Editorial
- Annex C
  - Technical non-binding
  - *carried over (last time punted to WG)*
- Annex D
  - Technical binding
  - Technical nonbinding
  - Missing description of SPI interfaces, CRC issues
- One comment raised against Annex B: punt to WG
  - Comment number #304
  - Recommend the WG to reject this one.

	Numbers	Comments
Clause 7		
Technical non-binding	2	2 punts
Editorial	4	3 accepted/accepted-modified; punt
Annex C		
Technical non-binding	1	mislabeled it's against Annex D
carried over (last time punted to WG)	1	request for presentation; punt
Annex D		
Technical binding	3	accept/accepted-modified (*)
Technical nonbinding	4	accept/accepted-modified (*)

* Addressed by #309 to accept anxD_SONET_RS_hp4.pdf

Punts and Issues

- Punts
  - Old punts with recommendation: #3643 ..chart
  - #304: clock sync ..chart
  - #107: POS PHY framing
  - #102: RS/PHY
  - #103: RS/PHY

- Demonstrating compliancy can be a problem: e.g. integrated MAC-PHY
- Nirmal to provide advice on CRC and bit order transmission

Carried Over #3646

- The editors note about "delay constraints" is correct. However, the constraints for the PHY are well specified in 802.3. What is needed in RPR are constraints for the RS layer, MAC layer, MAC Control layer, and any other layers which parse incoming packets and cause an action to occur.

- Editor view:
  - The PHY delay for all standard PHY's are well defined as pointed out by the commenter reference to 802.3 PHY
  - The delay implication shall be highlighted in an annex as how it affects the RPR services and protection in term of an equation as different ring configuration produces result.
  - MAC delay, PHY delay, medium delay

- CRG recommends closing this comment by rejecting it, since contribution has not been received for 4 months.

P802.17D1.1 Comment # 304

- The clock-sync, in terms of underrun/overrun is now handles in clause #6. This clause should not address the issue of synchronized timers, as needed for diagnostics (time stamps) as well as synchronous traffic support.

POS PHY Framing (#107)

- POS Framing method:
  - We missed: Page 105, draft 1.1

  a) Address and Control Field compression is always used;

  b) The Protocol Field is not used;

  c) The FCS is neither computed nor appended to the frame;

  d) The Asynchronous Control Character Map (ACCM) is not used.

RS / PHY comments

- Comment # 102
- Comment #103