

Annotations from P802.3ap-D3.2CmpD3.1PD2.pdf

Page 3

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 19:35:29
Widow

Page 4

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 19:36:24
What others in sponsor ballot? Can this note be removed completely now?

Page 5

Annotation 1; Label: Piers Dawe; Date: 18/12/2006 12:15:08
stan-dards.

Annotation 2; Label: Piers Dawe; Date: 18/12/2006 12:15:11
read-ing/

Annotation 3; Label: Piers Dawe; Date: 08/01/2007 19:38:34
p1 has "IEEE Standards Activities Department"

Annotation 4; Label: Piers Dawe; Date: 08/01/2007 19:36:59
Please don't split URLs across lines

Page 6

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 19:40:28
Missing space?

Annotation 2; Label: Piers Dawe; Date: 18/12/2006 12:16:55
70, 71,

Page 10

Annotation 1; Label: Piers Dawe; Date: 18/12/2006 12:19:36
5: 14 pm

Page 11

Annotation 1; Label: Piers Dawe; Date: 18/12/2006 12:21:55
Implementation Conformance Statement

Annotation 2; Label: Piers Dawe; Date: 18/12/2006 12:22:14
Characteristics

Annotation 3; Label: Piers Dawe; Date: 18/12/2006 12:22:50
Sublayer and Baseband Medium,

Annotation 4; Label: Piers Dawe; Date: 18/12/2006 12:22:56
Sublayer and Baseband Medium,

Annotation 5; Label: Piers Dawe; Date: 18/12/2006 12:23:12
Type

Annotation 6; Label: Piers Dawe; Date: 21/12/2006 00:30:47
Type

Annotation 7; Label: Piers Dawe; Date: 08/01/2007 19:42:53
Stray capitals

Page 12

Annotation 1; Label: Piers Dawe; Date: 18/12/2006 12:23:34
Sublayer and Baseband Medium, Type

Page 14

Annotation 1; Label: Piers Dawe; Date: 18/12/2006 12:25:48
Block

Annotation 2; Label: Piers Dawe; Date: 18/12/2006 12:25:49
Diagram.

Annotation 3; Label: Piers Dawe; Date: 18/12/2006 12:26:00
capability

Annotation 4; Label: Piers Dawe; Date: 18/12/2006 12:26:01
Enable

Annotation 5; Label: Piers Dawe; Date: 18/12/2006 12:26:15
Requirements.

Annotation 6; Label: Piers Dawe; Date: 18/12/2006 12:26:18
Error Monitoring

Annotation 7; Label: Piers Dawe; Date: 21/12/2006 00:35:21
Maybe more should be in lower case? Not sure.

Page 17

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 19:56:14
And do we need to mention the DME in 802.5? Depends if IEEE 100 still relevant

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:39:51
l/c (twice)

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 22:35:50
Differential

Page 18

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 19:59:39
aFECMode ?
(out of scope!)

Page 21

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 17:46:33
&

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:43:57
write out "and"?

Page 40

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 20:02:58

Remove unwanted new-lines, make table full width table & resize columns to fit contents

Page 41

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:02:48

Page Received

Page 45

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:05:15

Insert anchor sentence: The assignment of bits in the Backplane Ethernet status register is shown in Table 45-155.

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 18:11:18

When the AN

Page 56

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 18:54:58

Implementation Conformance Statement

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 18:55:01

Protocol Implementation Conformance Statement

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 18:22:51

Sublayer

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 18:22:52

Baseband

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 18:22:53

Medium,

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 18:22:54

Type

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 18:25:26

PHY (physical layer)

Annotation 6; Label: Piers Dawe; Date: 21/12/2006 01:45:46

Clause

Page 58

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 19:02:32

function

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 19:02:42

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 19:02:46

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 20:53:44

(max.)

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:19:26
Font size of NOTE

Page 69

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:41:32
PMD supports rather than Device implements?

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 18:38:05
Constraints

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 18:46:31
repetition

Page 70

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 18:48:42
The PCS associated with this

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 18:48:43
PMD

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 18:59:30
Function

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 18:59:30
Transmit-ter

Page 71

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 19:01:40
Return Loss

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 19:01:40
Impedance

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 19:01:55
&

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 19:03:14
Variable Mapping

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 19:04:28
100ppm

Page 72

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 20:34:22
100ppm

Annotation 2; Label: Piers Dawe; Date: 08/01/2007 20:08:03
Font size?

Page 73

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:47:44

Something like: A PMD is connected to the 100BASE-X PMA of Clause 36, and to the medium through Clause 73 Auto-Negotiation and the MDI. A PMD is optionally combined with the management functions that may be accessible through the management interface defined in Clause 45 or by other means.

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 20:35:09
(physical layer)

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 20:38:28
Sublayer and Baseband Medium, Type

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 20:40:21
While? whereas?
Are they compatible?

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 20:40:24
Clause

Annotation 6; Label: Piers Dawe; Date: 08/01/2007 20:08:43
What's too far to the right on this page?

Page 74

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:46:24
quantum

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 20:47:53
Font size

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 20:47:42
and MDIO status variables to PMD status variables as shown in

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 20:47:55
function

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 21:25:44
Blank line?

Annotation 6; Label: Piers Dawe; Date: 21/12/2006 02:00:47
The PCS associated with this PMD shall support

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:38:03
Global

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:20:13
Font size

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 20:50:37

(max.)

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 20:56:57
Data acquisition

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 20:59:06
Missing plate?

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 21:28:39
Test Fixture

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 22:25:52
maximum

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 22:25:54
minimum

Annotation 6; Label: Piers Dawe; Date: 08/01/2007 20:09:59
less than

Annotation 7; Label: Piers Dawe; Date: 08/01/2007 20:10:09

Annotation 8; Label: Piers Dawe; Date: 08/01/2007 20:10:17
greater than

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:15:16

Page 83

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:30:06
pÿ ©

Page 84

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:31:59
69B

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 01:53:36
missing full stop.

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:34:53
Interface

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 21:36:09
Constraints

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 01:56:12
Not the PMD's problem

Page 88

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:41:43

Column widths

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 21:42:49
Signal

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 21:43:06
Signal

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 01:56:42
Font size (several items in the various PICS tables)

Page 89

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:46:36
Transmit-ters

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 21:46:44
Variable Mapping

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 21:46:52
Per Table 71– 3 & Table 71– 4.

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:22:20
Return Loss

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 22:22:21
Impedance

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 22:23:06
100ppm

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 22:26:55
Maximum differen-tial peak- to- peak Minimum differen-tial peak- to- peak Maximum differen-tial peak- to- peak dif-ference

Page 92

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 21:48:17
is shall be

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 21:48:50
(physical layer)

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 22:10:04
Clause

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:10:39
The PCS associated with this PMD shall

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:12:30
Connector

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 22:16:14
Service

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 22:16:17
Interface

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 22:16:20
Service

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 22:16:21
Interface

Page 95

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:30:50
NOTEs font size

Page 97

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 02:04:53
UI

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 22:37:54
baud.

Page 98

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:38:42
it's

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 22:47:23
it's

Annotation 3; Label: Piers Dawe; Date: 04/01/2007 20:31:54
it's

Page 99

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 02:06:47
Unwanted space

Page 100

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:48:31
Resize columns

Page 101

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 02:07:35
value to be used?

Page 106

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:53:04
.

Page 108

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 20:14:53

Duty cycle distortion

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 22:57:28
Cycle Distortion

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:04:47
Interface

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:05:06
Constraints

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 23:05:16
Analog

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 23:43:48
Forward Error

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 23:43:49
Correction

Annotation 6; Label: Piers Dawe; Date: 20/12/2006 23:43:54
Error Correction

Annotation 7; Label: Piers Dawe; Date: 20/12/2006 23:44:11
Genera-tion

Annotation 8; Label: Piers Dawe; Date: 08/01/2007 20:15:24
Analog

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:05:30
The PCS associated with this

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:05:57
Variable Mapping

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:06:33
72.6.10. 2. 2

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 23:06:46
Encoding

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 23:07:32
Channel Encoding

Page 125

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:44:58
500ms

Page 126

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:45:07
as

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:45:31
of?

Page 131

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:50:34
shall

Page 133

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:06:57
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:06:58
logic

Page 136

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:07:05
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:07:06
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:07:17
logic

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:07:20
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:07:22
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:07:24
logic

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:07:27
logic

Annotation 5; Label: Piers Dawe; Date: 21/12/2006 00:07:29
logic

Annotation 6; Label: Piers Dawe; Date: 21/12/2006 00:07:32
logic

Annotation 7; Label: Piers Dawe; Date: 21/12/2006 00:07:32
logic

Annotation 8; Label: Piers Dawe; Date: 21/12/2006 00:07:37
logic

Annotation 9; Label: Piers Dawe; Date: 21/12/2006 00:07:39
logic

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:07:41
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:07:45
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:07:47
logic

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:07:49
logic

Annotation 5; Label: Piers Dawe; Date: 21/12/2006 00:07:51
logic

Page 139

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 01:00:04
Function

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:46:39
This will ensure there are no interoperability issues

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:07:53
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:07:56
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:07:58
logic

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:08:00
logic

Annotation 5; Label: Piers Dawe; Date: 21/12/2006 00:08:02
logic

Page 142

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:58:08
Page break

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:08:05
logic

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:08:13
logical

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:08:14
logical

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:08:22
logic

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:08:23
logic

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:08:30
logical

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:08:31
logical

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:08:32
logical

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:08:33
logical

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:08:38
logical

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:08:39
logical

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:08:45
logical

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:08:45
logical

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Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:08:48
logical

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:08:49
logical

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:08:52
logical

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:08:52
logical

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Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:47:46

25- 50

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:06:32
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:06:32
logic

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:06:33
logic

Annotation 5; Label: Piers Dawe; Date: 21/12/2006 00:06:33
logic

Annotation 6; Label: Piers Dawe; Date: 21/12/2006 00:06:38
logic

Annotation 7; Label: Piers Dawe; Date: 21/12/2006 00:06:39
logic

Annotation 8; Label: Piers Dawe; Date: 21/12/2006 00:09:03
logical

Annotation 9; Label: Piers Dawe; Date: 21/12/2006 00:09:04
logical

Page 151

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 02:33:24
All these should be in style e.g. "60 ms to 75 ms"

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:48:07
1.6- 3.0

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 23:48:08
3.4- 4.8

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 23:48:14
40– 50

Annotation 5; Label: Piers Dawe; Date: 20/12/2006 23:48:18
350– 375

Annotation 6; Label: Piers Dawe; Date: 20/12/2006 23:48:19
305– 330

Annotation 7; Label: Piers Dawe; Date: 20/12/2006 23:48:39
500- 510

Annotation 8; Label: Piers Dawe; Date: 21/12/2006 00:09:08
logic

Annotation 9; Label: Piers Dawe; Date: 21/12/2006 00:09:08
logic

Annotation 10; Label: Piers Dawe; Date: 21/12/2006 00:09:11
logic

Annotation 11; Label: Piers Dawe; Date: 21/12/2006 00:09:12
logic

Page 153

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:48:58
0to

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:09:24
logic

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:13:44
OK, no need for "logic"!

Page 159

Annotation 1; Label: Piers Dawe; Date: 08/01/2007 20:23:26
to

Page 161

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:56:28
Encoding

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:56:33
inFigure

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:09:33
logic

Page 165

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:09:35
logic

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:09:41
logic

Page 166

Annotation 1; Label: Piers Dawe; Date: 20/12/2006 23:59:03
25- 50ms 60- 75ms 4. 8 to 6.2ns 6. 6 to 8.0ns 4. 0- 4.8ns 1. 6- 2.4ns

Annotation 2; Label: Piers Dawe; Date: 20/12/2006 23:59:09
500- 510ms

Annotation 3; Label: Piers Dawe; Date: 20/12/2006 23:59:12
40- 50ms

Annotation 4; Label: Piers Dawe; Date: 20/12/2006 23:59:23
350- 375ns 305- 330ns

Page 168

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 02:36:13
two

Page 176

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:15:31
by?

Page 179

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:16:39
Empty line?

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:16:58
Footnote is not on the right page

Page 181

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:35:57
Error

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:36:00
Error Indication

Page 187

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:18:26
Man-agement

Page 188

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:35:37
Requirements

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 02:38:29
possible

Page 189

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:35:28
Error Monitoring

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:35:29
Error Monitoring

Page 205

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:25:25
shouldn't be any space, wrong kind of dash

Page 206

Annotation 1; Label: Piers Dawe; Date: 21/12/2006 00:26:24
.

Annotation 2; Label: Piers Dawe; Date: 21/12/2006 00:27:41
"32 bit" or "32-bit"

Annotation 3; Label: Piers Dawe; Date: 21/12/2006 00:27:15
65-bit ?

Annotation 4; Label: Piers Dawe; Date: 21/12/2006 00:27:15
(65b)