Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

Cl 40 SC 40.3.1.3.4 P 94 L 40 # 1
McIntosh, James Vitesse

Comment Type E Comment Status D
The underscores for the entire Sdn[2] equation implies that this is new.

SuggestedRemedy
Remove underscores from all but new part of the equation.
i.e., only "and (tx_mode != SEND_Z)" should be underlined.

Proposed Response Response Status O

Cl 40 SC 40.4.2.4 P 99 L 7 # 2
McIntosh, James Vitesse

Comment Type E Comment Status D
This very long paragraph is difficult to read. Please add a few breaks to make it easier.
I realize that this is in the "service to humanity" category, but this is new text.

SuggestedRemedy
Add a few new line breaks in the paragraph for readability.

Proposed Response Response Status O

Cl 40 SC 40.5.1.1 P 105 L 25 # 4
McIntosh, James Vitesse

Comment Type E Comment Status D
Register 7.60, Bit 7.60.2 uses same name as Register 7.20, Bit 7.20.2, "1000BASE-T EEE supported". This is confusing.

SuggestedRemedy
Change Register 7.60, Bit 7.60.2 name to "1000BASE-T EEE advertised" (or similar.

Proposed Response Response Status O

Cl 40 SC 40.5.1.1 P 105 L 28 # 5
McIntosh, James Vitesse

Comment Type E Comment Status D
Register 7.61, Bit 7.61.2 uses same name as Register 7.20, Bit 7.20.2, "1000BASE-T EEE supported". This is confusing. Additionally, this is the status of the link partner.

SuggestedRemedy
Change Register 7.61, Bit 7.61.2 name to "LP 1000BASE-T EEE advertised" (or similar.

Proposed Response Response Status O

Cl 45 SC 45.2.7.15a P 118 L 33 # 6
McIntosh, James Vitesse

Comment Type E Comment Status D
1000BASE-T wake time is now fixed. We no longer need bits 7.62.9:5 in Table 45-146.

SuggestedRemedy
Delete the corresponding text, currently 45.2.7.15a.1.

Proposed Response Response Status O
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
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<tr>
<td>Cl 40 SC 40.1.3</td>
<td>ER</td>
<td>D</td>
<td>McIntosh, James Vitesse</td>
</tr>
<tr>
<td>Cl 40 SC 40.4.5.1</td>
<td>P 100</td>
<td>L 33</td>
<td># 7</td>
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<td>D</td>
<td>ER</td>
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<td>ER</td>
<td>D</td>
<td>McIntosh, James Vitesse</td>
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<td>Cl 40 SC 40.5.1.1</td>
<td>P 105</td>
<td>L 24</td>
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<td>ER</td>
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<td>Cl 40 SC 40.3</td>
<td>P 93</td>
<td>L 21</td>
<td># 11</td>
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<td>McIntosh, James Vitesse</td>
<td>D</td>
<td>ER</td>
<td></td>
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<tr>
<td>Cl 40 SC 40.4.6.1</td>
<td>P 103</td>
<td>L 9</td>
<td># 12</td>
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<tr>
<td>McIntosh, James Vitesse</td>
<td>D</td>
<td>ER</td>
<td></td>
</tr>
</tbody>
</table>

**Proposed Response**

**Suggested Remedy**

1. I believe there are two errors here. First, there are many new clause "46" items that exist in clause 40 that I believe should be 40 instead.
2. Second, I believe the reference here should be pointing to the "Signal_detect" subclause rather than the "Transmitter operation during WAKE" subclause.

**Proposed Remedy**

- Change "46.6.1.2.7" to "40.6.1.3.5".

**Comment Status**

- D/dispatched

**Response Status**

- O/open
The text in the draft calls for a 0.1ppm/second limit on the short term frequency variation of the transmitter clock in the low power transmit mode. The commenter has solicited input from several industry experts on this specification and expects to have some feedback on this requirement. Based on the feedback received, the commenter may provide a suggested remedy at or prior to the meeting.

Suggested Remedy

See presentation

Proposed Response Response Status O

---

1) Screened systems should not be excluded from the objectives (delete "UTP")
2) 150 Ohm is not a recognized media in ISO/IEC 11801:2002 and is not commonly found as a legacy cabling type (delete "150 ohm STP")
3) Add reference to TIA Standards
4) ISO refers to cabling in terms of "class" not "category" of performance (copy text from 802.3at draft)
4) Allow cabling grades higher than category 5 (copy text from 802.3at draft)

Suggested Remedy

Re-write bullet point d) as:

"Support cable plants using Class D or better or optical fiber cabling as specified in ISO/IEC 11801:1995. When Class D cabling is used, the cabling system components (cables, cords, and connectors) used to provide the link segment shall consist of Category 5e components as specified in ANSI/TIA/EIA-568-C.2 and ISO/IEC 11801:2002.

NOTE—ANSI/TIA/EIA-568-C.2 provides a specification (category 5e) for cabling that meets the minimum requirements for 100BASE-X operation."

Proposed Response Response Status O

---

The values below the lpi_quiet_time header are for refresh.
The values below the lpi_refresh_time header are for quiet.

Suggested Remedy

Reverse the column headers.

Proposed Response Response Status O
<table>
<thead>
<tr>
<th>Cl</th>
<th>SC</th>
<th>P</th>
<th>L</th>
<th>#</th>
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<th>Comment Status</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>55</td>
<td>55.3.5.3</td>
<td>162</td>
<td>51</td>
<td>18</td>
<td>ER</td>
<td>D</td>
<td>Fix sentence to address Master and Slave.</td>
</tr>
<tr>
<td>45</td>
<td>45.2.3.9b</td>
<td>115</td>
<td>39</td>
<td>19</td>
<td>T</td>
<td>D</td>
<td>Change minimum value for Ts for 10GBASE-T to 1.6 usec.</td>
</tr>
<tr>
<td>55</td>
<td>55.3.2.2.21</td>
<td>159</td>
<td>39</td>
<td>20</td>
<td>T</td>
<td>D</td>
<td>The Table defines Minimum Tw_phy time as 4.8 usec for 10GBASE-T. The minimum Tw_phy time does not include Sleep and should be defined as follows: Tw_phy = (Alert time + min Wake Time) = (4 + 1) = 1.6 usec.</td>
</tr>
</tbody>
</table>

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID
The table defines the Ts max as 2.88 usec. Sleep is defined as 9 full frames + 1 partial frame. 1 frame consists of 50 blocks, so a partial frame can consist of between 1 block and 49 blocks, which can be rounded up to 1 frame. Therefore, the max number of Sleep frames is 10.

Ts max = 10 frames * 320 nsec = 3.20 usec.

Suggested Remedy
Change Ts max for 10GBASE-T from 2.88 usec to 3.20 usec.

The table defines bit 7.62.1 as reduced energy refresh or normal energy refresh, which is not supported for 10GBase-T. This does not map into 10GBase-T autoneg capabilities, which are:

Refresh Times of 4,8,16, or 32 frames
Wake Times of 1,3,5,7,9 frames.

In the editors note, it states that this register is a placeholder pending firm definitions.

Suggested Remedy
Since each technology is allocated one bit, and the 10GBASE-T needs 2-bits for refresh and 3-bit for Wake, multiple registers will be needed to define EEE auto-negotiation controls. These registers need to be defined, and the placeholder register need to be removed.

Suggested Remedy
Define what characters may be transmitted on lanes 1-3 when lane 0 is low power idle.

Suggested Remedy
Define what characters are valid on lanes 1-3 while LPI character is on lane 0.
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Cl 46 SC 46.3.2.4a P 126  L 11   # 26
Rick, Tidstrom  Broadcom

Comment Type  TR  Comment Status  D

The sentence does not specify the conditions for RX_CLK to be halted by the PHY.

"The PHY may halt RX_CLK at any during the low power idle state as shown in Figure 46-8a if and only if the clock stoppable bit is asserted".

Suggested Remedy
Define requirements to halt RX_CLK.

For the TX_CLK, it may be halted at any time more than 128 clock cycles after the start of low power idle.

Proposed Response  Response Status  O

Cl 55 SC 55.1.3.3 P 153  L 34   # 28
Rick, Tidstrom  Broadcom

Comment Type  TR  Comment Status  D

"The quiet-refresh cycle continues until the PCS function detects IDLE codewords on the XGMII interface."

This statement is vague as to what is required to exit low power idle.

Is a single IDLE character sufficient, or is two consecutive transfers of TXD[31:0] that map into a single 64B/65B block, with all lanes containing IDLE characters required to exit low power idle?

Suggested Remedy
Change to a more specific sentence to define the exit criteria.

Proposed Response  Response Status  O

Cl 55 SC 55.1.2.2.21 P 159  L 4   # 29
Rick, Tidstrom  Broadcom

Comment Type  TR  Comment Status  D

The sub-clause states that "In the transmit direction the transition to low power transmit mode begins when the PCS transmit function detects a 64B/65B block composed of LP_IDLE codewords".

The PCS transmit function does not detect 64B/65B blocks, it generates them.

Suggested Remedy
Change sentence like shown below:

In the transmit direction the transition to low power transmit mode begins when the PCS transmit function detects an LPI control character in Lane 0 of two consecutive transfers of TXD[31:0] that will be mapped into a single 64B/65B block.

Proposed Response  Response Status  O

Comment Type  TR  Comment Status  D

The word codewords is not currently used in clause 55. The sentence below is also vague as to what is required for the PCS to enter low power idle.

"The PCS initiates a transition to the lower power transmit mode when it detects LP_IDLE codewords on the XGMII interface."

Suggested Remedy
Change sentence to:

The PCS initiates a transition to the lower power transmit mode when it detects LP_IDLE codewords on the XGMII interface.

Proposed Response  Response Status  O
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

Cl SC P L #
55 55.3.2.2.21 159 16 # 30
Rick, Tidstrom Broadcom

Comment Type TR Comment Status D
The sentence states:

After a complete 64B/65B block of LPI codewords is detected at the XGMII,

The PCS transmit function does not detect 64B/65B blocks, it generates them.

Suggested Remedy
Change sentence to:

After a complete 64B/65B block of LPI characters is generated by the PCS transmit function,

Proposed Response Response Status O

Cl SC P L #
55 55.3.2.2.21 159 22 # 31
Rick, Tidstrom Broadcom

Comment Type TR Comment Status D
The following sentence is not correct:

The quiet-refresh is repeated until IDLE or LF codewords are detected at the XGMII.

The current standard does not support the MAC sending a LF to wake-up the PHY. Only IDLE characters should be used to wake-up the PHY. If the MAC wants to send a LF, it needs to send IDLE characters to wake-up the PHY. Then after the PHY is awake, it can send the LF.

Suggested Remedy
Change sentence to:

The quiet-refresh is repeated until IDLE codewords are detected at the XGMII.

Proposed Response Response Status O

Cl SC P L #
55 55.3.2.2.21 159 32 # 33
Rick, Tidstrom Broadcom

Comment Type TR Comment Status D
The following statement is vague with regard to error:

"or lpi_wake_time repeated local fault characters if an 'error' has been detected."

Suggested Remedy
"Error" needs to be defined as any character that is received other than an IDLE or LP_IDLE character while the PHY is in low power mode.

Also, local fault characters should be changed to Local Fault blocks.

Proposed Response Response Status O
Cl 55  SC  55.4.2.2.1  P 171  L 27  # 34
Rick, Tidstrom  Broadcom

Comment Type TR  Comment Status D

The following sentence is not correct:

All other pairs shall transmit quiet or refresh as described in subclause 55.3.5.

Refresh is not transmitted while Alert is being transmitted.

Suggested Remedy

Change sentence to:

"All other pairs shall transmit quiet as described in subclause 55.3.5."

Proposed Response  Response Status O

Cl 55  SC  55.6.1  P 175  L 2  # 35
Rick, Tidstrom  Broadcom

Comment Type TR  Comment Status D

Table 55-10

Defines number of valid wake frames as 1-9.

Suggested Remedy

Change to 1,3,5,7,9. Since the number of wake values has been reduced from 9 to 5, the extended bit-field can be changed from U26:U23 to U25:U23 or U26:24.

Proposed Response  Response Status O

Cl 55  SC  55.3.5.4  P 169  L 36  # 36
Rick, Tidstrom  Broadcom

Comment Type TR  Comment Status D

For the SEND_ERROR state, the value for tx_coded is shown as

tx_coded <= ERROR.

The SEND_ERROR state is entered when the PCS transmit function receives a character other than IDLE of LP_ILDE while in low power mode. The /E/ character is not the best character to send to indicate that the MAC has sent an invalid character.

Suggested Remedy

The value should be changed to Local Fault.

tx_coded <= /LF/

Proposed Response  Response Status O

Cl 55  SC  55.3.5.4  P 166  L 31  # 37
Rick, Tidstrom  Broadcom

Comment Type TR  Comment Status D

This comment is relative to comment 29 about the SEND_ERROR state of the EEE transmit state diagram.

Since it is recommended that the SEND_ERROR state transmit a Local Fault instead of an /ERROR/ character, the TX_WE state should not transition to the TX_E state.

Suggested Remedy

Change transition from TX_WE to TX_C.

Proposed Response  Response Status O
This comment is relative to the previous two comments about transmitting a Local Fault instead of an /ERROR/ character when exiting with Error from low power mode.

During Wake from LPI, the RX_W should only get IDLE characters or /LF/ characters.

Also if the lpi_rx_wake_timer_done = true happens without seeing an /I/ or /LF/ means that all of the Wake Frames were bad. Instead of going to RX_C the FSM should transition to RX_E.

**Suggested Remedy**

Change transition condition from RX_W to RX_C to be:

\[ R_{TYPE}(rx\_coded) = I + R_{TYPE}(rx\_coded) = LF \]

Change transition condition from RX_W to RX_E to be

\[ lpi\_rx\_wake\_timer\_done = true \]

**Proposed Response**

**Response Status** O

---

**Comment ID # 39**

**Cl 78 SC 78.4.2 P 220 L 9**

**Comment Status** D

**Comment Type** TR

**Rick, Tidstrom Broadcom**

**Figure 78-3**

LLDP and EEE TLV are high level communication protocols between the MAC, and can be used to adjust system parameters. MACs do not care about refresh times. Refresh times should be handled PHY to PHY using auto-negotiation.

**Suggested Remedy**

Remove Refresh Duty Cycle from TLV information string.

**Proposed Response**

**Response Status** O

---

**Comment ID # 41**

**Cl 45 SC 45.2.7.15a P 118 L 23**

**Comment Status** D

**Comment Type** E

**Rick, Tidstrom Broadcom**

**When discussing how the EEE mode control register will map into extended next pages, it references register bits 7.60.10 to 7.60.0.**

**Suggested Remedy**

The register bits referenced should be 7.62.10 to 7.62.0.

**Proposed Response**

**Response Status** O

---

**Comment ID # 42**

**Cl 55 SC 55.1.3.3 P 153 L 29**

**Comment Status** D

**Comment Type** E

**Rick, Tidstrom Broadcom**

**"The sleep signal is composed of repeated LP_IDLE codewords".**

**Suggested Remedy**

The word "codeword" is currently not used in clause-55.

**Suggested Remedy**

Replace codewords with 64B/65B blocks.

**Proposed Response**

**Response Status** O
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>43</td>
<td>E</td>
<td>D</td>
<td>Replace codewords with characters.</td>
</tr>
<tr>
<td>44</td>
<td>E</td>
<td>D</td>
<td>Replace codewords with 64B/65B blocks.</td>
</tr>
<tr>
<td>45</td>
<td>E</td>
<td>D</td>
<td>Change from: LP_IDLE XGMII codewords. to: LP_IDLE 64B/65B blocks.</td>
</tr>
<tr>
<td>47</td>
<td>E</td>
<td>D</td>
<td>Change from: codewords encoded using the 65B-LDPC coding technique. to: 64B/65B blocks.</td>
</tr>
</tbody>
</table>
Comments on IEEE P802.

Cl 55 SC 55.3.2.3 P 160 L 12 # 48
Rick, Tidstrom Broadcom

Comment Type E Comment Status D
Line 13
Line 15
Line 22
Line 23
Line 24
Line 35

The word codeword is not currently used in clause 55.

Suggested Remedy
Replace codewords with blocks.

Proposed Response Response Status O

Cl 55 SC 55.3.5 P 161 L 33 # 49
Rick, Tidstrom Broadcom

Comment Type E Comment Status D
The word "mode" is misspelled as "modee".

Suggested Remedy
Change spelling to mode

Proposed Response Response Status O

Cl 78 SC 78.1.3 P 216 L 3 # 50
Rick, Tidstrom Broadcom

Comment Type E Comment Status D
LP_Quiet_st state is a typo

Suggested Remedy
Change to LP_Quiet state

Proposed Response Response Status O

Cl 45 SC 45.2.3.2 P 113 L 16 # 51
Rick, Tidstrom Broadcom

Comment Type ER Comment Status D
Table 45-84

Reserved bits are referenced as 1.1.15:12.

Suggested Remedy
They should be referenced as 3.1.15:12.

Proposed Response Response Status O

Cl 55 SC 55.3.2.3 P 160 L 46 # 52
Rick, Tidstrom Broadcom

Comment Type ER Comment Status D
The values for quiet and refresh are reversed.

Suggested Remedy
From: All EEE-capable PHY's shall support the lpi_quiet_time=32, lpi_refresh_time=96.
To: All EEE-capable PHY's shall support the lpi_quiet_time=96, lpi_refresh_time=32.

Proposed Response Response Status O

Cl 30 SC 65 P 65 L 1 # 53
Diab, Wael Broadcom

Comment Type TR Comment Status D
The MIB extention to support the LLDP framework defined will need to go into C30. This needs to be as an update to the changes that 802.3bc does.

Suggested Remedy
Please an editor's note to that effect so it can be a placeholder

Proposed Response Response Status O

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID
The current scheme described for parameter changes using LLDP is not inline with the LLDP framework defined by 802.1ABC.

Suggested Remedy
The issues along with a detailed remedy that can serve as a starting point for this section is described in diab_01_0109.pdf.

Comment Status D
Response Status O

Arrow head sizes are not consistent in the state machine shown in the following pages:

Suggested Remedy
Change all "true" to "TRUE" and all "false" to "FALSE"

Comment Status D
Response Status O

"Reconcilliation" Spelling

Suggested Remedy
Reconciliation
Cl. 36 SC 36.2.5.1.3 P 75 L 25 # 58
Pillai, Velu Broadcom

Comment Type TR  Comment Status D
Closing brackets are not matching.
* SUDI([/D21.5/] * [/D6.5/])
* SUDI([/D2.2/] * [/D26.4/] * [/D6.5/])

Suggested Remedy
It can either be
* SUDI([/D21.5/] * [/D2.2/] * [/D26.4/] * [/D6.5/])
* SUDI([/D21.5/] * [/D2.2/] * [/D26.4/] * [/D6.5/])

Proposed Response  Response Status O

Cl. 36 SC 36.2.5.1.5 P 75 L 51 # 51
Pillai, Velu Broadcom

Comment Type TR  Comment Status D
rx_deact_timer
This timer is started when the PMD's receiver enters the RX_SLEEP state.

Suggested Remedy
rx_deact_timer
This timer is started when the PMD's receiver enters the RX_DEACT state.

Proposed Response  Response Status O

Cl. 36 SC 36.2.5.1.3 P 75 L 36 # 60
Pillai, Velu Broadcom

Comment Type ER  Comment Status D
On line 36 and 39 change
a Active state

Suggested Remedy
an Active state

Proposed Response  Response Status O

Cl. 45 SC 45.2.3.1.3a P 112 L 47 # 63
Pillai, Velu Broadcom

Comment Type TR  Comment Status D
Clock stoppable is applicable to transmit clock for GMII and XGMII. Hence that needs to be mentioned in the description.

Suggested Remedy

Proposed Response  Response Status O
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

Cl 45 SC table 45-84 P 113 L 16 # 64
Pillai, Velu Broadcom

**Comment Type** T **Comment Status** D
Under Bits: 1.1.15:12 It should be as suggested.

**Suggested Remedy**
3.1.15:12

**Proposed Response** Response Status O

---

Cl 45 SC Table 45-84 P 113 L 18 # 65
Pillai, Velu Broadcom

**Comment Type** T **Comment Status** D
Table 45-84 is a PCS status register. Hence the description for bits 11 to 8 should say "PCS", instead of "PMA/PMD". If this comment is accepted, then the bit description on 45.2.3.2.1a - 1d should also change all the reference to "PMA/PMD" to "PCS".

**Suggested Remedy**

**Proposed Response** Response Status O

---

Cl 46 SC 46.3.2.4a P 126 L 11 # 66
Pillai, Velu Broadcom

**Comment Type** TR **Comment Status** D
The diagram or the description does not mention RX_CLK stopping after 128 clock cycles.

**Suggested Remedy**

The MAC device may halt RX_CLK at any time more than 128 clock cycles after the start of the low power.

Also show it in Fig 46-8a

**Proposed Response** Response Status O

---

**Comment Type** ER **Comment Status** D
The large inserted paragraph is difficult to read. It should be edited to clarify the content by breaking into smaller paragraphs.

**Suggested Remedy**

Replace the large paragraph with the following edited text:

When the PHY supports Energy Efficient Ethernet, PHY Control will transition to a low power idle mode in response to concurrent requests for low power operation from the local PHY (loc_lpi_req = TRUE) and remote PHY (rem_lpi_req = TRUE).

Upon activation of the low power mode, the PHY Control asserts tx_mode = SEND_I for period of time defined by lpi_update_timer which allows the remote PHY to prepare for the transition to the WAIT QUIET state.

When lpi_update_timer expires, PHY Control asserts tx_mode = SEND_Z and transmission ceases.

During the WAIT QUIET and QUIET states, the PHY may deactivate transmit and receive functions in order to conserve energy. However, in the WAIT QUIET state, the PHY shall be capable of correctly decoding rem_lpi_req and rem_lpi_mode.

The PHY will remain in the QUIET state no longer than the time implied by lpi_quiet_timer. When lpi_quiet_timer expires, the PHY initiates a wake sequence.

The wake sequence begins with a transition to the WAKE state where the PHY will transmit (tx_mode = SEND_I) for period lpi_waketx_timer and simultaneously start a parallel timer, lpi_wakemz_timer. Since it is likely that transmit circuits were deactivated while in the QUIET state, this transmission is not expected to be compliant 1000BASE-T signaling, but rather of sufficient quality and duration to be detected by the remote PHY receiver and initiate the wake sequence in the remote PHY. Upon expiration of lpi_waketx_timer, the PHY will enter the WAKE SILENT state and cease transmission (tx_mode = SEND_Z). The PHY will remain in the WAKE SILENT state until lpi_wakemz_timer has expired, at which point it is assumed transmitter circuits have stabilized and compliant 1000BASE-T signaling can be transmitted.

At this point the MASTER transitions to the WAKE TRAINING state and transmits to the SLAVE PHY. The remaining wake sequence is essentially an accelerated training mode sequence leading to entry into the UPDATE state. Once scrambler synchronization is achieved, the incoming value of rem_lpi_req can be determined.

If low power operation is no longer requested by either the local or remote PHY, then both PHYS return to the SEND IDLE OR DATA state and the normal mode of operation (tx_mode = SEND_N). If both PHYS continue to request low power operation, then both PHYS remain in the UPDATE state and continue to transmit for time defined by lpi_update_timer. This time is intended to allow the remote PHY to refresh its receiver state.
The three paragraphs titled "LPI Capability" are confusing and could be edited to be easier for implementors to understand. Suggest that the information be reorganized and broken into shorter paragraphs.

**Suggested Remedy**

Replace the three paragraphs with the following edited version:

The optional LPI 10GBASE-T capability allows compliant PHYs to transition to LPI mode of operation when link utilization is low. The EEE transmit state diagram, Figure 55-19, shows how the link enters and leaves LPI mode.

When PCS_Reset is asserted the state diagram enters the TX_NORMAL state. The PCS initiates a transition to the lower power transmit mode when it detects LP_IDLE codewords on the XGMII interface.

After a complete 64B/65B block of LPI codewords is detected at the XGMII, the PHY transmits the Sleep signal to indicate to the link partner that it is transitioning to the lower power transmit mode.

The Sleep signal comprises 9 full LDPC frames composed of LP_IDLE XGMII codewords encoded using the 65B-LDPC coding technique. The 9 full frames may be preceded by a partial frame of LP_IDLE XGMII codewords.

The PCS turns off the transmit signal through the PMA_UNITDATA.request primitive using the lpi_tx_mode variable after the PMA asserts SEND_N.

After the Sleep signal is transmitted LP_IDLE symbols shall be input to the PCS scrambler continuously until the PCS Transmit Function exits the lower power transmit mode.

When the lpi_tx_mode variable takes the value QUIET the PCS shall pass zeros to the PMA through the PMA_UNITDATA.request primitive.

Following the transmission of the Sleep signal, quiet/refresh signaling begins, as described in Clause 55.3.5.

When the lpi_tx_mode variable takes the value REFRESH_A the PCS shall pass the PMA training signal to the PMA on pair A, to allow both the local and remote PHY to refresh adaptive filters and timing loops. The PCS passes zeros to all other pairs while lpi_tx_mode has the value REFRESH_A, REFRESH_B, REFRESH_C and REFRESH_D operate in a similar manner for the other pairs.

The quiet-refresh cycle is repeated until IDLE or LF codewords are detected at the XGMII.

 LF codewords indicate to the PCS transmit function that the MAC is requesting a transition
back to the full data mode. /LF/ codewords indicate to the PCS transmit function that an error condition has occurred. Either of these events cause the PCS transmit function to set the PMA_UNITDATA.request message to the value ALERT.

The alert signal is not synchronized with respect to the refresh/quiet cycle but shall be synchronized so that the alert signal from the PMA begins on a LDPC frame boundary.

After the Alert message the PCS completes the transition from low power idle mode to normal mode by sending a Wake signal which is composed of lpi_wake_time repeated /I/ codewords encoded using the 65B-LDPC coding technique if an error condition is not detected, or lpi_wake_time repeated local fault characters if an error has been detected.

The PCS initiates return to normal mode by sending IDLE code words on the XGMII interface. IDLE codewords can be presented at the XGMII at any time after the time period specified by lpi_wake_time for the selected lpi_tx_wake_time parameter.

The lpi_wake_time is a parameter that is resolved during Auto-Negotiation as described in 55.6.3. lpi_wake_time is an integer multiple of LDPC frames, chosen from the values shown in Table 55-2 below. The lpi_wake_time value shown in the table is the maximum PHY wake time value equivalent to Tw_phy as defined by Clause 78).

Proposed Response:

Response Status: O

Dietz, Bryan Alcatel-Lucent

Comment Type: ER  Comment Status: D

The conceptual description can be edited to clarify it for new readers.

Suggested Remedy:

Replace text in section 78.1.3 with the following. Retain figures in the same position as in current draft.

Low Power Idle mode is an optional mode that allows power saving by switching off part of the communication device functionality when no data needs to be transmitted or/and received. The decision on whether system should enter or exit Low Power Idle mode is done on the MAC level and communicated to PHY level in order to allow power saving. Figure 78-1 shows the decision flow and agents involved.

In the transmit direction, entrance to Low Power Idle mode of operation is triggered by the reception of LP_IDLE codewords on the MAC interface. The specific interface depends on the communication standard being used, therefore this interface is shown as xxMII in the diagram.

Following reception of LP_IDLE codeword, PHY transmits a special LP_Sleep signal to communicate to the link partner that the local system is entering Low Power Idle mode.

In 100BASE-T and 10GBASE-T EEE modes, the transmit function of the local PHY enters a quiet mode after the LP_Sleep signal transmission.

In 1000BASE-T Low Power Idle mode, the transmit function of the local PHY enters a quiet mode after the local PHY transmits LP_Sleep and receives LP_Sleep from the remote PHY.

The transmit function of the local PHY is enabled Periodically to transmit LP_Refresh signals that are used by the link partner to update adaptive filters and timing circuits in order to maintain link integrity.

This quiet-refresh cycle continues until local MAC signals to the PHY that Low Power Idle mode should end by sending IDLE codewords. The transmit function in the PHY communicates this to the link partner by sending a special LP_Wake signal for a pre-defined period of time. Then the PHY enters Active_st and resumes normal operation mode.

In the receive direction, entering Low Power Idle mode is triggered by the reception of LP_Sleep signal from the link partner. This signals that the link partner is about to enter Low Power Idle mode. After sending the LP_Sleep signal, the link partner ceases transmission and enters LP_Quiet_st state. While Link partner is in LP_Quiet state, the local receiver can disable some functionality to reduce power consumption.

The link partner periodically transmits LP_Refresh signals that are used by the local PHY to update adaptive coefficients and timing circuits. This quiet-refresh cycle continues until
the link partner initiates transition back to full data mode by transmitting LP_Wake signal for a pre-determined period of time. This allows the local receiver to prepare for the normal operation. After a system specified recovery time the link supports nominal operational data rate.

Figure 78-2 illustrates general principles of the EEE-compliant transmitter operation.

If both link partner enter and exit Low Power Idle mode simultaneously this mode of operation is called symmetric. If each link partner can entrance and exit Low Power Idle mode independently this mode of operation is called asymmetric.

No data frames are lost or corrupted during the transition to or from the Low Power Idle mode.

Proposed Response  
Response Status  O

---

**Comment ID #70**

Dietz, Bryan  
Alcatel-Lucent  

Comment Type  E  
Comment Status  D  

Subclause numbers do not appear to match 802.3-2005. Should this be numbered 22.2.1.3?

SuggestedRemedy  
Update numbering if appropriate.

Proposed Response  
Response Status  O

---

**Comment ID #72**

Dietz, Bryan  
Alcatel-Lucent  

Comment Type  ER  
Comment Status  D  

The meaning of the second paragraph is unclear, perhaps due to an editing error. The phrase "any transitions of the CRS signal" occurs in two sentences without any clear reason for the second sentence.

SuggestedRemedy  
Revert to the 802.3-2005 wording or else clarify what is meant by this change. The 802.3-2005 wording was:

While the RX_DV signal is de-asserted, any transition of the CRS signal from de-asserted to asserted must cause a transition of CARRIER_STATUS from the CARRIER_OFF to the CARRIER_ON value, and any transition of the CRS signal from asserted to de-asserted must cause a transition of CARRIER_STATUS from the CARRIER_ON to the CARRIER_OFF value. At any time after CRS and RX_DV are both asserted, de-assertion of RX_DV must cause CARRIER_STATUS to transition to the CARRIER_OFF value. This transition of CARRIER_STATUS from the CARRIER_ON to the CARRIER_OFF value must be recognized by the MAC sublayer, even if the CRS signal is still asserted at the time.

Proposed Response  
Response Status  O
PLS_Carrier.indication is now based on both LPI and traditional RX_DV and CRS signals. Carrier indication is normally ignored in the full duplex Annex 4A MAC. However, with LPI, the MAC will operate in full duplex and use PLS_Carrier.indication to defer transmit.

The precedence between LPI and RX_DV/CRS is unclear. Unnecessary transmit deferral could occur due to Rx activity. See presentation.

### Suggested Remedy

See presentation. Revise section 22.2.1.1.3 to clarify signals and algorithm used to assert carrier indication.

---

The original branch condition from RX_SLEEP to IDLE state

\[
\text{signal\_status = ON \* (rx\_bits[9:5] = /I/ \+ rx\_bits[4:0] = /I/)}
\]

can be made more restrictive to

\[
\text{signal\_status = ON \* (rx\_bits[9:5] = /I/ \* rx\_bits[4:0] = /I/)}
\]

### Suggested Remedy

change to

\[
\text{signal\_status = ON \* rx\_bits[9:0] = IDLES}
\]

---

Define the behavior of the PHY when it doesn't support EEE but receives LP_IDLE.

### Suggested Remedy

Insert new text after the first paragraph of 78.3:

If a PHY does not support EEE, either through its own capabilities or through those negotiated with its link partner, then it shall ignore any LP_IDLE codewords it receives.

---

Add transmit clock stoppable bit.

### Suggested Remedy

Change 3.0.10 to "Receive clock stoppable".

Add 3.0.9 and name it "Transmit clock stoppable".

Change Reserved to bits 3.0.8:7

Correspondingly, change subclause heading 45.2.3.1.3a to Receive clock stoppable and introduce a new subclause 45.2.3.1.3b called Transmit clock stoppable.

---

Insert new text after the first paragraph of 78.3:

If a PHY does not support EEE, either through its own capabilities or through those negotiated with its link partner, then it shall ignore any LP_IDLE codewords it receives.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Cl</th>
<th>SC</th>
<th>P</th>
<th>L</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
</tr>
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<tbody>
<tr>
<td>79</td>
<td>46</td>
<td>46.3.1.5.a</td>
<td>123</td>
<td>49</td>
<td>T</td>
<td>D</td>
<td>Michael, Grimwood Broadcom Corporation</td>
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<tr>
<td></td>
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<td></td>
<td>Section 45.2.3.1.3a points to the Receive clock stoppable bit but this section deals with the transmit clock.</td>
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<td></td>
<td></td>
<td>Change &quot;clock stoppable&quot; to &quot;transmit clock stoppable&quot;</td>
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<td></td>
<td>Change 45.2.3.1.3a to the appropriate new section with the transmit clock stoppable bit (45.2.3.1.3b proposed in another comment).</td>
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<td>39</td>
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<td>1000BASE-T and 100BASE-TX LPI have the same nominal quiet time but different nominal sleep and refresh times. For consistency, make the 100BASE-TX sleep and refresh timers, lpi_tx_ts_timer and lpi_tx_tr_timer, have the same nominal value as the 1000BASE-T lpi_update_timer.</td>
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<td>&quot;The timer shall have a period between 100 us to 120 us.&quot;</td>
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<td>&quot;The timer shall have a period between 180 us to 250 us.&quot;</td>
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<td>Figure 46-7a shows the wrong value for TXD&lt;7:0&gt; during wake time.</td>
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<td>Show TXD&lt;7:0&gt; = 0x07 during the period shown as &quot;wake time&quot;.</td>
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<td>Figure 46-8a shows the wrong value for RXD&lt;7:0&gt; during wake time.</td>
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<td>Clarify the ordered set rules for the detection of LP_IDLE.</td>
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<td><strong>Response Status</strong> O</td>
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**Comment ID #** 83  
1/6/2009 11:17:10 AM
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

Michael, Grimwood
Broadcom Corporation

CL 45 SC 45.2.3.9a P 114 L 21 # 84

Comment Type: T
Comment Status: D

Register 7.20 is already allocated in IEEE802.3an Table 45-125, "AN LP base page ability register." EEE capability register is 3.20 as defined in 45.2.3.

Suggested Remedy
Change "7.20" to "3.20" throughout section 45.2.3.9a.

Proposed Response
Response Status: O

Michael, Grimwood
Broadcom Corporation

CL 45 SC 45.2.3.9b P 115 L 21 # 85

Comment Type: T
Comment Status: D

Register 7.21 is already allocated in IEEE802.3an Table 45-125, "AN LP base page ability register." EEE reduced energy capability register is 3.21 as defined in 45.2.3.

Suggested Remedy
Change "7.21" to "3.21" throughout section 45.2.3.9a.

Proposed Response
Response Status: O

Michael, Grimwood
Broadcom Corporation

CL 40 SC 40.4.6.1 P 103 L 23 # 87

Comment Type: T
Comment Status: D

In reference to the PHY Control State Diagram in Figure 40-15b, a corner-case, out-of-sync condition can occur when loc_lpi_req changes to FALSE and the local link partner is near the end of its WAKE_TRAINING state and the remote link partner has transitioned from WAKE_TRAINING to UPDATE.

Suggested Remedy
Setting loc_lpi_mode to OFF during WAKE_TRAINING avoids this out-of-sync condition since detection of rem_lpi_mode = OFF initiates a transition from UPDATE to active. However, this changes the original intent of lpi_mode since it is also used for the transitioning into and out of the LP_IDLE state in the PCS Receive State Diagram (Figure 40-10a). Instead, in Figure 40-15b, replace loc_lpi_mode with a new signaling variable, loc_sleep_mode, and use its PCS-encoded signaling, rem_sleep_mode, to replace rem_lpi_mode. Also, set loc_sleep_mode <= ON in the UPDATE state and loc_sleep_mode <= OFF in the WAKE_TRAINING state. In Figure 40-15a, in the SEND IDLE OR DATA state, set loc_sleep_mode <= OFF. In Section 40.3.1.3.4, for the generation of cext_errn, replace loc_lpi_mode with loc_sleep_mode. Make other necessary changes in order to introduce the new state variables and associated PMA service primitives.

A presentation will be submitted detailing the resolution to this issue.

Proposed Response
Response Status: O

Michael, Grimwood
Broadcom Corporation

CL 40 SC 40.5.1.1 P 105 L 22 # 86

Comment Type: T
Comment Status: D

Register 7.20 is already allocated in IEEE802.3an Table 45-125, "AN LP base page ability register." EEE capability register is 3.20 as defined in 45.2.3.

Suggested Remedy
Change "7.20" to "3.20".

Proposed Response
Response Status: O

CL 40 SC 40.4.6.1 P 103 L 23 # 87

Comment Type: T
Comment Status: D

In reference to the PHY Control State Diagram in Figure 40-15b, a corner-case, out-of-sync condition can occur when loc_lpi_req changes to FALSE and the local link partner is near the end of its WAKE_TRAINING state and the remote link partner has transitioned from WAKE_TRAINING to UPDATE.

Suggested Remedy
Setting loc_lpi_mode to OFF during WAKE_TRAINING avoids this out-of-sync condition since detection of rem_lpi_mode = OFF initiates a transition from UPDATE to active. However, this changes the original intent of lpi_mode since it is also used for the transitioning into and out of the LP_IDLE state in the PCS Receive State Diagram (Figure 40-10a). Instead, in Figure 40-15b, replace loc_lpi_mode with a new signaling variable, loc_sleep_mode, and use its PCS-encoded signaling, rem_sleep_mode, to replace rem_lpi_mode. Also, set loc_sleep_mode <= ON in the UPDATE state and loc_sleep_mode <= OFF in the WAKE_TRAINING state. In Figure 40-15a, in the SEND IDLE OR DATA state, set loc_sleep_mode <= OFF. In Section 40.3.1.3.4, for the generation of cext_errn, replace loc_lpi_mode with loc_sleep_mode. Make other necessary changes in order to introduce the new state variables and associated PMA service primitives.

A presentation will be submitted detailing the resolution to this issue.

Proposed Response
Response Status: O

Michael, Grimwood
Broadcom Corporation

CL 40 SC 40.5.1.1 P 105 L 22 # 86

Comment Type: T
Comment Status: D

Register 7.20 is already allocated in IEEE802.3an Table 45-125, "AN LP base page ability register." EEE capability register is 3.20 as defined in 45.2.3.

Suggested Remedy
Change "7.20" to "3.20".

Proposed Response
Response Status: O

Michael, Grimwood
Broadcom Corporation
**Comment ID # 88**

<table>
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<th>Cl 24 SC 24.3.1</th>
<th>P 47</th>
<th>L 23</th>
<th>Michael, Grimwood</th>
<th>Broadcom Corporation</th>
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</table>

**Comment Type:** T  **Comment Status:** D  

The "Receive State Diagram" in Figure 24-11 has a corner case condition in which under certain degenerate signal status conditions, it is possible to indefinitely transition back and forth between RX_QUIET and RX_WAKE, and never transition to RX_LPI_LINK_FAIL.

This condition could occur if signal_status toggles between ON and OFF with the following sequence and associated states:

1. State is RX_QUIET and signal_status toggles to ON.
2. State transitions to RX_WAKE and lpi_rx_tw_timer is reset.
3. signal_status toggles to OFF prior to lpi_rx_tw_timer expiring causing a transition back to RX_QUIET, causing lpi_rx_tq_timer to be reset.
4. Prior to lpi_rx_tq_timer expiring, signal_status toggles to ON (Causing a Repeat of step 1 and potentially an endless sequence of 2. through 4.).

**SuggestedRemedy**

Modify the "Receive State Diagram" such that lpi_rx_tq_timer is effectively not reset upon re-entry to state RX_QUIET.

A presentation will be submitted detailing this suggested remedy.

**Proposed Response**  **Response Status:** O

---

**Comment ID # 89**

<table>
<thead>
<tr>
<th>Cl 55 SC 55.3.5</th>
<th>P 174</th>
<th>L 14</th>
<th>Michael, Grimwood</th>
<th>Broadcom Corporation</th>
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</table>

**Comment Type:** T  **Comment Status:** D  

Clarify the interval of the quiet period applicable to the maximum power specification.

**SuggestedRemedy**

Change:

Average Launch Power (as measured 28 LDPC frames after Refresh period and 28 LDPC frames before the next Refresh period on the same lane) for each Transmitter shall be less than -41dBm.

**Proposed Response**  **Response Status:** O

---

**Comment ID # 90**

<table>
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<tr>
<th>Cl 55 SC 55.5.3.5</th>
<th>P 174</th>
<th>L 14</th>
<th>Michael, Grimwood</th>
<th>Broadcom Corporation</th>
</tr>
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</table>

**Comment Type:** T  **Comment Status:** D  

Clarify that the 10GBASE-T LPI Transmit Clock Frequency specification is related to the rate of change of the clock.

Remove "transmit" from mode and add punctuation.

**SuggestedRemedy**

Change:

In the lower power transmit mode the transmitter clock short term frequency variation shall be less than 0.1 ppm/second.

To:

In the lower-power mode, the transmitter clock short term rate of frequency variation shall be less than 0.1 ppm/second.

**Proposed Response**  **Response Status:** O

---

**Comment ID # 91**

<table>
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<th>L 13</th>
<th>Michael, Grimwood</th>
<th>Broadcom Corporation</th>
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**Comment Type:** E  **Comment Status:** D  

Typo, "...during while..."

**SuggestedRemedy**

Eliminate the word "during".

**Proposed Response**  **Response Status:** O

---

**Comment ID # 92**

<table>
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<tr>
<th>Cl 22 SC 22.7.1</th>
<th>P 33</th>
<th>L 46</th>
<th>Healey, Adam</th>
<th>LSI Corporation</th>
</tr>
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</table>

**Comment Type:** E  **Comment Status:** D  

Superfluous ")."

**SuggestedRemedy**

Delete ")."

**Proposed Response**  **Response Status:** O

---
The state diagram depicted in Figure 22-21, in combination with the definition of Carrier_Status in 22.2.1.1.3, describes the desired behavior, but this could be more clearly shown by adding the assignment of CARRIER_STATUS to the state diagram.

**Proposed Response**

Modify the state diagram to show CARRIER_STATUS = ON assignment in LPI_ASSERTED state and CARRIER_STATUS = OFF assignment in LPI_DEASSERTED state. Define state variables as appropriate.

---

**Comment Type**

E

**Comment Status**

D

tw_timer should be defined as timer rather than a counter. The "++" operator only implies that the counter tw_timer is incremented, not that it is incremented repeatedly while in the LPI_WAIT state or on what timescale it is incremented. Per 21.5.1, "After performing all the actions listed in a state block one time, the state block then continuously evaluates its exit conditions until one is satisfied at which point control passes through a transition arrow to the next block. While the state awaits fulfillment of one of its exit conditions, the actions inside do not implicitly repeat."

**SuggestedRemedy**

Add action "Start tw_timer" to the LPI_WAIT state and replace the transition condition for exiting the state with "tw_timer_done." Define tw_timer as a timer in 22.7.1 accordingly and state that the terminal count of the timer is the resolved wake time. Delete variable "resolved_tw."

---

**Comment Type**

T

**Comment Status**

D

LP_IDLE.indication is not used by the Transmit LPI state diagram. However, it seems like LP_IDLE.indication and LPI_IDLE.request constitute a service interface that should be defined somewhere in the document, and not necessarily in the list of state variables for the Transmit LPI state diagram.

**SuggestedRemedy**

Delete variable definition.

**Proposed Response**

Delete variable definition.
### Comments on IEEE P802.

**IEEE P802.3az D1.1 Energy Efficient Ethernet comments**

<table>
<thead>
<tr>
<th>Comment ID</th>
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<th># 98</th>
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<td>Healey, Adam</td>
<td>LSI Corporation</td>
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**Comment Type: T**

**Comment Status: D**

Constraints must be placed on the use of the LP_IDLE.request primitive to ensure correct PHY operation. A set of constraints has been described in law_02_1108, slide 10. One essential constraint is that the LP_IDLE must be asserted for a minimum period before it may be deasserted. This minimum assertion period may be PHY dependent. For example, for 1000BASE-T, it must exceed the maximum value of lpi_update_timer in order to ensure correct PHY operation (refer to comment against 40.4.6.1 for an explanation).

**Suggested Remedy**

Include appropriate constraints regarding the use of Energy Efficient Ethernet service interface primitives.

**Proposed Response**

**Response Status: O**

---

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<th>P 47</th>
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**Comment Type: T**

**Comment Status: D**

Per the Receive state diagram (Figure 24-11), from the IDENTIFY JK state, if rx_bits[9:0] is neither /I/P/ or /J/K/ then the state diagram transitions to the BAD SSD state where it remains until rx_bits[9:0] = IDLES again.

This implies that when the initial /I/P/ is not correctly detected (due to a bit error, for example), the PHY receiver will remain in the BAD_SSD state until normal idle signaling is received, and the receiver will not enter low power mode.

**Suggested Remedy**

Add a transition from BAD SSD to RX SLEEP with the transition condition rx_bits[9:0] = /P/P/.

**Proposed Response**

**Response Status: O**

---

<table>
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<th>P 94</th>
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**Comment Type: T**

**Comment Status: D**

There are conceptual issues with loc_lpi_mode encoding via cext_errn:

1. When the PHY is instructed to wake from low-power mode via that assertion of normal inter-frame at the GMII, the actual value of loc_lpi_mode can no longer be communicated (e.g. cext_errn will be tx_errorn since TXD = 0x00). Since the wake process does look that the state of rem_lpi_mode, this has not impact on PHY operation. However, this behavior is inconsistent with concept of signaling a state variable to the remote PHY.

2. Carrier Extension has no bearing on Energy Efficient Ethernet. Nesting the encoding of loc_lpi_mode in cext_errn should be avoided if possible.

**Suggested Remedy**

Remove changes to cext_errn. Instead, define sdn[1] as follows:


**Proposed Response**

**Response Status: O**
Per the PHY Control state diagram, part b, a transition from the UPDATE state to the WAKE state may be forced at any time by the assertion of loc_lpi_req = FALSE. Following additional IDLE transmission of duration lpi_waketx_timer, a period of forced silence (tx_mods = SEND_Z) will follow. This implies that:

1. Adaptive filter coefficient and timing updates may need to be aborted since the link partner's transmission may cease at any time during the update.

2. Since there is currently no constraint on how the power management agent asserts and de-asserts LP_IDLE, one can envision pathological timing scenarios where LP_IDLE is asserted at the GMII such that the PHY transitions to the UPDATE state, and then the LP_IDLE is de-asserted forcing the update of timing and adaptive filter coefficients to be aborted, and then LP_IDLE is asserted again such that the PHY returns to the update state. Repetitions of this timing cycle can starve the PHY of essential update degrading link performance.

While constraints regarding how the power management agent uses LP_IDLE could address this issue, a guaranteed minimum period of transmission from the link partner facilitates timing and filter coefficient updates and makes PHY layer performance independent of higher layer behaviors. This may be accomplished with simple modifications to the PHY Control state diagram.

Suggested Remedy

PHY Control state diagram changes will be submitted as a presentation to the Task Force.
There are two distinct application spaces to be addressed by Energy Efficient 1000BASE-T. One application space places higher value on the lowest achievable power while the other places a higher value on the fastest achievable wake time. These objectives are at odds since measures that may be taken to reduce power require longer wake up times. Furthermore, in many cases, applications that prioritize lower power are less sensitive to latency.

This suggests a need for a negotiated wake time.

Suggested Remedy

Define two energy modes: lowest energy and fastest wake. Define a "Preferred energy mode" bit to be advertised during Auto-Negotiation with the following values:

0 - indicates that lowest energy mode is preferred
1 - indicates that fastest wake is preferred

If either PHY advertises that fastest wake is preferred, then both PHYS will use fastest wake mode. If both PHYS advertise a preference for lowest energy, then both PHYS will use lowest energy mode.

Each mode is realized via the values of lpi_wake_timer and lpi_wakemz_timer.

For fastest wake mode:
- lpi_wake_timer = 16 us +/- TBD%
- lpi_wakemz_timer = 5 us +/- TBD%

For lowest energy mode:
- lpi_wake_timer = 24 +/- TBD%
- lpi_wakemz_timer = 8 +/- TBD%

Both modes must be implemented by a compliant PHY. The advertisement may also be sent via LLDP to allow the system to configure the mode during link operation based on application needs.
The variable wake time in Table 55-2 and the variable refresh time in Table 55-3 create an inordinate number of PHY implementation permutations and create a test and interoperability nightmare.

For example if only one implementer chooses to use an aggressive wake time for the first generation and others choose a longer wake time, then that PHY will be released on the market without any interoperability testing that uses the faster wake time. Much later, after many devices are in the field, other implementers will make more aggressive wake times and suddenly we will have severe interoperability problems.

The implementers involved in this standard should agree on the fastest wake time that they can all support and stick to that one. Similarly, the implementers should agree on the shortest refresh time that they can all implement and stick to that one.

Suggested Remedy
This commenter believes that the following two values are ideal:

\[ \text{ipi tx wake time} = 5 \text{ frames} \]
\[ \text{ipi refresh time} = 4 \text{ frames} \]

Change the text, tables, variable definitions and control functions to match these numbers.

Proposed Response  Response Status  O

Suggested Remedy
'modee' should be mode

Suggested Remedy
Several 'Sleep's on this page

Proposed Response  Response Status  O
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<td>163</td>
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<td>Extra . in the sentence.</td>
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<td>-</td>
<td>-</td>
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<td>169</td>
<td>L</td>
<td>A transition from SEND_SLEEP to SEND QUIET is missing.</td>
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<td>55.4.2.4</td>
<td>172</td>
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<td>172</td>
<td>L</td>
<td>There needs to be text added refering to Figure 55-24.</td>
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<td>Add a line stating that Figure 55-24 is the EEE receive state diagram, which must be implemented in PHYs that support the EEE capability.</td>
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<td>78.2.3</td>
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<td>Tw_phy is described as 'Period of time between reception IDLE signal appearing on the xxMII interface and when first codewords are permitted on the xxMII interface'.</td>
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<td>The IDLE signal is a codeword. I think the second part of the sentence should say 'first data codewords'.</td>
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Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

Table of Comments:

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Comment Details:

1. **Comment #129**
   - **Type**: TR (technical required)
   - **SC**: 55.6.1
   - **Page**: 175
   - **Line**: 2
   - **Proposed Response**: Change the valid values to match those in grimwood_03_1108.pdf.
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #129
   - **Page**: 28 of 46

2. **Comment #130**
   - **Type**: E (editorial required)
   - **SC**: 55.6.1
   - **Page**: 175
   - **Line**: 2
   - **Proposed Response**: Change both the TBDs on line 2 and 6 to "55.3.5 and 55.6.3".
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #130

3. **Comment #131**
   - **Type**: ER (editorial required)
   - **SC**: 55.3.5.4
   - **Page**: 166
   - **Proposed Response**: Add a dashed line around the entire diagram on this page.
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #131

4. **Comment #132**
   - **Type**: ER (editorial required)
   - **SC**: 55.3.5.4
   - **Page**: 168
   - **Proposed Response**: Add a dashed line around the entire diagram on this page.
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #132

5. **Comment #133**
   - **Type**: E (editorial required)
   - **SC**: 55.3.2.2.14
   - **Page**: 158
   - **Line**: 45
   - **Proposed Response**: Change reference to Figures 55-15 and 55-16.
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #133

6. **Comment #134**
   - **Type**: E (editorial required)
   - **SC**: 55.3.5.4
   - **Page**: 169
   - **Proposed Response**: Add a box saying the state diagram is only implemented for EEE capable PHYs.
   - **Comment Status**: D (dispatched)
   - **Response Status**: O (open)
   - **Comment ID**: #134

**Type**: TR/technical required  ER/editorial required  GR/general required  T/technical  E/editorial  G/general

**Comment Status**: D/dispatched  A/accepted  R/rejected  RESPONSE STATUS: O/open  W/written  C/closed  U/unsatisfied  Z/withdrawn

**Sort Order**: Comment ID

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TYPE: TR/technical required  ER/editorial required  GR/general required  T/technical  E/editorial  G/general

COMMENT STATUS: D/dispatched  A/accepted  R/rejected  RESPONSE STATUS: O/open  W/written  C/closed  U/unsatisfied  Z/withdrawn

SORT ORDER: Comment ID
Comments on IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Cl 55  SC 55.3.2.2.21  P 159  L 13  # [135]
Parnaby, Gavin Solarflare Communica

Comment Type  E  Comment Status  D

‘during while’ should be while.

[also the formatting of these two paragraphs looks wrong].

SuggestedRemedy

Proposed Response  Response Status  O

Cl 55  SC 55.3.2.3  P 160  L 7  # [136]
Parnaby, Gavin Solarflare Communica

Comment Type  E  Comment Status  D

PCS_Status asserted okay is not described consistently on this page. See lines 7 and line 33.

SuggestedRemedy

Change both to PCS_status=OKAY

Proposed Response  Response Status  O

Cl 24  SC 24.2.2.5  P 43  L 13  # [137]
Dietz, Bryan Alcatel-Lucent

Comment Type  E  Comment Status  D

Two extra words in sentence "with a sequence of signal stream".

SuggestedRemedy

Delete "sequence of" so it reads "it replaces the continuous IDLE code-groups with a signal stream comprising".

Proposed Response  Response Status  O

Cl 40  SC 40.1.4  P 88  L 49  # [138]
Dietz, Bryan Alcatel-Lucent

Comment Type  E  Comment Status  D

Missing word

SuggestedRemedy

Insert "that it" after PHY to read: "Optionally, the ability to signal to the remove PHY that it has entered the low power mode or that it is in the normal mode of operation."

Proposed Response  Response Status  O

Cl 45  SC 45.2.3.9b  P 115  L 23  # [139]
Dietz, Bryan Alcatel-Lucent

Comment Type  E  Comment Status  D

The term "reduced energy EEE modes" is unclear. If the rest of the specification uses LPI to stand for reduced energy, then LPI should be used here. If "reduced energy" is an important phrase, then it should be defined.

If changed here, please change table 45-88b also.

SuggestedRemedy

Change "reduced energy" to "LPI" or "reduced energy/LPI". Also change table 45-88b.

Proposed Response  Response Status  O

Cl 55  SC 55.3.5.3  P 163  L 36  # [140]
Dietz, Bryan Alcatel-Lucent

Comment Type  E  Comment Status  D

Definition of lpi_tx_mode could be clarified by minor editing.

SuggestedRemedy

Please break up paragraph into a bullet list with entries like

*The variable is set to REFRESH_A if…
*The variable is set to REFRESH_B if…

Proposed Response  Response Status  O
Dietz, Bryan  
Alcatel-Lucent  

Comment Type: E  Comment Status: D  
Twr min and max values are surprising Min > max.  

Suggested Remedy:  
Check values and edit table if needed.  

Proposed Response  
Response Status: O  

---  

Bennett, Michael  
LBNL  

Comment Type: E  Comment Status: D  
The sentence "The transmit and receive paths can enter and exit low power state independently" is stating that there is a low power state for each path, so "state" should be "states"  

Suggested Remedy:  
change state to states  

Proposed Response  
Response Status: O  

---  

Tellado, Jose  
Teranetics  

Comment Type: ER  Comment Status: D  
Is "." accepted as a multiplication symbols?  

Suggested Remedy:  
Is "." accepted as a multiplication symbols?  

Proposed Response  
Response Status: O  

---
Comments on IEEE P802.

Cl 55 SC 55.3.5.3 P 162 L 46 # 157
Tellado, Jose Teranetics

Comment Type ER Comment Status D
Change PAM-2 to PAM2. Multiple locations

Suggested Remedy

Proposed Response Response Status O

Cl 55 SC 55.3.5 P 160 L 33 # 158
Tellado, Jose Teranetics

Comment Type ER Comment Status D
Change "=OKAY" to "=OK"

Suggested Remedy

Proposed Response Response Status O

Cl 55 SC 55.3.5.1 P 162 L # 159
Tellado, Jose Teranetics

Comment Type T Comment Status D
why isn't the "v=" column equal to the "u=" column offset by approx lpi_offset?

Suggested Remedy

Proposed Response Response Status O

Cl 55 SC 55.6.3 P 175 L 29 # 160
Tellado, Jose Teranetics

Comment Type T Comment Status D
why not smallest advertised lpi_regresh_time_value? Largest will always be 32.

Suggested Remedy

Proposed Response Response Status O
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Jan 2009

1. Comment 164

**Comment ID:** 164

**Cl:** 55 **SC:** 55.1.3.3 **P:** 153 **L:** 39

Taich, Dmitry Teranetics

**Comment Type:** E **Comment Status:** D

We don't modify data rate - it is always 10Gb/s. We only force device to be operated in Normal mode or Low Power Idle mode.

**Suggested Remedy:**

Replace "link again supports the full 10Gb/s data rate" by "Normal operational mode is resumed"

**Proposed Response:**

Response Status: O

2. Comment 165

**Comment ID:** 165

**Cl:** 55 **SC:** 55.1.3.3 **P:** 153 **L:** 51

Taich, Dmitry Teranetics

**Comment Type:** E **Comment Status:** D

We don't modify data rate - it is always 10Gb/s. We only force device to be operated in Normal mode or Low Power Idle mode.

**Suggested Remedy:**

Replace "link again supports the full 10Gb/s data rate" by "Normal operational mode is resumed"

**Proposed Response:**

Response Status: O

3. Comment 166

**Comment ID:** 166

**Cl:** 55 **SC:** 55.3.3.3.21 **P:** 159 **L:** 39

Taich, Dmitry Teranetics

**Comment Type:** ER **Comment Status:** D

Columns in Table 55-2 seem to be reversed.

**Suggested Remedy:**

Fix table according to the comment

**Proposed Response:**

Response Status: O

4. Comment 167

**Comment ID:** 167

**Cl:** 55 **SC:** 55.3.5 **P:** 161 **L:** 39

Taich, Dmitry Teranetics

**Comment Type:** ER **Comment Status:** D

Columns in Table 55-3 seem to be reversed.

**Suggested Remedy:**

Update draft with proposed test modes definition and encourage readers to comment. Current form does not seem to do it successfully.

**Proposed Response:**

Response Status: O

5. Comment 168

**Comment ID:** 168

**Cl:** 55 **SC:** 55.3.5 **P:** 174 **L:** 9

Taich, Dmitry Teranetics

**Comment Type:** ER **Comment Status:** D

Editors note includes reference to taich_01_1108.pdf regarding test modes. This presentation contains very specific recommendations as readers to new test modes definition. I believe it would be beneficial to update draft with proposed test modes definition.

**Suggested Remedy:**

Update draft with test modes proposal as in taich_01_1108.pdf

**Proposed Response:**

Response Status: O

6. Comment 169

**Comment ID:** 169

**Cl:** 55 **SC:** 55.2.2.3.1 **P:** 156 **L:** 3

Taich, Dmitry Teranetics

**Comment Type:** T **Comment Status:** D

In addition to two listed cases, "SYMB_4D" primitive should take value of SEND_Z during last 128 symbols of the Alert pattern

**Suggested Remedy:**

Update 55.2.2.3.1 accordingly

**Proposed Response:**

Response Status: O
IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Comments on IEEE P802.

Cl 55 SC 55.3.5.2.2 P 163 L 40 # 170
Taich, Dimitry Teranetics

Comment Type TR Comment Status D

SuggestedRemedy

lpi tx_mode variable definition should be determined by tx_active_pair value. Currently all four pairs active/quiet share same calculation formula - seems like copy-paste typo.

Fix lpi tx_mode variable definition as below:

The variable is set to REFRESH_A when tx_lpi_active * (tx_active_pair==PAIR_A * tx_refresh active).

The variable is set to REFRESH_B when tx_lpi_active * (tx_active_pair==PAIR_B * tx_refresh active).

The variable is set to REFRESH_C when tx_lpi_active * (tx_active_pair==PAIR_C * tx_refresh active).

The variable is set to REFRESH_D when tx_lpi_active * (tx_active_pair==PAIR_D * tx_refresh active).

Proposed Response Response Status O

Cl 55 SC 55.4.2.2.1 P 171 L 27 # 171
Taich, Dimitry Teranetics

Comment Type TR Comment Status D

Text reads as following: "The alert signal shall be transmitted on pair A when the PHY operates as a MASTER. The Alert signal shall be transmitted on pair C when the PHY operates as a SLAVE. All other pairs shall transmit quiet or refresh as described in subclause 55.3.5." Last sentence is incorrect.

modify last sentence to read "All other pairs shall transmit quiet (SEND_Z symbols) as described 55.3.5."* 

Proposed Response Response Status O

Cl 14 SC 14.3.1.2 P 20 L 41 # 172
Law, David 3Com

Comment Type TR Comment Status D

Class D can be either Category 5 or Category 5e dependant on the year of the standard. ISO/IEC 11801:1995 Class D is equivalent to Category 5, ISO/IEC 11801:2002 Class D is equivalent to Category 5e.

SuggestedRemedy

Suggest that .. Class D channel as specified in ISO/IEC 11801.' be change to read ‘.. Class D channel as specified in ISO/IEC 11801:1995.’.

Proposed Response Response Status O

Cl 14 SC 14.1.1.1 P 19 L 10 # 173
Law, David 3Com

Comment Type TR Comment Status D

Now that we have the two 10BASE-T PHYs we need to be clear what the distances are supported for the various cabling types. These are:

10BASE-T supports 0 to 100 m on simplex link segments meeting or exceeding the channel specified in subclause 14.4. 10BASE-Te supports 0 to 100m on simplex link segments meeting or exceeding the Class D channel as specified in ISO/IEC 11801:1995.

SuggestedRemedy

Make the following changes:

[1] In subclause 14.1.1.1 add the following text to the end of item c):

The 10BASE-T PHY provides for operating over 0 m to at least 100 m of twisted pair cabling meeting or exceeding the simplex link segment specification found in 14.4. This specification is generally met by 0.5 mm telephone twisted pair. The 10BASE-T PHY provides for operation over 0 m to at least 100 m of ISO/IEC 11801:1995 Class D or better cabling.

[2] In subclause 14.1.1.3 'Twisted-pair media' (not currently included in draft) add the following new paragraph:

The medium for 10BASE-Te is a channel meeting or exceeding the requirements of the Class D channel specified by ISO/IEC 11801:1995.

[3] Subclause 14.4 'Characteristics of the simplex link segment' needs to be reviewed and updated in respect to the use of Cat 5 by 10BASE-Te.

Proposed Response Response Status O

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID
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</table>

**Comment 174**

I'm maybe missing something here by 45.2.7.13a 'EEE advertisement (Register 7.60)' only defines 6 bits of the 11 bits available in a Unformatted Next Page so I can't see why in the Annex 28C changes both Message code 10 and Message code 11 are defined for EEE. Further the Annex 73A changes only define Message code 10.

**Suggested Remedy**

Either define what Message code 11 is required for or return it to be a reserved value.

**Comment 175**

Low power idle on the receive GMII is indicated by 'Assert low power idle', see Table 35-2 (page 71).

**Suggested Remedy**

Change '.. is indicated as low power idle at the GMII ..' to read '.. is indicated as Assert low power idle at the GMII ..'. Update similar reference to the GMII as required.

**Comment 176**

According to the state diagram shown in Figure 40-9 this value will only be asserted when 1000BTtransmit is also true, not just when 'Assert low power idle' is present on the GMII.

**Suggested Remedy**

Update the description of the TRUE and FALSE conditions as required. Also may need to updated the current definition of 1000BTtransmit in subclause 40.3.3.1 which states 'Use by Carrier Sense process'.

**Comment 177**

The Low power idle state isn't requested by the MAC - see model shown in Figure 22-20a (page 33).

**Suggested Remedy**

Change the text '.. either the MAC or the link partner requests low power operation ..' to read '.. either the local or link system requests low power operation ..'.
<table>
<thead>
<tr>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment ID</th>
<th>Law, David</th>
<th>Page 35 of 46</th>
</tr>
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<tbody>
<tr>
<td>ER</td>
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<td>180</td>
<td>3Com</td>
<td></td>
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<tr>
<td>ER</td>
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<td>181</td>
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<tr>
<td>TR</td>
<td>D (late)</td>
<td>182</td>
<td>3Com</td>
<td></td>
</tr>
</tbody>
</table>

**Comment Type: ER**

Maintain link quality is very broad and really what is happening is a tracking of the changes in the channel characteristics. Suggest text parallel to that used in 1000BASE-T would be better.

**Suggested Remedy**

Suggest 'While the link is in the lower power mode a periodic refresh signal is used to maintain link quality.' be changed to read 'While the PHY is in lower power mode the PHY periodically transmits a refresh signal to allow the remote PHY to refresh its receiver state (e.g. timing recovery, adaptive filter coefficients) and thereby track long term variation in the timing of the link or the underlying channel characteristics.'.

**Proposed Response**

Response Status O

---

<table>
<thead>
<tr>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment ID</th>
<th>Law, David</th>
<th>Page 35 of 46</th>
</tr>
</thead>
<tbody>
<tr>
<td>E</td>
<td>D (late)</td>
<td>183</td>
<td>3Com</td>
<td></td>
</tr>
</tbody>
</table>

**Comment Type: E**

I believe we are using the term wake rather than alert.

**Suggested Remedy**

Suggest 'An alert signal ..' is changed to read 'A wake signal ..'.

**Proposed Response**

Response Status O

---

<table>
<thead>
<tr>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment ID</th>
<th>Law, David</th>
<th>Page 35 of 46</th>
</tr>
</thead>
<tbody>
<tr>
<td>TR</td>
<td>D (late)</td>
<td>184</td>
<td>3Com</td>
<td></td>
</tr>
</tbody>
</table>

**Comment Type: TR**

Class D is necessary but not sufficient to specify the cabling since this can be either Category 5 or category 5e dependant on the year of the ISO/IEC 11801 standard. ISO/IEC 11801:1995 Class D is equivalent to Category 5. ISO/IEC 11801:2002 Class D is equivalent to Category 5e. We should also make the reference to the TIA standard clearer.

**Suggested Remedy**

Suggest that '.. of class D (Category 5) or better cabling.' be change to read '.. Class D, or better, cabling as specified in ISO/IEC 11801:1995. This requirements can also met by Category 5 cable and components as specified in ANSI/TIA/EIA-568-A-1995.'.

**Proposed Response**

Response Status O

---

<table>
<thead>
<tr>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment ID</th>
<th>Law, David</th>
<th>Page 35 of 46</th>
</tr>
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<tbody>
<tr>
<td>TR</td>
<td>D (late)</td>
<td>184</td>
<td>3Com</td>
<td></td>
</tr>
</tbody>
</table>

**Comment Type: TR**

The 10BASE-Te PHY is somewhat orthogonal to EEE as it doesn't support disabling functionality in attached systems during periods of low link utilization. It should therefore appear in a separate paragraph from Auto-Negotiation.

In addition, while 10BASE-Te reduces power consumption, and enables a move to more modern geometries, which again saves power, it is not clear what is meant by 'power consumption saving schemes'.

**Suggested Remedy**

Change the text '.. power consumption saving schemes to ..' to simply read '.. power consumption saving to ..', make the text starting 'EEE also ..' into a separate paragraph.

**Proposed Response**

Response Status O

---

**TYPE:** TR/technical required  ER/editorial required  GR/general required  T/technical  E/editorial  G/general  
**COMMENT STATUS:** D/dispatched  A/accepted  R/rejected  
**RESPONSE STATUS:** O/open  W/written  C/closed  U/unsatisfied  Z/withdrawn  
**SORT ORDER:** Comment ID  
**Comment ID # 184** 1/6/2009 11:17:10 AM
It isn't clear that Tw_phy has all possible delays included in it and it appears there may need to be a Tw_phy allocation from the transmit and receive PHY to insure interoperability.

In addition the symbol Tw_sys seems to be used for three different parameters, Transmit Tw (subclause 78.4.2.1), Receive Tw (subclause 78.4.2.2) and Resolved Transmit Tw_sys (subclause 78.4.2.3). Suggest for increased clarity different symbols should be used for each of these parameters.

Suggested Remedy
Please see presentation law_1_0109.pdf

Law, David 3Com

The penultimate paragraph of subclause 78.1.3 states 'If both link partner enter and exit Low Power Idle mode simultaneously this mode of operation is called symmetric. If each link partner can entrance and exit Low Power Idle mode independently this mode of operation is called asymmetric.'.

As far as I can see all PHYs, including 1000BASE-T, support system entry and exit to power saving mode asymmetrically. In the one case of 1000BASE-T, the PHYs enters and exits power saving mode symmetric, all other PHYs enter and exit asymmetrically. Further the 1000BASE-T PHY still signals Low Power Idle requests asymmetrically.

Since system entry and exit to power saving is the same for all PHY types, defining two modes just to describe one PHYs entry and exit to power saving seems like a slightly complex approach and it would be better to simply mention this exception in the particular PHY in question.

Suggested Remedy
I would prefer that specific mention of the symmetric and asymmetric modes are removed and that it is simply noted in 1000BASE-T that the PHY doesn’t enter power saving mode until both ends of the link are signaling Low Power Idle. It should be further noted that Low Power Idle requests are passed from one end of the link to the other regardless and the system energy savings can be achieved even if the PHY is not in that mode.

If the consensus is not to remove symmetric and asymmetric mode, make it clear that the only impact is on the power savings of the PHY, that Low Power Idle is always passed across the link, and that system energy savings are always asymmetric.

See law_2_0109.pdf.
This paragraph states: 'Implementations that support Energy Efficient Ethernet shall comply with all mandatory parts of IEEE Std 802.1AB and shall support the EEE Type, Length, Value (TLV) defined in 78.1.2.'

According to [http://www.ieee802.org/3/az/public/may08/hays_02_0508.pdf#Page=5], which was adopted in May 2008 as a baseline [http://www.ieee802.org/3/az/public/may08/802.3az-minutes-2008-05.pdf#Page=6-Motion #1] the use of LLDP is optional. Based on this I would have expected that LLDP would not be mandated for EEE and while I may have missed it I can't find a motion to make LLDP mandatory for EE devices.

**Suggested Remedy**
Update this subclause to make it clear that LLDP is optional for EEE.

**Proposed Response**

---

It is odd to see mention of Half Duplex mode here when EEE only supports Full Duplex mode.

**Suggested Remedy**
remove first sentence, also suggest that 'On top of the above considerations, ..' be changed to read 'In addition, ..'.

**Proposed Response**

---

The MAC device should not present a start code for valid transmit data until after the wake up time specified.

For MII and GMII showing the TXD as "zero" was valid, but in XGMII an idle is "07".

**Suggested Remedy**
Add a line:

The MAC device should be setting TXD<7:0> to 07 during the wake time.

Fig 46-7a needs to be corrected accordingly

**Proposed Response**

---

Hence it should be:

deserting RXC<0> and asserting RXD<7:0> to 07 during the wake time.

**Suggested Remedy**

---

Id(e) being detected in any row and the rest of the rows in the same column being detected as[KU or KU], will result in reporting LP_IDLE in lane 0 and IDLE in lanes 1 to 3

**Proposed Response**

---
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment ID</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Suggested Remedy</th>
<th>Proposed Response</th>
<th>Response Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>193</td>
<td>193</td>
<td>TR</td>
<td>D</td>
<td>There is no exit condition from RX_LINK_FAIL state other than &quot;reset=TRUE&quot;.</td>
<td></td>
<td></td>
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<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Will come up with a suggestion.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>T</td>
<td>D</td>
<td>Need to add a note for devices that do not support LPI</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>E</td>
<td>D</td>
<td>new term needs to be underlined.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>T</td>
<td>D</td>
<td>Sync state machine needs changing for LPI.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Change sync state machine - sync_status becomes code_sync_status (add new variable in 36.2.5.1.3).</td>
<td></td>
<td></td>
</tr>
<tr>
<td>196</td>
<td>196</td>
<td>T</td>
<td>D</td>
<td>If the optional Low Power Idle function is not implemented then sync_status is identical to code_sync_status. Otherwise the relationship between sync_status and code_sync_status is given by 36-9b the LPI receive state diagram.</td>
<td></td>
<td></td>
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<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Add a penultimate paragraph.</td>
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TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID
Cl 36 SC 36.2.5.2.8 P 83 L 6 # 198
Barrass, Hugh Cisco
Comment Type T  Comment Status D
sync_status is now distinct from code_sync_status

add a term to update sync_status

Suggested Remedy
Add a term in state RX_ACTIVE:

sync_status<=code_sync_status

Proposed Response  Response Status O

Cl 36 SC 36.2.5.2.8 P 83 L 7 # 199
Barrass, Hugh Cisco
Comment Type T  Comment Status D
sync_status is now distinct from code_sync_status

transition must be forced to update sync_status appropriately.

Suggested Remedy
Change detect_idle

to detect_idle + sync_status != code_sync_status

Proposed Response  Response Status O

Cl 36 SC 36.2.5.2.8 P 83 L 32 # 200
Barrass, Hugh Cisco
Comment Type T  Comment Status D
Transition from RX_WAKE needs to include sync status and no timeout.

Suggested Remedy
change detect_idle

to !rx_tw_timer_done * code_sync_status = OK * detect_idle

Proposed Response  Response Status O

Cl 36 SC 36.2.5.2.9 P 84 L 20 # 203
Barrass, Hugh Cisco
Comment Type T  Comment Status D
The MDIO status variables need to be here (not Clause 70)

Suggested Remedy
Add a new section 36.2.5.2.8, with the information currently in Table 70-3

Proposed Response  Response Status O
Cl 48 SC 48.2.6.2.2 P 134 L 31 # 204
Barrass, Hugh Cisco

Comment Type: T  
Comment Status: D

align_status is no longer controlled solely by align state machine.

SuggestedRemedy

| Change 48.2.6.2.2 Synchronization |
| change align_status flag is set to FAIL to deskew_align_status flag is set to FAIL |

Proposed Response:  
Response Status: O

Cl 48 SC 48.2.6.2.3 P 134 L 32 # 205
Barrass, Hugh Cisco

Comment Type: T  
Comment Status: D

align_status is no longer controlled solely by align state machine.

SuggestedRemedy

| Add variable deskew_align_status into 48.2.6.1.3 |
| Change align_status > deskew_align_status in 48-8. |

Change 48.2.6.2.3 Deskew

The PCS shall implement the Deskew process as depicted in Figure 48-8 including compliance with the associated state variables as specified in 48.2.6.1. The Deskew process is responsible for determining whether the underlying receive channel is capable of presenting coherent data to the XGMII. The Deskew process assigns the deskew_align_status flag to indicate that the PCS has successfully deskewed and aligned code-groups on all lanes. The Deskew process attempts deskew and alignment whenever the deskew_align_status flag is de-asserted. The Deskew process is otherwise idle. If the optional Low Power Idle function is not implemented then align_status is identical to deskew_align_status. Otherwise the relationship between align_status and deskew_align_status is given by 48-9b the LPI receive state diagram. Whenever the align_status flag is set to FAIL the condition is indicated as a link_status=FAIL condition in the status register bit 4.1.2 or 5.1.2.

Proposed Response:  
Response Status: O

Cl 48 SC 48.2.6.2.5 P 135 L 11 # 206
Barrass, Hugh Cisco

Comment Type: T  
Comment Status: D

align_status is no longer controlled solely by align state machine.

SuggestedRemedy

| Add a term tx_quiet <= false |

Proposed Response:  
Response Status: O

Cl 48 SC 48.2.6.2.5 P 136 L 6 # 207
Barrass, Hugh Cisco

Comment Type: T  
Comment Status: D

align_status is no longer controlled solely by align state machine.

SuggestedRemedy

| In state RX_ACTIVE, add a term align_status <= deskew_align_status |

Proposed Response:  
Response Status: O

Cl 48 SC 48.2.6.2.5 P 136 L 8 # 208
Barrass, Hugh Cisco

Comment Type: T  
Comment Status: D

align_status is no longer controlled solely by align state machine.

SuggestedRemedy

| Change transition out of state RX_ACTIVE from ||IDLE|| to ||IDLE|| + align_status != deskew_align_status |

Proposed Response:  
Response Status: O
Cl 48 SC 48.2.6.2.5 P 136 L 32 # 209
Barrass, Hugh Cisco
Comment Type T Comment Status D
Transition from RX_WAKE needs to include align status and no timeout.
SuggestedRemedy
Change transition out of RX_WAKE from ||LPIDLE||
to !rx_tw_timer_done * deskew_align_status=OK * ||LPIDLE||
Proposed Response Response Status O

Cl 48 SC 48.2.6.2.5 P 136 L 36 # 210
Barrass, Hugh Cisco
Comment Type T Comment Status D
Transition from RX_WAKE needs to include align status and no timeout.
SuggestedRemedy
Change transition out of RX_WAKE from ||IDLE||
to !rx_tw_timer_done * deskew_align_status=OK * ||IDLE||
Proposed Response Response Status O

Cl 49 SC 49.2.9 P 140 L 38 # 213
Barrass, Hugh Cisco
Comment Type T Comment Status D
block lock is no longer controlled solely by lock state machine.
SuggestedRemedy
Change 49.2.9 Block synchronization
Add a paragraph
If the optional Low Power Idle function is not implemented then block_lock is identical to rx_block_lock. Otherwise the relationship between block_lock and rx_block_lock is given by 49-15 the LPI receive state diagram.
Proposed Response Response Status O

Cl 49 SC 49.2.13.2.2 P 142 L 16 # 214
Barrass, Hugh Cisco
Comment Type T Comment Status D
block lock is no longer controlled solely by lock state machine.
SuggestedRemedy
Add rx_block_lock
Description same as block_lock - from the lock state diagram, used to generate block_lock, may be overridden by the optional LPI receive state machine
Proposed Response Response Status O
Cl 49 SC 49.2.13.2.2 P 142 L 32 # 215
Barrass, Hugh Cisco

Comment Type T Comment Status D
For 10GBASE-KR, tx_quiet needs to indicate refresh & wake states (i.e. 4 values).

SuggestedRemedy
- change tx_quiet definition to

An enumerated variable set to TRUE when the transmitter is in the TX_QUIET state, set to REFRESH when the transmitter is to send refresh signaling, set to WAKE when the transmitter is to send wake signaling and set to FALSE otherwise. When set to TRUE, the PMD will disable the transmitter as described in 71.6.6. When set to REFRESH or WAKE the PMD will send training signals as described in 71.6.12.

Proposed Response Response Status O

Cl 49 SC 49.2.13.2.5 P 143 L 15 # 216
Barrass, Hugh Cisco

Comment Type T Comment Status D
Need a wake timer

SuggestedRemedy
- add tx_tw_timer

This timer is started when the PMD’s receiver enters the TX_WAKE state. The timer terminal count is set to TWL. When the timer reaches terminal count it will set the tx_tw_timer_done = TRUE.

Proposed Response Response Status O

Cl 49 SC 49.2.13.2.6 P 143 L 23 # 217
Barrass, Hugh Cisco

Comment Type T Comment Status D
tx_quiet definition has changed.

SuggestedRemedy
- change PMD_TXQUIET message definition to

A signal sent by the PCS/PMA LPI transmit state machine to the PMD. When TRUE this indicates that the transmitter is in a quiet state and may cease to transmit a signal on the medium. When REFRESH or WAKE this indicates that the transmitter must send specific signals to support LPI operation.

Proposed Response Response Status O

Cl 49 SC 49.2.13.3 P 143 L 37 # 218
Barrass, Hugh Cisco

Comment Type T Comment Status D
block lock is no longer controlled solely by lock state machine.

SuggestedRemedy
- Change fig 49-12 Lock state diagram

block_lock -> rx_block_lock
6 instances

Proposed Response Response Status O

Cl 49 SC 49.2.13.3.1 P 146 L 11 # 219
Barrass, Hugh Cisco

Comment Type T Comment Status D
State TX_ACTIVE needs to set tx_quiet = false

SuggestedRemedy
- Add a term tx_quiet <= false

Proposed Response Response Status O
### Comments on IEEE P802

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Commenter</th>
<th>Proposed Response</th>
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</thead>
<tbody>
<tr>
<td>222</td>
<td>T</td>
<td>D</td>
<td>Barrass, Hugh</td>
<td>Add a state TX_WAKE.</td>
</tr>
<tr>
<td></td>
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<td>Cisco</td>
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<td></td>
</tr>
<tr>
<td>221</td>
<td>T</td>
<td>D</td>
<td>Barrass, Hugh</td>
<td>In state TX_REFRESH change tx_quiet &lt;= false to tx_quiet &lt;= refresh</td>
</tr>
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<tr>
<td>225</td>
<td>T</td>
<td>D</td>
<td>Barrass, Hugh</td>
<td>Change transition out of RX_WAKE from R_TYPE(rx_raw) = LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) = LI</td>
</tr>
<tr>
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<tr>
<td>224</td>
<td>T</td>
<td>D</td>
<td>Barrass, Hugh</td>
<td>Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI</td>
</tr>
<tr>
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</tr>
</tbody>
</table>

**Suggested Remedy**

- A new state is required to control sending extra training frames during a wake cycle for 10GBASE-KR.

- Transition from TX_QUIET & TX_REFRESH with T_TYPE(tx_raw) != LI go into new state.

- After tx_tw_timer expires, transition to TX_ACTIVE.

**Comment Status**

- D/dispatched

**Response Status**

- O/open

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Add a state TX_WAKE.

**Suggested Remedy**

- Add a state TX_WAKE.

- Includes term tx_quiet <= wake

- Transitions from TX_QUIET & TX_REFRESH with T_TYPE(tx_raw) != LI go into new state.

- After tx_tw_timer expires, transition to TX_ACTIVE.

---

**Comment ID #** 224

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) = LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) = LI

**Suggested Remedy**

- In state TX_REFRESH change tx_quiet <= false to tx_quiet <= refresh

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- Transition from RX_WAKE needs to include lock status and no timeout.

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- In state RX_ACTIVE add a term block_lock <= rx_block_lock

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- Transition from RX_WAKE needs to include lock status and no timeout.

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- Transition from RX_WAKE needs to include lock status and no timeout.

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- Transition from RX_WAKE needs to include lock status and no timeout.

---

**Comment ID #** 225

**Commenter** Barrass, Hugh

**Proposed Response** Change transition out of RX_WAKE from R_TYPE(rx_raw) != LI to !rx_tw_timer_done * rx_block_lock=OK * R_TYPE(rx_raw) != LI

**Suggested Remedy**

- Transition from RX_WAKE needs to include lock status and no timeout.
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<thead>
<tr>
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<th>P</th>
<th>L</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>226</td>
<td>147</td>
<td>36</td>
<td>T</td>
<td>D</td>
<td>Block lock is no longer controlled solely by lock state machine.</td>
<td>In state RX_LINK_FAIL add a term block_lock &lt;= false</td>
</tr>
</tbody>
</table>
| 227        | 148 | 7  | T            | D            | A new parameter is needed for wake time | TWL Local Wake Time from LPI deasserted to TX_ACTIVE state 10 us  
also change Tsi and TUL to 5 us |
| 228        | 148 | 22 | T            | D            | The MDIO status variables need to be here (not Clause 72) | Change section 49.2.14.1, with the information currently in Table 72-3 |
| 229        | 179 | 10 | T            | D            | There is no enable for LPI | Delete "When this capability is enabled" |
| 230        | 179 | 10 | E            | D            | Typo | Change PDM to PMD |
| 231        | 179 | 10 | T            | D            | Reference is TBD & uses poor terminology. | Change PCS LPI modes described in 36.2.2.2.x. to PCS LPI behavior described in 36.2.5.2.8. |
Comments on IEEE P802.

IEEE P802.3az D1.1 Energy Efficient Ethernet comments

Proposed Response

#232
Cl 70  SC 70.3a  P 179  L 32  #232
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
Reference is TBD & uses poor terminology.

SuggestedRemedy
Change PMA LPI modes described in 36.2.2.x.
to PMD LPI messages described in 36.2.5.1.6.

Proposed Response

#233
Cl 70  SC 70.6  P 180  L 8  #233
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
LPI status should come from PCS.

SuggestedRemedy
Move (new) LPI status to Clause 36.

Proposed Response

#234
Cl 71  SC 71.1  P 186  L 43  #234
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
There is no enable for LPI

SuggestedRemedy
Delete "When this capability is enabled"

Proposed Response

#235
Cl 71  SC 71.6.12  P 189  L 19  #235
Barrass, Hugh  Cisco

Comment Type  E  Comment Status  D
Typo

SuggestedRemedy
Change PDM to PMD

Proposed Response

#236
Cl 71  SC 71.5  P 188  L 9  #236
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
LPI status should come from PCS.

SuggestedRemedy
Move (new) LPI status to Clause 48.

Proposed Response

#237
Cl 72  SC 72.1  P 196  L 35  #237
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
There is no enable for LPI

SuggestedRemedy
Delete "When this capability is enabled"

Proposed Response

#238
Cl 72  SC 72.3  P 197  L 40  #238
Barrass, Hugh  Cisco

Comment Type  T  Comment Status  D
LPI status should come from PCS.

SuggestedRemedy
Move (new) LPI status to Clause 49.

Proposed Response
Cl 72 SC 72.6.10.2.3.3 P 199 L 27 # 239
Barrass, Hugh Cisco

Comment Type T Comment Status D

refresh & wake are signaled from PCS.

Suggested Remedy
Change the last sentence to read.

When tx_quiet has the values REFRESH or WAKE states the coefficient update fields shall be set to hold.

Proposed Response Response Status O

Cl 72 SC 72.6.10.2.4.5 P 200 L 51 # 240
Barrass, Hugh Cisco

Comment Type T Comment Status D

refresh & wake are signaled from PCS.

Suggested Remedy
Change the last sentence to read.

When tx_quiet has the values REFRESH or WAKE states the coefficient status shall not be updated.

Proposed Response Response Status O

Having the stateful definition in this clause is redundant when it is already specified in clause 49. The signaling contained in the training frames during refresh & wake is defined above.

The LPI transmit state function adds no new information & can be deleted. 10 training frames (refresh) is approx. the same as 4.5uS, 20 frames is 9uS. Instead of defining a different state machine to send training frames during refresh & wake define that the transmitter sends training frames continuously when tx_quiet = REFRESH or WAKE.

Suggested Remedy
Delete this whole section and replace with...

define that the transmitter sends training frames continuously when tx_quiet = REFRESH or WAKE.

Receiver function needs change to training state machine (fig 72-5):

SEND_DATA state : rx_quiet = true --> new state RX_SLEEP

RX_SLEEP new state (training <= TRUE, signal_detect <= false): rx_quiet = false --> new state RX_WAKE

RX_WAKE new state : frame_lock --> new state RX_TRAINING

RX_TRAINING new state: rx_trained --> SEND_DATA

Also note that local coefficient values should be frozen during state RX_SLEEP and RX_WAKE.

[editor's note: synchronization with FEC function is not defined. If support for FEC with LPI is required then this must be addressed] (same as we have now!)