Proposed responses

Cl  01  SC  1.4  P14  L 28  # 1
Anslow, Peter  Nortel Networks

Comment Type  E  Comment Status  D
There should be a space between a number and its unit. This should be a non-breaking space (ctrl space) to avoid the unit appearing on a different line from the number.

Suggested Remedy
change "10Mb/s" to "10 Mb/s"

Proposed Response  Response Status  W
PROPOSED ACCEPT.

Also make the same change in any other places where the same error occurs.

Cl  14  SC 14.8  P22  L 53  # 4
Anslow, Peter  Nortel Networks

Comment Type  E  Comment Status  D
Items c) and d) from the base standard have been modified but no changes are shown

Suggested Remedy
show changes to items c) and d) with underline and strikethrough font as appropriate.

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

Cl  22  SC 22.7a  P30  L 8  # 5
Anslow, Peter  Nortel Networks

Comment Type  E  Comment Status  D
There should be a space between a number and its unit. This should be a non-breaking space (ctrl space) to avoid the unit appearing on a different line from the number.

Suggested Remedy
change "100Mb/s" to "100 Mb/s"

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

Delete table per comment #199

Cl  14  SC 14.10.4.5.12  P24  L 28  # 3
Anslow, Peter  Nortel Networks

Comment Type  E  Comment Status  D
TS2 is an added row so the subclause number and Req should also be in underline font.
Also applies to LS5 in 14.10.7.4.1

Suggested Remedy
Show "14.3.1.2.1" and "C" in underline font
Show "LS5 row in underline font

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

Also modify the editing instruction by changing the "insert" to a "change" as an insert does not require underlining.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 45.2.3</th>
<th>Page 115</th>
<th>Line 21</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
<td>D</td>
<td>In Table 45-83 before the 802.3az changes we have a row: 3.16 through 3.23 Reserved In the added rows you have: 3.21 Reserved You should therefore show the row for 3.16 through 3.23 as modified to be: 3.16 through 3.19 Reserved</td>
<td>Suggested Remedy</td>
<td>Show the row for 3.16 through 3.23 as modified to be: 3.16 through 3.19 Reserved</td>
<td>PROPOSED ACCEPT IN PRINCIPLE</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 45.2.3.1</th>
<th>Page 116</th>
<th>Line 10</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>T</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>T</td>
<td>Comment Status</td>
<td>D</td>
<td>In Table 45-84 the name for bit 3.0.10 is &quot;Clock stop enable&quot;. However in 45.2.3.1.3a the name is given as &quot;Clock stoppable&quot;. Making these names different is a source of confusion.</td>
<td>Suggested Remedy</td>
<td>change the names so that they are the same.</td>
<td>PROPOSED ACCEPT IN PRINCIPLE</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 74.4.1</th>
<th>Page 215</th>
<th>Line 46</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
<td>D</td>
<td>In title of Figure 74-2 &quot;diagra&quot; should be &quot;diagram&quot;</td>
<td>Suggested Remedy</td>
<td>Change &quot;diagra&quot; to &quot;diagram&quot;</td>
<td>PROPOSED ACCEPT</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 78.2</th>
<th>Page 228</th>
<th>Line 34</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
<td>D</td>
<td>comment 12 against Draft 2.0 has not been fully implemented</td>
<td>Suggested Remedy</td>
<td>In Table 78-2 change greek letter mu followed by &quot;sec&quot; to greek letter mu followed by &quot;s&quot; in 3 places</td>
<td>PROPOSED ACCEPT</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 78.2</th>
<th>Page 228</th>
<th>Line 34</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
<td>D</td>
<td>comment 12 against Draft 2.0 has not been fully implemented</td>
<td>Suggested Remedy</td>
<td>In Table 78-2 change greek letter mu followed by &quot;sec&quot; to greek letter mu followed by &quot;s&quot; in 3 places</td>
<td>PROPOSED ACCEPT</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 78.2</th>
<th>Page 228</th>
<th>Line 34</th>
<th>Comment Status</th>
<th>Comment Type</th>
<th>Proposed Response</th>
<th>Anslow, Peter</th>
<th>Nortel Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
<td>D</td>
<td>comment 12 against Draft 2.0 has not been fully implemented</td>
<td>Suggested Remedy</td>
<td>In Table 78-2 change greek letter mu followed by &quot;sec&quot; to greek letter mu followed by &quot;s&quot; in 3 places</td>
<td>PROPOSED ACCEPT</td>
<td></td>
</tr>
</tbody>
</table>
Proposed responses

Cl 79 SC 79 P239 L 1 # 12
Anslow, Peter Nortel Networks

Comment Type E Comment Status D
The format of the clause title for clause 79 is still incorrect. As pointed out in comment 14 against draft 2.0 there should be a "." after the "79"

Suggested Remedy
change "79 IEEE" to "79. IEEE"

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 79 SC 79.3.a P240 L 1 # 13
Anslow, Peter Nortel Networks

Comment Type E Comment Status D
The response to comment 15 against draft 2.0 has not been implemented. The heading numbers are still incorrect

Suggested Remedy
Change from
79.3.a
79.3.0.1
79.3.0.2
79.3.0.3
79.3.0.4
to
79.3.a
79.3.a.1
79.3.a.2
79.3.a.3
79.3.a.4

Proposed Response Response Status W
PROPOSED ACCEPT.

Will check this editorially at all steps of producing the next version draft.

Cl 36 SC 36-7 P81 L # 14
Sela, Oren Mellanox

Comment Type T Comment Status D
In 36-7a there is a missing exit condition for LPI_K - SUDI([\D21.5\] + [\D2.2\])

Suggested Remedy
Add and arch from LPI_K to RX_CB (C) when SUDI([\D21.5\] + [\D2.2\])

Proposed Response Response Status W
PROPOSED REJECT.

There is no support for configuration updates during LPI.

Cl 49 SC 49.2.6 P148 L 25 # 15
Mark, Gustlin Cisco

Comment Type T Comment Status D
"Change 49.2.6 for scrambler reset" is out of date, should be bypass.

Suggested Remedy
Change to:
"Change 49.2.6 for scrambler bypass"

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 49 SC 49.2.6 P149 L 1 # 16
Mark, Gustlin Cisco

Comment Type T Comment Status D
I believe this statement should be deleted:
"To aid block synchronization in the receiver when the optional LPI function is supported, the registers of scrambler shall be held at logic zero while scrambler_reset is TRUE."

Suggested Remedy

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

See comment #239

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID
<table>
<thead>
<tr>
<th>Cl</th>
<th>SC</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
<th>Response Status</th>
<th>Suggested Remedy</th>
</tr>
</thead>
<tbody>
<tr>
<td>49</td>
<td>49.2.9</td>
<td>T</td>
<td>D</td>
<td>This statement says the scrambler will be bypassed to aid synchronization, but I think this is only needed if FEC is enabled, state this condition.</td>
<td></td>
<td>Clarify the statement that this only applies if FEC is used.</td>
</tr>
<tr>
<td>49</td>
<td>70.6.10</td>
<td>E</td>
<td>D</td>
<td>'responds' should not be underlined</td>
<td></td>
<td>as above.</td>
</tr>
<tr>
<td>49</td>
<td>70.6.5</td>
<td>E</td>
<td>D</td>
<td>optional should not be underlined as it is in the base document. Same problem in 70.6.4 on line 4.</td>
<td></td>
<td>as above.</td>
</tr>
</tbody>
</table>

Proposed Response: **Proposed Accept IN PRINCIPLE.**

Mark, Gustlin Cisco

**Comment Type**: TR/technical required  ER/editorial required  GR/general required  T/technical  E/editorial  G/general

**Comment Status**: D/dispatched  A/accepted  R/rejected  E/open  W/written  C/closed  U/unsatisfied Z/withdrawn

**Sort Order**: Comment ID

**Page**: 4 of 75  **Date**: 11/13/2009  4:45:43 AM
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Comment Status</th>
<th>Response Status</th>
<th>Suggested Remedy</th>
<th>Proposed Response</th>
<th>Comment ID</th>
<th>Type</th>
<th>Comment Status</th>
<th>Response Status</th>
<th>Suggested Remedy</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>23</td>
<td>E</td>
<td>D</td>
<td>W</td>
<td>Change .FEC</td>
<td>PROPOSED ACCEPT.</td>
<td>24</td>
<td>T</td>
<td>D</td>
<td>W</td>
<td>&quot;LP_IDLE.request shall remain to be set to DEASSERT for 1 second following link_status changing state to OK&quot; reads awkwardly.</td>
<td>LPI_IDLE.request shall not be set to ASSERT unless the attached link has been operational for one second (i.e. link_status = OK, according to the underlying PCS/PMA).</td>
</tr>
</tbody>
</table>
### Proposed responses

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC</th>
<th>P</th>
<th>L</th>
<th>Comment</th>
<th>Status</th>
<th>Suggested Remedy</th>
<th>Proposed Response</th>
<th>Response Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>27</td>
<td>74</td>
<td>216</td>
<td>37</td>
<td>TR</td>
<td>D</td>
<td>74.5.4 should really be 74.5.1.4</td>
<td>74.5.5 should really be 74.5.1.5</td>
<td>Marris, Arthur</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>74.5.6 should really be 74.5.1.6</td>
<td>74.5.7 should really be 74.5.1.7</td>
<td>Cadence</td>
</tr>
<tr>
<td>28</td>
<td>00</td>
<td>4</td>
<td>13</td>
<td>E</td>
<td>D</td>
<td>IEEE Std 802.3av-2009 was approved, which means that the TM should be used as well.</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Insert &quot;IEEE Std 802.3av-2009&quot; to &quot;IEEE Std 802.3avTM-2009&quot;. Scrub the text for any other missing &quot;TM&quot; marks.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>29</td>
<td>14</td>
<td>14.8</td>
<td>23</td>
<td>E</td>
<td>D</td>
<td>&quot;Which of the two specifications is implemented, i.e. '10BASE-T or 10BASE-Te (not both).&quot;</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Change &quot;i.e. '10BASE-T or 10BASE-Te (not both).&quot; to &quot;i.e. either 10BASE-T or 10BASE-Te.&quot;</td>
<td></td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>14</td>
<td>14.10.3</td>
<td>24</td>
<td>E</td>
<td>D</td>
<td>&quot;The mapping is changed if EEE capability is supported, this is described in 22.7a.&quot; - suggest to reword to read &quot;The mapping is changed if EEE capability is supported, as described in 22.7a.&quot;</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Split PICS item into two separate lines, one for 10BASE-T and the second for 10BASE-Te</td>
<td></td>
<td></td>
</tr>
<tr>
<td>31</td>
<td>22</td>
<td>14.3.1.2</td>
<td>22</td>
<td>T</td>
<td>T</td>
<td>I think the purpose of this PICS item is to identify the MAU type included in the given PHY. Wouldn't it make more sense to have a separate row / entry for 10BASE-T and 10BASE-Te, so that someone reading this PICS can identify without any doubts immediately what type of MAU is used?</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Split PICS item into two separate lines, one for 10BASE-T and the second for 10BASE-Te</td>
<td></td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>14</td>
<td>22.2.1</td>
<td>22</td>
<td>E</td>
<td>D</td>
<td>&quot;Table 14-1-Voltage template values for Figure 14-9 (continued)&lt;Default ¬¹ Font&gt;&quot; contains some garbage. Remove &quot;&lt;Default ¬¹ Font&gt;&quot;??</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Duplicates of comment #2</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Comment Status:** D/dispatched A/accepted R/rejected **Response Status:** O/open W/written C/closed U/unsatisfied Z/withdrawn **Sort Order:** Comment ID

---

**TYPE:** TR/technical required ER/editorial required GR/general required **COMMENT STATUS:** D/dispatched A/accepted R/rejected **RESPONSE STATUS:** O/open W/written C/closed U/unsatisfied Z/withdrawn **SORT ORDER:** Comment ID

---

Page 6 of 75 11/13/2009 4:45:43 AM
Proposed responses

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Comment Status</th>
<th>Comment</th>
<th>Proposed Response</th>
<th>Response Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>33</td>
<td>E</td>
<td>D</td>
<td>&quot;diagram (see fig 22-21). The signal&quot; should read &quot;diagram (see Figure 22-21). The signal&quot; Marek sure that the link is live</td>
<td>PROPOSED ACCEPT IN PRINCIPLE.</td>
<td>W</td>
</tr>
<tr>
<td>34</td>
<td>T</td>
<td>D</td>
<td>&quot;when Clock stop enable is asserted&quot; - should read &quot;when the Clock stop enable bit is asserted&quot;</td>
<td>PROPOSED ACCEPT.</td>
<td>W</td>
</tr>
<tr>
<td>35</td>
<td>T</td>
<td>D</td>
<td>&quot;For EEE capability, the PHY indicates that it is receiving low power idle by asserting&quot; &gt; I thought all occurrences of &quot;low power idle&quot; were to be replaced with &quot;LPI&quot; which is already defined in the initial section of this draft?</td>
<td>PROPOSED ACCEPT IN PRINCIPLE.</td>
<td>W</td>
</tr>
<tr>
<td>36</td>
<td>E</td>
<td>D</td>
<td>(1) &quot;and to the link partner that a break in the data stream is expected&quot; - break has usually negative connotation. Use &quot;interruption&quot; or something in the lines. (2) Missing space in line 8, page 30 in &quot;specified only for 100&lt;&lt;HERE SHOULD BE A SPACE&gt;&gt;Mb/s operation&quot; (3) text under Figure 22-20a is strangely indented - fix it please.</td>
<td>PROPOSED ACCEPT.</td>
<td>W</td>
</tr>
<tr>
<td>37</td>
<td>T</td>
<td>D</td>
<td>What is &quot;The LPI_REQUEST parameter&quot;? Do you mean &quot;The LP_IDLE.request parameter&quot;?? Please clarify. The same in line 5, page 31. Figure 22-21 seems to indicate that LP_IDLE.request is meant here Similar comment applies to clause 46.4a.1.</td>
<td>PROPOSED ACCEPT IN PRINCIPLE.</td>
<td>W</td>
</tr>
</tbody>
</table>

Hajduczenia, Marek
ZTE Corporation
Proposed responses

IEEE P802.3az D2.1 Energy Efficient Ethernet comments

Proposed Response

Cl 22 SC 22.7a.2.2 P 31 L 26 # 38
Hajduczenia, Marek ZTE Corporation

Comment Type T Comment Status D

"Condition that is true until such time as the power supply for the device that contains the RS has reached the operating region." - what is this "operating region"? Do you mean "operating condition"?

Suggested Remedy

Please clarify per comment

Proposed Response Response Status W
PROPOSED REJECT.

This wording matches the definition used in Clause 46 (in 802.3-2008).

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

Cl 24 SC 24.2.2.5 P 39 L 21 # 39
Hajduczenia, Marek ZTE Corporation

Comment Type T Comment Status D

"PCS returns to the normal state when it detects the termination of an LPI command." - so it exits the LPI mode when it detects that the LPI asset is no longer active or when it detects that the LPI deassert was activated? In the latter case, the text should read "PCS returns to the normal state when it detects an LPI termination command."

Suggested Remedy

Clarify please.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

Please see the response to comment #144.

Cl 24 SC 24.2.2.5 P 39 L 45 # 41
Hajduczenia, Marek ZTE Corporation

Comment Type E Comment Status D

(1) "Tq before a Refresh or Wake state appears" - a state does not appear, it occurs.
(2) line 47, same page: "transmitted for default or negotiated amount of time denoted by Tw" > "transmitted for <<a>> default or negotiated amount of time denoted by Tw"
(3) line 51, same page: "to notify the upper layer the change of operation mode" > "to notify the upper layer <<about/on>> the change of operation mode"

Suggested Remedy

Per comment

Proposed Response Response Status W
PROPOSED ACCEPT.

Please see the response to comment #144.
Upon successfully receiving SLEEP code-groups, the 100BASE-X PCS enters the LPI mode - my idea was that only 100BASE-TX supports (page 34, point g) LPI. So why refer to generic 100BASE-X PCS type?

Clarify per comment

PROPOSED ACCEPT IN PRINCIPLE.

Please see the respons to comment #144.

The 100BASE-X is changed to 100BASE-TX.

(1) "as depicted in Figure 24-11b" - link is not live
(2) line 11: "The following constants are required only for the optional EEE capability" > "The following constants are required to support the optional EEE capability. Similar changes in line 29, page 40 and line 17, page 41.
(3) line 13: "The SLEEP code-group (/P/) used for LPI state delineator, as specified in 24.2.2.1" > "The SLEEP code-group (/P/) used <<by the>> LPI state delineator, as specified in 24.2.2.1"

For item (2), do we need to change all instances of "required only for" to "required to support" throughout the draft?

Some of the timers have a range of value which is acceptable. Who / What decides what the final value should be, how is such selection done and does that affect interoperability between devices i.e. what happens if the receiving side expect the maximum value nad the transmitter uses the minium value. Does this break operation of an EEE enabled link?

Please clarify questions in the comment.

PROPOSED ACCEPT IN PRINCIPLE.

Please see the response to comment #144.

There are new paragraphs explaining the meaning and the use of the value of timers.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Sc</th>
<th>Page</th>
<th>Line</th>
<th>Type</th>
<th>Comment Status</th>
<th>Recommended Action</th>
</tr>
</thead>
<tbody>
<tr>
<td>46</td>
<td>24.3.1.8</td>
<td>46</td>
<td>15</td>
<td>T</td>
<td>D</td>
<td>Change all sentences in the following places: P.46, L.15, P.46, L.36, P.50, L.31, P.50, L.51</td>
</tr>
<tr>
<td>47</td>
<td>24.3.1.8.1</td>
<td>50</td>
<td>18</td>
<td>T</td>
<td>D</td>
<td>Change the text in the following places: P.50, L.38 to</td>
</tr>
<tr>
<td>48</td>
<td>24.4.1</td>
<td>50</td>
<td>18</td>
<td>T</td>
<td>D</td>
<td>Change the text in the following places: P.50, L.38 to</td>
</tr>
</tbody>
</table>

Hajduczenia, Marek
ZTE Corporation

**Comment Type**: T (technical)
**Comment Status**: D (dispatched)

**Suggested Remedy**
- Change all sentences with ".generated . only for the EEE capability..." to ".generated . only if the EEE is supported..."
- Change total four places in the draft.
- Change the text in P.46,L.23,24 to "The lpi_link_fail parameter takes on one of two values: TRUE or FALSE, indicating whether a link failure condition has been set (TRUE) or not (FALSE)."
- Change the text in P.46,L.43 to "The rx_lpi parameter takes on one of two values: TRUE or FALSE, indicating whether the receiver is in LPI mode (TRUE) or not (FALSE)."
- Change the text in P.50,L.38 to "The rx_quiet parameter takes on one of two values: TRUE or FALSE, indicating whether the receiver is in Quiet state (TRUE) or not (FALSE)."
- Change the text in P.51,L.5 to "The tx_quiet parameter takes on one of two values: TRUE or FALSE, indicating whether the transmitter is in Quiet state (TRUE) or not (FALSE)."
Proposed response

#46

Cl 24 SC 24.4.1.4 P 50 L 31

Hajduczenia, Marek
ZTE Corporation

Comment Type T Comment Status D

Again, language "This primitive is generated by the Receive Process of PCS only for the EEE capability" should read "This primitive is generated by the Receive Process of PCS if the EEE capability is supported"

Similar comment against line 51, same page.

Suggested Remedy
Per comment

PROPOSED ACCEPT IN PRINCIPLE.

See the response of comment #46

#50

Cl 25 SC 25.4.11.1.1 P 55 L 30

Hajduczenia, Marek
ZTE Corporation

Comment Type T Comment Status D

"the NRZ bit" or "the nrz bit" - which is it then?

Suggested Remedy
which is the correct capitalization?

PROPOSED ACCEPT IN PRINCIPLE.

The term "driver" is used in the TP-PMD original text. It is the last part of the Transmit functional blocks. Apparently, the driver means to drive the TP cable.

The text in this draft already points to the source of reference:
"to the driver (see TP-PMD 7.1.3)."

The TP-PMD 7.1.3 has a single word title "Driver". The term "driver" (lower case) is used throughout the document of ANSI+X3.263-1995.pdf.

PROPOSED ACCEPT IN PRINCIPLE.

NRZ is an official acronym defined and used throughout the ANSI+X3.263-1995.pdf. It is also used in the original text of Clause 24 and 25.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 30.12.1.22</th>
<th>P62</th>
<th>L19</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>52</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;LocTxSystemValue as defined in 78.4.2.3.&quot; - link is not live. Similar comment in line 33, same page. Similar comment in line 44, same page. Similar comment in line 4, page 63. Similar comment in line 16, page 63. Similar comment in line 26, page 64. Similar comment in line 40, page 64. Similar comment in line 51, page 64. Similar comment in line 13, page 65. Similar comment in line 25, page 65.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 35.2.2.4</th>
<th>P67</th>
<th>L2</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>54</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;The use of TXD&lt;7:0&gt; to signal LPI transitions is described in 35.2.2.6a&quot; - missing &quot;.&quot;, at the end.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 35.2.2.4</th>
<th>P67</th>
<th>L</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>55</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;For EEE capability, the RS shall use the combination of TX_EN de-asserted, TX_ER asserted and TXD&lt;7:0&gt; equal to 0x01 shown in Table 35-1 as a request to enter, or remain in low power idle&quot; should read &quot;For EEE capability, the RS shall use the combination of TX_EN de-asserted, TX_ER asserted and TXD&lt;7:0&gt; equal to 0x01&lt;&lt;, as&gt;&gt; shown in Table 35-1 as a request to enter, or remain &lt;&lt;in the LPI mode.&gt;&gt;&quot;</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 35.2.2.9a</th>
<th>P70</th>
<th>L33</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>56</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (figure 35-9a) if and only if the Clock stop enable bit is asserted (45.2.3.1.3a).&quot; should read &quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (&lt;&lt;Figure 35-9a&gt;&gt;) if and only if the Clock stop enable bit is asserted (&lt;&lt;see 45.2.3.1.3a&gt;&gt;).&quot;</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 35.2.2.9a</th>
<th>P70</th>
<th>L</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>57</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (figure 35-9a) if and only if the Clock stop enable bit is asserted (45.2.3.1.3a).&quot; should read &quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (&lt;&lt;Figure 35-9a&gt;&gt;) if and only if the Clock stop enable bit is asserted (&lt;&lt;see 45.2.3.1.3a&gt;&gt;).&quot;</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>SC 35.2.2.9a</th>
<th>P70</th>
<th>L</th>
<th>#</th>
<th>Proposed Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>58</td>
<td>Hajduczenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td>&quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (figure 35-9a) if and only if the Clock stop enable bit is asserted (45.2.3.1.3a).&quot; should read &quot;While the PHY device is indicating LPI the PHY device may halt the RX_CLK as shown in (&lt;&lt;Figure 35-9a&gt;&gt;) if and only if the Clock stop enable bit is asserted (&lt;&lt;see 45.2.3.1.3a&gt;&gt;).&quot;</td>
</tr>
</tbody>
</table>
Proposed responses

IEEE P802.3az D2.1 Energy Efficient Ethernet comments

November 2009

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Cl</th>
<th>SC</th>
<th>Page</th>
<th>Line</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment</th>
<th>Suggested Remedy</th>
<th>Proposed Response</th>
<th>Response Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>57</td>
<td>36</td>
<td>36.2.4.7</td>
<td>75</td>
<td>12</td>
<td>E</td>
<td>D</td>
<td>&quot;The ability to transmit or receive /LI/, /LI1/ and /LI2/ is an option for certain PHYs to support Energy Efficient Ethernet (see Clause 78).&quot; there is a line break in /LI1/ is a kind of awkward</td>
<td>Per comment</td>
<td>PROPOSED ACCEPT.</td>
<td></td>
</tr>
<tr>
<td>58</td>
<td>36</td>
<td>36.2.4.12a</td>
<td>75</td>
<td>49</td>
<td>T</td>
<td>D</td>
<td>&quot;For the EEE capability this variable is affected by the LPI receive state diagram. Without the EEE capability this variable is identical to code_sync_status controlled by the synchronization state diagram&quot; should read &quot;If EEE is supported, this variable is affected by the LPI receive state diagram. If EEE is not supported, this variable is identical to code_sync_status controlled by the synchronization state diagram&quot;</td>
<td>Per comment</td>
<td>PROPOSED ACCEPT.</td>
<td></td>
</tr>
<tr>
<td>59</td>
<td>36</td>
<td>36.2.5.1.2</td>
<td>76</td>
<td>3</td>
<td>T</td>
<td>D</td>
<td>&quot;The following constant is used only for the EEE capability.&quot; there are several entries which say &quot;... for the EEE capability.&quot; - suggest to reword that to read &quot;... if the EEE capability is supported.&quot; Scrub the draft, including subsections of 36.2.5.1</td>
<td>Per comment</td>
<td>PROPOSED REJECT.</td>
<td></td>
</tr>
</tbody>
</table>

This wording was agreed during the comment resolution for D2.0.

TYPE: TR/technical required  ER/editorial required  GR/general required  T/technical  E/editorial  G/general
COMMENT STATUS: D/dispatched  A/accepted  R/rejected  RESPONSE STATUS: O/open  W/written  C/closed  U/unsatisfied  Z/withdrawn
SORT ORDER: Comment ID
<table>
<thead>
<tr>
<th>Comment ID #</th>
<th>Proposal Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>62</td>
<td><em>is in progress hence 1000BTtransmit (see 40.3.3.1) will also be FALSE</em> should be reworded to <em>is in progress hence the variable 1000BTtransmit (see 40.3.3.1) will also be set to FALSE</em></td>
</tr>
<tr>
<td>63</td>
<td>(1) different font sizes for e.g. &quot;SEND_I&quot; (2) text in some boxes is misaligned within the boxes e.g. &quot;DISABLE 1000BASE-T TRANSMITTER&quot; and others</td>
</tr>
<tr>
<td>64</td>
<td>&quot;40.6.1.2.7 Transmitter operation during WAKE&quot; should read &quot;40.6.1.2.7 Transmitter operation during the WAKE state&quot;</td>
</tr>
<tr>
<td>65</td>
<td>Not entirely sure why the value/comment field in PCT18 and PCT19 needs to have 'shall' statements in them. The same comment against item PCR5 and PMF24 through PMF37. The same comment against item PME71 through PME77. The same comment against item AN15.</td>
</tr>
</tbody>
</table>

**Proposed Response**

- **Response Status:** W

PROPOSED ACCEPT IN PRINCIPLE.

It may also be helpful to clarify how the 1000BTtransmit is set to FALSE.

Change text to:

"hence 1000BTtransmit (see 40.3.3.1) will be set FALSE by the PCS Transmit state diagram."

- **Response Status:** W

PROPOSED ACCEPT IN PRINCIPLE.

These issues exist in the base document. However, since the state diagram is being modified by this amendment, the editor will correct the font size and text alignment issues.

**Proposed Response**

- **Response Status:** W

PROPOSED ACCEPT IN PRINCIPLE.

In the base document, PICS do incorporate the keyword "shall" in the "Feature" and/or "Value/Comment" fields. Clause 40 does this to excess. While this may appear to be unusual, there is no rule (to the editor's knowledge) that prohibits it.

However, there is a difference in the style of the EEE-related PICS and the PICS in Clause 40 of the base document. For better or worse, it preferred to consistent with the base document style.

Update the PICS to be consistent with the style of existing Clause 40 PICS.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Cl</th>
<th>SC</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
<th>Response Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>45</td>
<td>45</td>
<td>45.2.3.1.3a</td>
<td>T</td>
<td>D</td>
<td>There are still occurrences of &quot;low power idle&quot; which have not been replaced with LPI as defined at the initial section of the draft. Scrub the draft accordingly.</td>
<td></td>
</tr>
<tr>
<td>66</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Per comment.</td>
</tr>
<tr>
<td>66</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>PROPOSED ACCEPT IN PRINCIPLE.</td>
<td>W</td>
</tr>
<tr>
<td>66</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Change line 21 to LPI.</td>
<td></td>
</tr>
<tr>
<td>66</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Also page 117, line 29</td>
<td></td>
</tr>
<tr>
<td>67</td>
<td>45</td>
<td>45.2.3.1.3a</td>
<td>E</td>
<td>D</td>
<td>&quot;see 22.2.2.9a, 35.2.2.9a, 46.3.2.4a&quot; should read</td>
<td></td>
</tr>
<tr>
<td>67</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>&quot;see 22.2.2.9a, 35.2.2.9a, and 46.3.2.4a&quot;</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Similar on page 117, line 31</td>
<td></td>
</tr>
<tr>
<td>67</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>&quot;see 22.2.2.9a, 35.2.2.9a, 46.3.2.4a&quot; should read</td>
<td></td>
</tr>
<tr>
<td>67</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>&quot;see 22.2.2.9a, 35.2.2.9a, and 46.3.2.4a&quot;</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SuggestedRemedy</td>
<td>Per comment.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Response</td>
<td>Response Status</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>PROPOSED ACCEPT.</td>
<td></td>
</tr>
<tr>
<td>68</td>
<td>45</td>
<td>45.2.3.2.2a</td>
<td>T</td>
<td>D</td>
<td>&quot;If bit 3.1.6 is set to 1&quot; in some instances, you write &quot;set to 1/0&quot; etc. In other instances, you write &quot;set to a zero/a one&quot;. Pick one nomenclature and use consistently, unless there is anything in the IEEE style guidelines to define what style should be used.</td>
<td></td>
</tr>
<tr>
<td>69</td>
<td>45</td>
<td>45.2.3.9a.1</td>
<td>E</td>
<td>D</td>
<td>&quot;If the device supports EEE operation for 10GBASE-KR as defined in 72.1 this bit shall be set to 1.&quot; is missing a comma before &quot;this bit ...&quot;. Similar in lines 37, 41, 45, 49, 53 on the same page</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SuggestedRemedy</td>
<td>Per comment.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Response</td>
<td>Response Status</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>PROPOSED ACCEPT.</td>
<td></td>
</tr>
<tr>
<td>70</td>
<td>45</td>
<td>45.2.7.14a</td>
<td>E</td>
<td>D</td>
<td>&quot;All of the bits in the EEE LP advertisement register are read only.&quot; should read &quot;All of the bits in the EEE LP advertisement register are &lt;&lt;read-only&gt;&gt;.&quot;</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SuggestedRemedy</td>
<td>Per comment.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Response</td>
<td>Response Status</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>PROPOSED ACCEPT.</td>
<td></td>
</tr>
</tbody>
</table>
Proposed responses

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Cl.</th>
<th>SC.</th>
<th>Page</th>
<th>Line</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Comment</th>
<th>Proposed Response</th>
<th>Response Status</th>
<th>Comment ID</th>
<th>Cl.</th>
<th>SC.</th>
<th>Page</th>
<th>Line</th>
</tr>
</thead>
<tbody>
<tr>
<td>71</td>
<td>46</td>
<td>46.1.7</td>
<td>125</td>
<td>17</td>
<td>E</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td>46</td>
<td>46.3.1.5a</td>
<td>P 126</td>
<td>22</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Hajduszenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>Comment Status</td>
<td>Comment Type</td>
<td>Comment Status</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(1) &quot;mapping changes slightly when LPI signaling is in operation&quot; - how much is slightly? Either it changes or not. Remove &quot;slightly&quot;</td>
<td>D</td>
<td>(1) &quot;LPI state by asserting TXC and setting TXD to 06 (in all lanes).&quot; - that value 06 is decimal, hexadecimal or in some other encoding. Similar comment to 46.3.2.4a, line 20, page 127</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(2) &quot;LPI_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link_status = OK, according to the underlying PCS/PMA). LP_IDLE.request shall remain to be set to DEASSERT for 1 second following link_status changing state to OK.&quot;- this block of text is written in smaller font than the rest of the paragraph</td>
<td>E</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SuggestedRemedy</td>
<td>Proposed Response</td>
<td>Response Status</td>
<td>W</td>
<td>PROPOSED ACCEPT IN PRINCIPLE.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(1) &quot;The mapping is changed if EEE capability is supported.&quot;</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(2) fix font size.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>72</td>
<td>46</td>
<td>46.3</td>
<td>125</td>
<td>45</td>
<td>E</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td>48</td>
<td>48.2.6.1.3</td>
<td>P 136</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Hajduszenia, Marek</td>
<td>ZTE Corporation</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>Comment Status</td>
<td>Comment Type</td>
<td>Comment Status</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>&quot;RX_CLK may be halted according to 46.3.2.4a&quot; is written in larger font than the rest of the paragraph.</td>
<td>D</td>
<td>&quot;For EEE capability, this variable is affected by the LPI receive state diagram. Without EEE capability this variable is identical to deskew_align_status controlled by the deskew state diagram&quot;</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SuggestedRemedy</td>
<td>Proposed Response</td>
<td>Response Status</td>
<td>W</td>
<td>PROPOSED ACCEPT.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Proposed Response</td>
<td>Per comment</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Proposed Response</td>
<td>Response Status</td>
<td>W</td>
<td>PROPOSED REJECT.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Proposed Response</td>
<td>Per comment</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The "capability" wording was agreed after very long discussions during comment resolution for D2.0.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment ID</th>
<th>Comment ID</th>
</tr>
</thead>
<tbody>
<tr>
<td>75</td>
<td>76</td>
<td>77</td>
</tr>
</tbody>
</table>

**Cl 48 SC 48.2.4.2 P 134**

Hajduczenia, Marek
ZTE Corporation

**Comment Type T**

**Comment Status D**

Personally, I think "||LPIDLE||" should be "||LPI_IDLE||", which is what it is i.e. it is an LPI IDLE. Do not remove that extra I from within the acronym.

**Suggested Remedy**

Suggest a change per comment. Scrub draft as needed.

**Proposed Response Response Status W**

PROPOSED REJECT.

LPI stands for Low Power Idle, therefore a second "I" would be unnecessarily redundant.

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment ID</th>
<th>Comment ID</th>
</tr>
</thead>
<tbody>
<tr>
<td>76</td>
<td>77</td>
<td>80</td>
</tr>
</tbody>
</table>

**Cl 48 SC 48.2.6.1.2 P 135**

Hajduczenia, Marek
ZTE Corporation

**Comment Type E**

**Comment Status D**

"...specified in 48.2.4.2.3" and "For EEE capability".

**Suggested Remedy**

Per comment

**Proposed Response Response Status W**

PROPOSED ACCEPT.

**Cl 48 SC 48.2.6.2.5 P 141**

Hajduczenia, Marek
ZTE Corporation

**Comment Type E**

**Comment Status D**

"when true. The receive LPI" - sometimes you capitalize true, sometimes you do not. Which is it? It does not seem to be consistent even within a single clause.

**Suggested Remedy**

Per comment

**Proposed Response Response Status W**

PROPOSED ACCEPT IN PRINCIPLE.

"There is only one instance of "true" - change to "TRUE"
Proposed Response

Cl 78 SC 78.1 P 222 L 15 # 81
Hajduczenia, Marek ZTE Corporation

Comment Type T
Comment Status D

(1) I thought that MAC was not operated at any specific data rate. I suggest to drop "EEE supports the IEEE 802.3 MAC operation at 100 Mb/s, 1000 Mb/s, and 10 Gb/s." EEE should not care about what data rate the MAC is operating it, since it does not use MAC directly in any way. EEE does not extend MAC in any specific way.

(2) Change sentence "For operation over twisted pair cabling systems, the PHY's supported are 100BASE-TX, 1000BASE-T and 10GBASE-T. For operation over electrical backplanes, the PHY's supported are 1000BASE-KX, 10GBASE-KX4 and 10GBASE-KR." to read: "For operation over twisted pair cabling systems, EEE supports the following PHYs: 100BASE-TX, 1000BASE-T and 10GBASE-T. For operation over electrical backplanes, EEE supports the following PHYs: 1000BASE-KX, 10GBASE-KX4 and 10GBASE-KR."

Suggested Remedy

Per comment.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.
Re (1), as per comment.

Re (2) Change sentence:
"For operation over twisted pair cabling systems, the PHY's supported are 100BASE-TX, 1000BASE-T and 10GBASE-T. For operation over electrical backplanes, the PHY's supported are 1000BASE-KX, 10GBASE-KX4 and 10GBASE-KR." to read:
"For operation over twisted pair cabling systems, EEE supports the 100BASE-TX PHY, the 1000BASE-T PHY and the 10GBASE-T PHY. For operation over electrical backplanes, EEE supports the 1000BASE-KX PHY, the 10GBASE-KX4 PHY and the 10GBASE-KR PHY."

Suggested Remedy

Per comment.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

"xMII interface in this diagram represents any of the family of medium independent interfaces supported by EEE" and which are those in particular? Since there is already such an introduction, you are invited to provide details what types of xMII are supported. IMHO it would improve transparency of the description.

Suggested Remedy

Per comment.

Proposed Response Response Status W

PROPOSED REJECT.

It is not clear to the editor that listing the xMII interfaces adds any significant clarity.

Proposed Response Response Status W

PROPOSED ACCEPT.

"After a delay the LPI" - what delay? I think this delay is parametrized in the text of the clause, so it should be either spelled out what the value is or what it depends on. A reference to 78.4 should be made much sooner.
Also missing comma after "After a delay"

Suggested Remedy

Per comment.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

The comma will be added as suggested. No other change will be made as there is a reference to 78.4 already in the last sentence of the paragraph.
"At the start of 'assert LPI' encoding on the xMII, the PHY signals sleep"
should read
"When the start of 'assert LPI' encoding on the xMII is detected, the PHY signals"

I am not sure what 'signal sleep' really means. Is it a special code-group or something elsealtogether? The sentence reads just fine without it. This term 'sleep' is also used infailing sentences without ever defining what this is and what it is used for. Please remove it consistently or define altogether what this 'sleep' is, how it is transmitted etc. Otherwise it seems like a poor description of transmission of LPI encoding onto the other side of the link.

Suggested Remedy
Per comment

PROPOSED ACCEPT IN PRINCIPLE.

Change:
"At the start of 'assert LPI' encoding on the xMII, the PHY signals sleep"
to:
"When the start of 'assert LPI' encoding on the xMII is detected, the PHY signals"

Also see response to comment #87

"...the transmit function of the local PHY enters a quiet mode..."
with:
"...the local PHY transmitter goes quiet...

Depending on the PHY, LPI mode can involve a repeating sequence of sleep and refresh states.

Suggested Remedy
Per comment

PROPOSED ACCEPT IN PRINCIPLE.

"quiet mode" is used in two places - Page 226 lines 19 and 32.

Replace:
"...the transmit function of the local PHY enters a quiet mode..."
on Page 226, lines 29 and 32 and any other place.

"quiet" refers to the state of a transmitter.

"The PHY then enters the normal operating state where data is transmitted or IDLEs are transmitted" why do we need to mention what is transmitted in a normal state? Just change that sentence to read "The PHY then enters the normal operating state."

Suggested Remedy
Per comment

PROPOSED ACCEPT.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Cl</th>
<th>SC</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
</tr>
</thead>
</table>
| 90         | 78 | 78.1.3.3.1 | E  | D             | Change "Figure 78-3 illustrates general principles of the EEE-capable transmitter operation." to read "Figure 78-3 illustrates a general operating principle of an EEE-capable transmitter."
|            | 78 | 78.1.4    | T  | D             | Change caption of Figure 78-3 to read "EEE operating cycle: active state - LPI mode - active state" |
|            | 78 | 78.1.3.2  | TR | D            | "assert LPI" on the xMII and the local receiver can disable some functionality to reduce power consumption" - change "some functionality" to "certain functional blocks" - this seems more precise. |
|            | 78 | 78.1.3.3.1 | T  | D             | Change "assert LPI" on the xMII and the local receiver can disable some functionality to reduce power consumption" - change "some functionality" to "certain functional blocks" - this seems more precise. |
|            | 78 | 78.1.3.3.1 | TR | D            | What is this 'sleep signal'? Where is this defined? How is it transmitted? |
|            | 78 | 78.1.3.3.2 | TR | D            | Similar comment was submitted against previous version of the draft and yet there are no changes so far. |

Proposed Response  
PROPOSED ACCEPT.

Proposed Response  
PROPOSED ACCEPT.

Proposed Response  
PROPOSED ACCEPT.

Proposed Response  
PROPOSED REJECT.

Proposed Response  
PROPOSED REJECT.

Proposed Response  
PROPOSED REJECT.

The sleep signal is defined in individual PHY clauses.
The suggested remedy is not actionable
Also see response to comment #87

The description seems accurate as is. Stating that disabling functionality requires disabling of some functional blocks makes an assumption on implementation that is unnecessary.

"EEE defines a low power mode of operation for the following 802.3 PHYs. Table 78-1 lists the clauses associated with each PHY" change to read "EEE defines a low power mode of operation for the 802.3 PHYs listed in Table 78-1, together with clauses associated with each PHY."

"EEE defines a low power mode of operation for the 802.3 PHYs listed in Table 78-1. The table also lists the clauses associated with each PHY."
Proposed responses

Cl 78 SC 78.1.4 P227 L 35 # 94
Hajduczenia, Marek ZTE Corporation

Comment Type T Comment Status D
Change caption of Table 78-1 to "PHY types supporting EEE"

SuggestedRemedy
Per comment

Proposed Response Response Status W
PROPOSED REJECT.

The existing table title seems adequate.

Cl 78 SC 78.2 P228 L 31 # 95
Hajduczenia, Marek ZTE Corporation

Comment Type T Comment Status D
Table 78-2 contains some parameters with three trailing decimal zeros. Is this deliberate? Please remove any unnecessary trailing zeros.

SuggestedRemedy
Per comment

Proposed Response Response Status W
PROPOSED REJECT.

The three zeros are after a comma (which is used as a separator for thousands) and not after a decimal point

Cl 78 SC 78.4.2.3 P232 L 21 # 96
Hajduczenia, Marek ZTE Corporation

Comment Type E Comment Status D
"A summary cross-references between" > "A summary of cross-references between"

SuggestedRemedy
Per comment

Proposed Response Response Status W
PROPOSED ACCEPT.
There is a “shall” statement in LPI Link Fail condition without the associated PICS item.

Suggested Remedy
Insert a new PICS entry for LPI Link Fail with the following comment:
"If the PHY fails to receive a valid Refresh or Wake signal before lpi_rx_{tq} timer expires, the receiver shall assume a link failure."

PROPOSED ACCEPT.

The Transmit state diagram (Figure 24-8) has been modified. However, the text in the Transmit Process (subclause 24.2.4.2) does not have proper description explaining the modification of the function for EEE capability.

Suggested Remedy
Change the first paragraph in 24.2.4.2 as shown below.
Note: text enclosed by the square bracket [ ] are new.

The Transmit process sends code-groups to the PMA via tx_bytes and the Transmit Bits process. When initially invoked, and between streams (delimited by TX_EN on the MII), [except in the LPI mode for the optional EEE capability,] the Transmit process sources continuous Idle code-groups (/I/) to the PMA. Upon the assertion of TX_EN by the MII, the Transmit process passes an SSD (/J/K/) to the PMA, ignoring the TXD <3:0> nibbles during these two code-group times. Following the SSD, each TXD <3:0> nibble is encoded into a five-bit code-group until TX_EN is deasserted. If, while TX_EN is asserted, the TX_ER signal is asserted, the Transmit process passes Transmit Error code-groups (/H/) to the PMA. Following the de-assertion of TX_EN, an ESD (/T/R/) is generated, after which the transmission of Idle code-groups is resumed by the IDLE state.

[If EEE Capability is supported, when the Transmit process successfully aligns and decodes two consecutive SLEEP (/P/) code-groups, it enters the LPI mode and stays in LPI states until either the IDLE code-groups are received, where it leads the Receive process to the IDLE state, or a link failure condition in the LPI mode occurs, where it causes the Receive process to enter the RX_{LPI\_LINK\_FAIL} state and eventually move to the IDLE state.]
There is a corner case:

The transmitter may enter the TX_QUIET state very briefly, and return to the IDLE state anytime when it receives a De-assert LPI from MII. The duration of transmitter staying in the TX_QUIET state may be too short to effectively assert the Signal_detection of the receiver at the remote link partner. Therefore, the receiver Equalizer (EQ) and Clock Recovery logic (CR) may lose the track due to the period of “no-signal” in the received channel. As a result, the receiver may stay in the RX_SLEEP state unable to decode the symbols correctly, and eventually move to LPI_LINK_FAIL state when the lpi_rx_ts_timer is up.

This scenario is a mistake and needs to change.

However, the fix will affect the wake shrinkage time. To reduce the impact, it's preferable to decrease the signal_detection time.

**Suggested Remedy**

Modify the Transmit State Diagram (Fig 24-8):

Change the maximum Assert time and De-assert time of Signal_detection of PMD in LPI mode (refer to Table 25-3) to 1 microsecond

Add a new timer lpi_tx_tm_timer in TX_QUIET state with a value range between 1 to 1.5 microseconds, and start it when entering TX_QUIET state

Change the branch condition between TX_QUIET and IDLE from "sentCodeGroup.indicate ? (TX_EN = TRUE + TX_ER = FALSE + TXD[3:0] != TX_LP_IDLE)" to "sentCodeGroup.indicate ? lpi_tx_tm_timer_done * (TX_EN = TRUE + TX_ER = FALSE + TXD[3:0] != TX_LP_IDLE)"

Parameters are modified in the second row of Table 78-4 under the PHY type 100BASE-TX:

Tphy_shrink_tx = 6.5
Tphy_shrink_rx = 11
Tphy_wake_rx = 11.5
Tw_sys_rx = 12.5

A presentation will be made in the Nov. meeting.

**Proposed Response**

PROPOSED REJECT.

Accept the suggested remedy except the Parameters modified in the second row of Table 78-4 under the PHY type 100BASE-TX:

Tw_phy = 18
Need proper descriptive text for the modification made on The Encoder state diagram (Figure 25-1) for EEE capability.

Suggested Remedy
Insert the following statement at the end of this paragraph:
The output of Encoder is set to a value ZERO_VOLTAGE when the transmitter is in a quiet line state (TX_QUIET, see PCS Transmit state diagram, Figure 24-8).

Change the last sentence of tx_quiet at L.51, P.55 from
"It is also used to set the initial state of Encoder state diagram." to
"It sets the Encoder state diagram to an initial state of ZERO_V."

PROPOSED ACCEPT IN PRINCIPLE.

Refine the statements as follow:
The PMD Encoder function of the 100BASE-TX with EEE capability is identical to that of the TP-PMD except that the output of the Encoder is set to a value ZERO_VOLTAGE when the transmitter is in a quiet line state of the LPI mode (TX_QUIET, see PCS Transmit state diagram, Figure 24-8).

Need proper descriptive text for the modification made on The Decoder state diagram (Figure 25-2) for EEE capability.

Suggested Remedy
Insert the following statement at the end of this paragraph:
The output of Decoder is set to a value ZERO when the receiver is in a quiet line state (RX_QUIET, see PCS Receive state diagram, Figure 24-11b).

Change the last sentence of rx_quiet at L.23, P.57 from
"It is also used to set the initial state of Decoder state diagram." to
"It sets the Decoder state diagram to an initial state of ZERO_VALUE."

PROPOSED ACCEPT IN PRINCIPLE.

Refine the statements as follow:
The PMD Decoder function of the 100BASE-TX with EEE capability is identical to that of the TP-PMD except that the output of the Decoder is set to a value ZERO when the receiver is in a quiet line state of the LPI mode (RX_QUIET, see PCS Receive state diagram, Figure 24-11b).

Add two more PICS entries as follows:

Code-groups used to measure jitter in the LPI mode shall be SLEEP code-group.
Jitter measurement time interval in the LPI mode shall be no less than 100 msec and no greater than 1 second.

PROPOSED ACCEPT.
There are "shall" statements in the following area without associated PICS entries:

25.4.11.1, P.55, L.24
25.4.11.2, P.56, L.50
25.4.11.3, P.57, L.45
25.4.11.4, P.57, L.51
25.4.11.5, P.58, L.29
25.4.11.6, P.58, L.36
25.4.11.7, P.58, L.43
25.4.11.7, P.55, L.44

Suggested Remedy
Add entries in the PICS list as suggested in the comment.

PROPOSED ACCEPT.

The duration of lpi_postupdate_timer has a period between 2.0us to 2.2us. It does not have a comfortable margin for the field application. The increase of this lpi_postupdate_timer has no impact on the wakeup time.

Suggested Remedy
Change the duration of lpi_postupdate_timer as follows:
Duration: This timer shall have a period between 4.0 microseconds to 4.4 microseconds

PROPOSED ACCEPT.

This comment is out of scope as it is on unchanged text and not related to outstanding disapprove, however, the commenter is correct in that the terminology was not updated to allign with the one decided by the wake-shrinkage ad-hoc.
The parameter \( T_{\text{sys}} \) (actual \( T_{\text{sys,t}} \)) can be a decimal number based on the value in the column \( T_{\text{sys,t}} \) of the table 78-4. However, the value holders of negotiated parameter described in this subclause ask for an integer with microsecond as the unit. It needs clarification on how to convert the intended \( T_{\text{sys,t}} \), which could consist of fraction of microseconds, to an integer number.

**Suggested Remedy**

Add in the text of 78.4.2.2 something like:

"This parameter should be rounded up to the nearest integer number when it is calculated and examined according to 78.2 and Table 78-4."

**Proposed Response**

This comment is out of scope as it is on unchanged text and not related to outstanding disapprove, however, the commenter is correct in that the current TLV fields do not take into account decimal locations. The proposed remedy is an efficient way to accommodate the issue.

The two exit conditions of the TX UPDATE state in Figure 78-4 "EEE DLL Transmitter State Diagram" should be swapped. That means the branch from TX UPDATE with conditions

"\( \text{NEW\_TX\_VALUE} < \text{LocResolvedTxSystemValue} \) + (\( \text{NEW\_TX\_VALUE} < \text{TempRxVar} \))" goes to MIRROR UPDATE state, while the branch with conditions

"\( \text{NEW\_TX\_VALUE} >= \text{LocResolvedTxSystemValue} \) + (\( \text{NEW\_TX\_VALUE} >= \text{TempRxVar} \))" goes to SYSTEM REALLOCATION state.

**Suggested Remedy**

Per comment

**Proposed Response**

This comment is out of scope as it is on unchanged text and not related to outstanding disapprove, however, the commenter is correct in that this is an error in the SM.
There is a "LPC capability that is defined. This capability has a direct impact on the functions performed by the PCS and PMA, yet the only new PICS are for the timers.

"Shall"s are needed to help define the way the PCS and PMA functions operate in LPI mode. Scrub the clause to make sure that functions modified or impacted by LPI have a corresponding PICS capability entry.

"Shall"s and associated PICS entries are added in the draft per comment #98, #99, #103, and #117.

What is more, the following shall statements and associated PICS entries are added:

P.47, L.15: Change 'Far-End Fault is not generated when in the LPI mode.' to "Far-End Fault shall not be generated when in the LPI mode."

P.48, L.12: Change 'If the EEE capability is supported, when the receiver is in the LPI mode, the assertion of lpi_link_fail sets the link_status to FAIL and eventually brings the receiver out of the LPI mode.' to "If the EEE capability is supported, when the receiver is in the LPI mode, the assertion of lpi_link_fail shall set the Link Monitor to LINK DOWN state and eventually brings the receiver out of the LPI mode."

P.47, line 43 and 51: Change "operates" to "shall operate" in the sentence of "In the absence of the optional EEE capability, the PHY operates as if the value of this variable is FALSE."

The subclause number overlaps with the existing subclause 25.4.11 of IEEE Std 802.3-2008.

What is more, it would be better to promote the Ethernet Efficient Ethernet to its own heading2 level. The volume of information here probably should not be buried as an exception.

Promote 25.4.11 to be 25.5 and modify the clause number of PICS from 25.5 to 25.6.

The figure number of "Figure 78-4 EEE DLL Transmitter State Diagram" duplicates with that of "Figure 78-4 LPI mode timing parameters and their relationship to minimum system wake time."

Insert a new PICS entry for the wake error counter with the following comment:

"For each transition of lpi_rx_tw_timer_done from false to true, the wake error counter shall be incremented."

Show Table 69-1 as in P802.3ba (with the 40GBASE-KR4 row and extra columns) as your basis for modification.

Will change table to match that of P802.3ba with editors note to show source.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Comment ID: D2.1 Energy Efficient Ethernet comments</th>
<th>Comment ID: November 2009</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cl 69 SC 69.1.1</td>
<td>P 198 L 7 # 119</td>
<td>Dawe, Piers Independent</td>
</tr>
<tr>
<td>Comment Type</td>
<td>E</td>
<td>Comment Status</td>
</tr>
<tr>
<td>Comment</td>
<td>As D2.0 comment 118: P802.3ba will be adding the objective &quot;a 4 lane 40Gb/s PHY&quot;. The addition by 802.3az of &quot;Optionally support Energy Efficient Ethernet will imply that 40GBASE-KR4 will support EEE.</td>
<td></td>
</tr>
</tbody>
</table>
| Suggested Remedy | If you intend to mandate EEE as an option for 40GBASE-KR4, Table 69-1 will make this clear. If you don't, change "Backplane Ethernet optionally supports Energy Efficient Ethernet (EEE) to reduce energy consumption." to "1000BASE-KX, 1000BASE-KX4 and 100BASE-KR optionally support Energy Efficient Ethernet (EEE) to reduce energy consumption."
| Proposed Response | Proposed ACCEPT IN PRINCIPLE. |
| See response to comment 26. |

| Cl 49 SC 49.2.9 | P 149 L 2 # 122 | Dawe, Piers Independent |
| Comment Type | T | Comment Status | D |
| Comment | "the scrambler input will bypass": "will" is deprecated (except in Clause 30 and as described in style manual) |
| Suggested Remedy | shall? (with PICS) "bypasses"? Scrub the draft. |
| Proposed Response | Proposed ACCEPT IN PRINCIPLE. |
| See comment #239 |
| Change will to shall |

| Cl 51 SC 51.4 | P 162 L 3 # 123 | Dawe, Piers Independent |
| Comment Type | E | Comment Status | D |
| Comment | Subclause heading for Table 51-3 is missing |
| Suggested Remedy | Insert "51.4 Sixteen-Bit Interface (XSBI)" |
| Proposed Response | Proposed REJECT. |
| The change instruction names the figure. |

| Cl 69 SC 69.2.3 | P 198 L 35 # 121 | Dawe, Piers Independent |
| Comment Type | E | Comment Status | D |
| Comment | AUTO-NEGOTIATION |
| Suggested Remedy | Auto-Negotiation |
| Proposed Response | Proposed ACCEPT. |

- Comment ID: 119
- Comment ID: 120
- Comment ID: 121
- Comment ID: 122
- Comment ID: 123
- Comment ID: 124

*TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID*
### Comment 125

**Comment Type:** ER

**Comment Status:** D

Without the underlines it would not be sufficiently clear what "EEE only" applies to.

**Suggested Remedy:**

These signals should be dotted as in Figure 51-3; so should the "Data output when scrambler_bypass is true" of Figure 49-5.

**Proposed Response:**

**Response Status:** W

PROPOSED ACCEPT IN PRINCIPLE.

Place dotted box around the signals.

Also around the "Data output." of Figure 49-5.

---

### Comment 126

**Comment Type:** E

**Comment Status:** D

Lines 22, 29, 33, 47 "A boolean"

Line 37 "An boolean"

Line 40 "this Boolean"

**Suggested Remedy:**

See online editors' guidance (capital B for Mr Boole) and correct. Scrub the draft.

**Proposed Response:**

**Response Status:** W

PROPOSED ACCEPT.

---

### Comment 127

**Comment Type:** TR

**Comment Status:** D

The Lock state diagram, which I don't think is optional, uses the variable "rx_block_lock" where the current standard has "block_lock". Yet 49.2.13.2.2 says "The following variables are used only for the EEE capability... rx_block_lock". Problem - and there may be similar problems e.g. in Clause 36. So I'm piling on to D2.0 comment 190 and 174, we need to preserve the non-EEE material in an undamaged state, by use of annexes like 4A, duplicate state diagrams or other means. Otherwise, users will go back to 802.3-2008 for non-EEE product, and any future maintenance to affected areas will be ignored.

**Suggested Remedy:**

Preserve the non-EEE material in an undamaged state, by use of annexes like 4A, duplicate state diagrams or other means.

**Proposed Response:**

**Response Status:** W

PROPOSED REJECT.

This was discussed at length during the resolution of comments against draft 2.0.

---

### Comment 128

**Comment Type:** TR

**Comment Status:** D

Resolution on Comment #130 against draft D2.0 was to change control code to 0x06, but it is still 0x07.

**Suggested Remedy:**

Change the control code to 0x06 at these locations.

Page 148, line 7

Page 149, line 42

**Proposed Response:**

**Response Status:** W

PROPOSED ACCEPT.
When the transmitter goes through activation or deactivation, the receiver will see invalid code words. hi_ber might get set before rx_block_lock becomes false (Page 151, line 3). This will cause the receive SM (fig 49-15) to transit from RX_LI to RX_INIT (because of Page 155, line 3).

Suggested Remedy

Change the transition to BER_MT_INIT (Page 151, line 2) from reset + r_test_mode + !rx_block_lock
To reset + r_test_mode + rx_lpi_active.
This will make it consistent with Clause 55: fig 55-14 (LFER monitor state diagram).

Proposed Response

The block lock is still required.
reset + r_test_mode + !rx_block_lock + rx_lpi_active

The transmitter can get a wake command while it is in TX_REFRESH, which means the LPI TX SM will go through the following state changes.

TX_ENERGY_ALERT -> TX_REFRESH -> TX_WAKE -> TX_WAKE_SCR_BYPASS and then to TX_ACTIVE.
Which means 1usec + 14usec + 12usec + 1usec + 1usec = 29usec.

The receiver wake timer is only 17 usec, hence the LPI RX SM will transition to RX_WTF state. But the above scenario is a valid wake. The way to avoid this is to increase the rx_tw_timer value.

Please note that the comment shows TX_ENERGY_ALERT state which is coming from a solution for a different comment. And its solution is addressed through pillai_1109_01.pdf. Now even without it, the issue exists.

Suggested Remedy

Increase the timeout for RX wake timer to 29us (min) to 30us (max).
The following are the changes that are required.

1. sub clause: 49.2.13.2.5, page 153, Line 19 Change TUL to TWR.
2. table 49-3, page 158, line 28: Change the values to 29us (min) to 30us (max).
3. table 49-3, page 158, line 31: Remove this line. There is no need for two TWR.

Proposed Response

If scrambler_bypass is not used then the wake time may be shorter.

1. as written
2. 26uS, 27 uS
3. 29uS, 30uS
To make the transition from RX_SLEEP to RX_ACTIVE more robust, we should change the transit condition from

\[ \text{!rx_tq_timer_done} \cdot \text{R_TYPE}(\text{rx_coded}) = \text{IDLE} \]

To

\[ \text{!rx_tq_timer_done} \cdot \text{rx_block_lock} \cdot \text{R_TYPE}(\text{rx_coded}) = \text{IDLE} \]

**Suggested Remedy**

The way to avoid this is by modifying the LPI transmit state diagram from entering SCR_BYPASS state during these three scenarios.

Each of the above three transitions needs to be modified to

- TX_SLEEP to TX_ACTIVE, TX_REF_SCR_BYPASS to TX_ACTIVE, and TX_RE_SCR_ON to TX_ACTIVE, respectively.
- Transition from TX_SLEEP: \( T_{TYPE}(\text{tx_raw}) \neq \text{LI} \), goes to TX_ACTIVE
- Transition from TX_REF_SCR_BYPASS: \( T_{TYPE}(\text{tx_raw}) \neq \text{LI} \cdot \text{one_us_timer_done} \), goes to TX_ACTIVE
- Transition from TX_RE_SCR_ON: \( T_{TYPE}(\text{tx_raw}) \neq \text{LI} \), goes to TX_ACTIVE

(the last one doesn't need to wait for the timer).
Comment Type: TR  Comment Status: D

"The value of the SIGNAL_DETECT is defined by the training state diagram shown in Figure 72-5 when rx_quiet = FALSE."

Does not sound correct. The rx_quiet = FALSE happens several times when the PHY is in EEE. Change this line to

Suggested Remedy

"The value of the SIGNAL_DETECT is defined by the training state diagram shown in Figure 72-5 when rx_lpi_active = FALSE."

Proposed Response

PROPOSED REJECT.

There currently is no rx_lpi_active signal defined from the PMA to the KR PHY. But based on other proposals, I believe there should be.

Comment Type: TR  Comment Status: D

Add EEE to CL 74 PICS

Suggested Remedy

Under 74.11.3 Major capabilities/options

Item: LPI
Feature: Rapid block lock
Subcals: 74.7.4.8
Value/Comment: Device implements Rapid block lock mechanism to support EEE.
Status: O
Support: Yes [ ] / No [ ]

Proposed Response

PROPOSED ACCEPT.
Proposed response

I think we should rename RX_WTF to RX_EXW (Extended wake) or at least add a "K", which will make it RX_WKTF (Wake time fault) Which ever way we decide, all the reference to WTF needs to be changed too.

Suggested Remedy

PROPOSED REJECT.

Changing the name will effect multiple lines in multiple clauses.

Proposed response

I think we should rename RX_WTF to RX_EXW (Extended wake) or at least add a "K", which will make it RX_WKTF (Wake time fault) Which ever way we decide, all the reference to WTF needs to be changed too.

Suggested Remedy

PROPOSED REJECT.

Changing the name will effect multiple lines in multiple clauses.

Proposed response

Modify the following transition conditions for RX_SLEEP to RX_ACTIVE
from detect_idle * ODD
to !rx_tq_timer_done * code_sync_status = OK * detect_idle * ODD
For the self loop for RX_SLEEP should be
frx_tq_timer_done * detect_lpidle
And for RX_SLEEP to RX QUIET
frx_tq_timer_done * signal_detect=FAIL

Suggested Remedy

PROPOSED ACCEPT.
Proposed responses

Presently in CL49 LPI receive state machine, the transition from RX_QUIET to RX_WAKE is enabled by energy_detect. Energy detect is more susceptible to noise and cross talks. This will unnecessarily make the LPI RX State machine transition out of the RX_QUIET state. Several comments and concerns were put forward against Draft 2.0 during the September interim. Changes were made to the CL49 LPI transmit and receive state diagrams to handle this appropriately during false energy detect. These changes still do not address the vulnerability of the Energy Detect.

Suggested Remedy

Pillai_1109_01.pdf addresses this issue and proposes a solution in detail. The idea is for the Transmitter to send out a pattern as a prequel before the refresh or wake sequence. During EEE mode, Energy detect function may use this alert pattern to detect electrical energy at the receiver.

The proposed pattern is a repeating "0XFF00" (eight "1"s and eight "0") for 1 usec.

Change to fig 49-16, LPI TX state diagram and all the other edits needed are show in Pillai_1109_01.pdf.

Proposed Response

Unnecessary transitions out of RX_QUIET because of noise will cause a waste of power but will not cause any malfunction. Any method used to improve the quality of energy_detect will improve the power savings but will not require changes to this state diagram.

Proposed Response

PROPOSED REJECT.

The transitions from the refresh states to TX_WAKE do not need to wait because the scrambler bypass will be held for 1 μS in state SCR_BYPASS.

Proposed Response

PROPOSED ACCEPT.

Same as comment #249

Note change to Clause 40.
Proposed Response

#143

Cl 24 SC 24.2.3.4 P 41 L 50 # 143
Healey, Adam LSI Corporation

Comment Type T Comment Status D

The duration of lpi_rx_tw_timer is required to be between 30 and 36 us. The lower limit here is superfluous. In addition, the PHY wake time allowance per Table 78-4 is 20.5 us and should be the gauge for correct operation of the PHY.

Suggested Remedy

Change:

“The timer shall have a period between 30 us to 36 us”

To:

“This timer shall have a period that does not exceed 20.5 us.”

It should be noted that the 20.5 us upper limit may not be correct. The timer is started when signal_status = ON and hence the transmitter wake time shrinkage and signal detect assertion time have already passed when the receiver begins it count. The value of 20.5 us is offered for now due to a lack of a more detailed calculation.

PROPOSED ACCEPT IN PRINCIPLE.

The response to this comment may be affected by the comment #102 since the wake time could be modified.

Change

“The timer shall have a period between 30 us to 36 us”

to:

“This timer shall have a period that does not exceed 20.5 us.”
The transmitter starts the LPI mode by sending a series of SLEEP code-groups for a duration denoted by Ts and then goes into the Sleep state. Following the Sleep state, the transmitter PCS sends a control signal to PMD through PMA to indicate the start of the Quiet state. Upon receiving the control signal, the PMD ceases the transmission by turning the output to a low power steady level (DC zero volt). The refresh function, which is used to maintain some internal parameters of the receiver of the remote link partner, such as those necessary for timing recovery and signal equalization, is accomplished by re-entering the Sleep state periodically from the Quiet state. The Quiet state is therefore not allowed to last longer than Tq before a Wake state or a Sleep state appears. At the end of the LPI mode, the stream is terminated by transmitting consecutive IDLE code-groups for duration Tw.

When the receiver of the link partner successfully receives and interprets the SLEEP code-groups, it enters the LPI mode. The receiver then sends Assert LPI across the MII (Table 22-2) to notify the upper layer of a change in mode of operation. When the receiver detects a Quiet state in the medium, it stops the receiver function and waits for reactivation. The reactivation is triggered by the wakeup process of PMD triggered by the incoming signal. If the receiver PCS interprets the signal as an IDLE signal it returns to the normal mode and stops sending the Assert LPI on MII. If the receiver PCS interprets the signal as an SLEEP signal it reenters the Sleep state to serve the refresh function. A continuous indication of signal detection on the channel through signal_status as communicated by the PMD_SIGNAL.indicate primitive controls the transitions among those receive states in the LPI mode as depicted in Figure 24-11b.

The timing parameter for each line state is specified in Table 24-2. The transmitter timing parameters control the sending of the signal while the receiver timing parameters set the watchdog timers to check the time-out condition of such a signal. Therefore, except for the wake time, the parameter value of the receiver is greater than that of the transmitter. In the case of wake time, the wake signal can be no shorter than the defined parameter value to ensure adequate time for the link partner to recover from the quiet state. The receiver uses this parameter to set the maximum recovery time by which its receiver function is fully operational. If the receiver is not fully operational, a wake error event is logged.

The assert_lpidle variable is defined to be an alias for:

\[
(xmit=DATA*TX_OSET.indicate*TX_EN=FALSE*TX_ER=TRUE*(TXD<7:0> =0x01))
\]

a) The TX_OSET.indicate message should be removed from this definition. Otherwise the state diagram in Figure 36-5 would exit the XMIT_LPIDLE state immediately after entering it since TX_OSET.indicate will not be set. The transitions conditions in the XMIT_LPIDLE state should then be changed to:

XMIT_DATA to XMIT_LPIDLE: assert_lpidle*TX_OSET.indicate
XMIT_LPIDLE to XMIT_LPIDLE: assert_lpidle*TX_OSET.indicate
XMIT_LPIDLE to XMIT_DATA: !assert_lpidle*TX_OSET.indicate

b) The XMIT_DATA state, and thus the XMIT_LPIDLE state, can only be reached when xmit=DATA. Therefore, the xmit=DATA could also be removed in the assert_lpidle definition.

Suggested Remedy
Per comment.

PROPOSED ACCEPT.
Cl 36 SC 36.2.5.1.5 P76 L 50 # 146
Healey, Adam LSI Corporation

Comment Type T Comment Status D

Issues with counter definitions:

a) With the exception of the wake_error_counter, are these truly counters, or timers as their names and usage suggests?

b) With the exception of rx_wf_timer, timer descriptions begin with "This timer is started when the PMD receiver enters the..." In some cases this should actually refer to the transmitter and in any case should refer to the PCS and not the PMD.

Suggested Remedy

a) Move definitions of *_timer to "36.2.5.1.7 Timers" (note that the subclause heading needs to be changed from "Timer" to "Timers").

b) For all instances of rx_*_timer, change the definition to read "PCS receiver" instead of "PMD receiver". For all instances of tx_*_timer, change definition to reach "PCS transmitter" instead of "PMD receiver."

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 36 SC 36.2.5.1.3 P76 L 24 # 147
Healey, Adam LSI Corporation

Comment Type TR Comment Status D

The aliases detect_lpidle and detect_idle could be asserted during data reception therefore the LPI Receive state diagram (Figure 36-7a) could bounce between RX_ACTIVE and RX_SLEEP states during normal operation.

A transition to RX_SLEEP will result in "Rx LPI indication" and "Rx LPI received" from being falsely asserted during normal operation. This is not likely what is intended.

Suggested Remedy

Implement the state diagram changes recommended in healey_01_1109.pdf.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

The state machine needs to ensure that "detect_lpidle" is only tested following a /K28.5/.

Add a state LPI_K. Transition from RX_ACTIVE to LPI_K - "SUDI(/K28.5/) * EVEN"

Then transition from LPI_K to RX_SLEEP - "detect_lpidle * sync_status = code_sync_status"; transition from LPI_K back to RX_ACTIVE - "detect_lpidle + sync_status != code_sync_status"

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

1. In the LP_IDLE state, "RUDI(/LI/)" should be "RUDI(/L/I)/". However, it is not clear why RUDI(/LI/) is even an action here since RX_UNITDATA.indicate is used by the Clause 37 Auto-Negotiation process which does not understand /LI/. It likely should just be removed.

2. Transitions to F and C should be qualified by the term "rx_lpi_active" and not "rx_lp_active" as shown.

Suggested Remedy
Per comment.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

Remove RUDI(/L/I/)

Change rx_lp_active to rx_lpi_active
There is no exit condition from LPI_K in the event a configuration ordered sets (C/) is received. The link partner could potentially restart Auto-Negotiation at any time, in which case it could start sending C/ ordered sets and no I/ or LI/ ordered sets would be sent. That would cause that the state diagram gets stuck in the LPI_K state.

Figure 36-7a requires the LPI Receive state diagram (Figure 36-9b) to break it out of this deadlock. If C/ ordered sets are received while the receiver is in RX_SLEEP, then rx_tq_timer will eventually expire and the transition to the RX_LINK_FAIL state will be taken. This will set sync_status to FAIL which will pop the Receive state diagram into the LINK_FAILED state. From here, the receiver may recover and Auto-Negotiation can proceed normally.

If C/ ordered sets are received while the receiver is in the RX_QUIET or RX_WAKE states, rx_tw_timer_done will eventually expire and the transition to the RX_WTF state will be taken. This will increment wake_error_counter (it is debatable whether this is appropriate or not) and move the RX_ACTIVE state. At this point, the receiver is deadlocked.

A more graceful handling of C/ ordered sets is desired.

Suggested Remedy
Implement the state diagram changes recommended in healey_01_1109.pdf.

Proposed Response Response Status W
PROPOSED REJECT.

The PCS transmit ordered set state machine does not permit sending C/ during LPIDLE.

Encoding notation for /LI1/ and /LI2/ are missing leading and trailing forward slashes.

Suggested Remedy
Change /LI1/ encoding to "/K28.5/D6.5/".
Change /LI2/ encoding to "/K28.5/D26.4/".

Proposed Response Response Status W
PROPOSED ACCEPT.
The duration of rx_tw_timer is specified to be TWR which in Table 48-10 is given a range of between 8 to 9 us. A lower limit here is superfluous. It implies that there is lower limit on the wake time.

**Suggested Remedy**

In the definition of rx_tw_timer change:

"The timer terminal count is set to TWR."

To:

"The timer terminal count shall not exceed the maximum value of TWR in Table 48-10."

Remove TWR(min) from Table 48-10.

**Proposed Response**

PROPOSED REJECT.

If TWR is set too low then the receiver may falsely assert wake time fault.

The duration of rx_tw_timer is specified to be TUL. This should be TWR. In Table 49-3 TWR is given a range between 11 to 12 us when scrambler_bypass_enable is FALSE and a range between 13 and 14 us when scrambler_bypass_enable is TRUE. A lower limit here is superfluous. It implies that there is lower limit on the wake time.

**Suggested Remedy**

In the definition of rx_tw_timer change:

"The timer terminal count is set to TUR."

To:

"The timer terminal count shall not exceed the maximum value of TWR in Table 49-3."

Remove TWR(min) from Table 49-3.

**Proposed Response**

PROPOSED REJECT.

If TWR is set too low then the receiver may falsely assert wake time fault.

With the exception of the rx_wf_timer, each timer definition mistakenly refers to the "PMD's" receiver or transmitter when it should refer to the "PCS" transmitter or receiver.

**Suggested Remedy**

Per comment.

**Proposed Response**

PROPOSED ACCEPT.

10GBASE-R service interface primitive names now map to several different functions. Bad idea. In the use of these primitive that follows, the parameter names suddenly become upper case (e.g. "tx_quiet" becomes "TX_QUIET").

**Suggested Remedy**

Recommend the following changes:

d) "FEC_SIGNAL.request(tx_quiet)" should become "FEC_TXQUIET.request(tx_quiet)"
e) "FEC_SIGNAL.request(rx_quiet)" should become "FEC_RXQUIET.request(rx_quiet)"
f) "FEC_SIGNAL.indication(energy_detect)" should become "FEC_ENERGY.indication(energy_detect)"
g) "FEC_SIGNAL.request(rx_lpi_active)" should become "FEC_LPIACTIVE.request(rx_lpi_active)"

This will also align with service interface primitive names used in the Clause 51 PMA.

In addition, consistently use lower case for the parameter names.

**Proposed Response**

PROPOSED ACCEPT.
Proposed responses

Healey, Adam LSI Corporation

Comment Type  T  Comment Status  D
Comment Type  TR  Comment Status  D

Cl 74 SC 74.4.1 P 215 L 40 # 157

In Figure 74-2...
1) There is a typ-o in the title -- "diagra" should be "diagram"
2) The figure implies that rx_lpi_active is passed from the FEC sublayer to the PMA sublayer. It is not, remove it.

Suggested Remedy
Per comment.

Proposed Response  Response Status  W

PROPOSED ACCEPT IN PRINCIPLE.

Accepting #1.

#2: There is a comment against CL72 (#133), if that gets accepted, then CL72 will be using rx_lpi_active

Cl 74 SC 74.5.5 P 216 L 38 # 158
Healey, Adam LSI Corporation

Comment Type  ER  Comment Status  D

Subclause headings make it impossible to reference the desired subject matter from the bookmarks.

"74.5.4 Service primitive from FEC for EEE support (optional)" should be "74.5.5 FEC_ENERGY.indication (optional)"

"74.5.5 Service primitive from PCS for EEE support (optional)" should be
"74.5.5 FEC_LPIACTIVE.request (optional)"

"74.5.6 Service primitive from PCS for EEE support (optional)" should be "74.5.6 FEC_RXQUIET.request (optional)"

etc...

Suggested Remedy
Please review the structure of the base document, as amended by P802.3ba, and be consistent with it. It would also be better if the primitive were defined in the same order they are listed in 74.5.1.

Proposed Response  Response Status  W

PROPOSED ACCEPT.
In Figure 40-9, it is not necessary to enforce entry into the LOC_LPI_REQ_OFF state when link_status != OK. Per 22.7a.1, LP_IDLE.request should remain de-asserted for 1 second after link_status = READY so this requirement is redundant.

In addition, it should be made clear that, for optional EEE capability, the PHY should be able to successfully complete training per Figure 40-15a even when loc_lpi_req and/or rem_lpi_req are set to TRUE. This is due to the fact that a 1000BASE-T link may re-train without setting link_status != OK. This implies that the LPI client will be unaware that the link is re-training and may present “Assert LPI” at the GMII.

**Proposed Response**

Remove link_status != OK term from the transition into the LOC_LPI_REQ_OFF state and add clarifying text to 40.4.2.4 per the comment.

**PROPOSED ACCEPT IN PRINCIPLE.**

Change Figure 40-9 per comment.

Add the following paragraph at the end of the text to be inserted in 40.4.2.4:

"When the PHY supports the optional EEE capability, it is possible for loc_lpi_req to be set to TRUE during re-training initiated in response to unsatisfactory receiver performance (i.e. transition from SEND IDLE OR DATA to SLAVE SILENT). This will correspond to the detection of rem_lpi_req = TRUE in the idle code-groups received during training. The PHY shall not be impeded from successfully completing training (e.g. acquisition of descrambler state) when rem_lpi_req = TRUE is encoded in received idle code-groups.""
Proposed response

Separate terms tx_dll_enable, tx_dll_ready and rx_dll_enable, rx_dll_ready are not necessary.

Comment:

The TX and RX state machines use the above conditions as an entry/exit point to the states. It is noted that both TX and RX state machine works on the transmission and reception of EEE TLV’s and both conditions are needed to be considered while entering/exiting to each of the state machine.

Suggested Remedy

Search and Replace tx_dll_enable and rx_dll_enable with dll_enable and clean up tables to reflect proper definition.

Search and Replace tx_dll_ready and rx_dll_ready with dll_ready and clean up tables to reflect proper definition.

PROPOSED REJECT.

This comment is out of scope for the recirculation as it is on unchanged text and not related to outstanding disapprove.

The comment requests a simplification to the current scheme and is not necessary for technical completeness of the draft and would result in considerable changes to the section. The commenter may wish to submit again during sponsor ballot.

Comment:

During normal operation the transmitting link partner is in the RUNNING state. If the transmitting link partner wants to initiate a change to the presently resolved value of Tw_sys, the local_system_change is asserted and the transmitting link partner enters the LOCAL CHANGE state where NEW_TX_VALUE is computed. +++ If the new value is smaller than the presently advertised value of Tw_sys or if the transmitting link partner is in sync with the receiving link partner, then it enters TX UPDATE state. +++ Otherwise it returns to the RUNNING state.

Comment: The portion in "+++" suggests that the local PHY’s TX or RX state machine can request for a change in its currently advertised Tw_sys value. However it is also noted that this is only allowed to reduce the value and there is no support to increase it or restore it to the previous value or a higher value.

Suggested Remedy

Add clarifying text in 78.4.2.5 (and possibly in 78.4.3.1) that the Transmit Tw_sys must always be the same or longer than the Receiver Tw_sys, so that the receiving link partner will always be ready to accept data, prior to data being sent by the Transmit link.

PROPOSED REJECT.

This comment is out of scope for the recirculation as it is on unchanged text and not related to outstanding disapprove.

In addition, the behaviour is not as described by the commenter. The SM will allow the link partner to move in either direction (up or down) as long as the link partners are in sync (i.e. the echo matches what the local link partner has). The constraint is only when they are out of sync.

Comment:

Incorrect reference for 22.2.2.4

Suggested Remedy

Should be 35.2.2.4

PROPOSED ACCEPT.
Cl 35  SC 35.4a.3.1  P 72  L 49  #  165
Koenen, David  Hewlett-Packard
Comment Type  E  Comment Status  D
  Two instances of MII instead of GMII in this paragraph.
Suggested Remedy
  Prefix MII with a G.
Proposed Response  Response Status  W
PROPOSED ACCEPT.

Cl 49  SC 49.2.13.3.1  P 156  L 26  #  166
Koenen, David  Hewlett-Packard
Comment Type  E  Comment Status  D
  Missing arrow head on line from RXQUIET to RX_LINK_FAIL.
Suggested Remedy
  Add arrow head.
Proposed Response  Response Status  W
PROPOSED ACCEPT.

Cl 36  SC 36.2.5.1.3  P 76  L 35  #  167
Koenen, David  Hewlett-Packard
Comment Type  ER  Comment Status  D
  rx_lpi_active is defined and appears in the state diagram, but doesn't appear to be used anywhere.
Suggested Remedy
  Remove rx_lpi_active definition and it's appearance in state diagrams, or use it somewhere.
Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

rx_lpi_active is used, but there is a typo - change rx_lp_active to rx_lpi_active (as per comment #149).

Cl 51  SC 51.2  P 162  L 1  #  168
Koenen, David  Hewlett-Packard
Comment Type  T  Comment Status  D
  rx_lpi_active appears in Figure 49-4 & Figure 74-2 going to the PMA, but does not appear in the PMA diagram or signal definitions.
Suggested Remedy
  Either add it to the PMA diagram and definitions or delete from the other figures and definitions.
Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

The signal should not be shown going to the PMA in Figure 74-2.

In Figure 49-4 add "(FEC sublayer only)"

Cl 74  SC 74.4.1  P 221  L 40  #  109
Koenen, David  Hewlett-Packard
Comment Type  T  Comment Status  D
  rx_lpi_active is not an output of the FEC nor an input to the PMA sublayer.
Suggested Remedy
  Delete from signal name from FEC to PMA on diagram.
Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

There is a comment against CL72 (#133), if that gets accepted, then CL72 will be using rx_lpi_active

Cl 72  SC 72.10  P 214  L 5  #  170
Kasturia, Sanjay  Teranetics
Comment Type  E  Comment Status  D
  Change "FED" to "FEC" to fix typo.
Suggested Remedy

Proposed Response  Response Status  W
PROPOSED ACCEPT.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Comment ID</th>
<th>Comment ID</th>
</tr>
</thead>
<tbody>
<tr>
<td>171</td>
<td>ER</td>
<td>174</td>
<td>ER</td>
</tr>
<tr>
<td>172</td>
<td>ER</td>
<td>175</td>
<td>ER</td>
</tr>
<tr>
<td>173</td>
<td>ER</td>
<td>176</td>
<td>TR</td>
</tr>
</tbody>
</table>

**Proposed responses**

- **Proposed Response**
  - **Response Status**: W
  - **Suggested Remedy**: PROPOSED ACCEPT IN PRINCIPLE.
- **Duplicate of comment #196**
- **Comment ID 172**
  - **Type**: ER
  - **Comment Status**: D
  - **Comment**: Delete Figure 14-9 as it is unchanged from the base text. Also delete Table 14-1 if it is unchanged from base text. Remove associated base text if it is unchanged.
  - **Suggested Remedy**: PROPOSED ACCEPT IN PRINCIPLE.
  - **Duplicate of comment #198**
- **Comment ID 173**
  - **Type**: ER
  - **Comment Status**: D
  - **Comment**: Page 21 line 11 Delete Fig 14-10 if unchanged from base text. Page 21, line 28 - Delete Fig 14-11 if unchanged from base text. Also delete associated text if unchanged from base text.
  - **Suggested Remedy**: PROPOSED ACCEPT IN PRINCIPLE.
- **Duplicate of comments #200 and #201**

**Comment Status**:
- D/dispatched
- A/accepted
- R/rejected

**Response Status**:
- O/open
- W/written
- C/closed
- U/unsatisfied
- Z/withdrawn
The variable `fec_rapid_block_lock_edge` is set to TRUE to detect when `fec_rapid_block_lock` changes state from FALSE to TRUE. When is it set to FALSE?

Referring the FEC Lock state diagram (Figure 74-3).

1. If `rx_lpi_active` is TRUE and the link partner's transmission ceases during the quiet period (signal_ok), the state diagram will not transition to `FEC_LOCK_INIT` state. It is not clear why this transition is inhibited; perhaps to stop `fec_block_lock` from being set to FALSE. However, there will be repeated parity check failures corresponding to the lack of an input signal. It seems that it can be safely assumed that `fec_block_lock` will be set to FALSE at some point during the quiet period and held there until refresh or wake.

2. As long as `fec_rapid_block_lock_edge` is TRUE, the state diagram is held in the `RESET_CNT` state.

3. When `fec_rapid_block_lock_edge` transitions from TRUE to FALSE, the state diagram tests the next available block. It proceeds to check for `n` = 4 consecutive good parity checks before `fec_block_lock` is set back to TRUE.

4. The variable `fec_signal_ok` is defined (page 219, line 10) to be `signal_ok*(fec_block_lock+fec_rapid_block_lock_edge)`. Therefore, this value will be set to TRUE while `fec_rapid_block_lock` is TRUE, and then be set to FALSE for at least `n` = 4 FEC blocks before being set to TRUE again.

5. This `fec_signal_ok` variable is communicated to the PCS via the `FEC_SIGNAL.indication` primitive, and used in the PCS Lock state diagram (Figure 49-12). The behavior of `fec_signal_ok` implies that the PCS lock diagram will first try to obtain block synchronization, and then be forced to lose it, and then try to obtain it again.

The intended behavior is unclear.

Suggested Remedy

1. If the intent is to have the PCS begin to acquire block lock when `fec_block_lock` is TRUE, then it seems unnecessary to include the term `+fec_rapid_block_lock_edge` in the definition of `fec_signal_ok`.

2. If the intent is to have the PCS begin to acquire block lock when `fec_rapid_block_lock_edge` is TRUE, the perhaps to correct entry point is `FEC_BLOCK_LOCK` where `fec_block_lock` is TRUE. In this case, the term `+fec_rapid_block_lock_edge` becomes redundant in the definition of `fec_signal_ok`. This assumes that the `fec_rapid_block_lock` process reliably identifies FEC block boundaries, since erroneous alignment wouldn't be detected for at least `m` = 8 FEC frames.

3. In either case, it seems that the qualification of `fec_signal_ok` for the optional EEE capability in 74.10.2.2 is not necessary and can be removed.

4. In either case, it seems necessary to define when `fec_rapid_block_lock_edge` is set to FALSE. It seems that this time should (considerably?) less than one FEC block following its time of its assertion.

Proposed Response

1. If the intent is to have the PCS begin to acquire block lock when `fec_block_lock` is TRUE, then it seems unnecessary to include the term `+fec_rapid_block_lock_edge` in the definition of `fec_signal_ok`.

2. If the intent is to have the PCS begin to acquire block lock when `fec_rapid_block_lock_edge` is TRUE, the perhaps to correct entry point is `FEC_BLOCK_LOCK` where `fec_block_lock` is TRUE. In this case, the term `+fec_rapid_block_lock_edge` becomes redundant in the definition of `fec_signal_ok`. This assumes that the `fec_rapid_block_lock` process reliably identifies FEC block boundaries, since erroneous alignment wouldn't be detected for at least `m` = 8 FEC frames.

3. In either case, it seems that the qualification of `fec_signal_ok` for the optional EEE capability in 74.10.2.2 is not necessary and can be removed.
Proposed responses from "parity_good_cnt = n" to "(parity_good_cnt = n + fec_rapid_block_lock)"

#d Change the transition condition between VALID PARITY and TEST_FEC_BLOCK from "test_fec_block * parity_good_cnt < n" to "(test_fec_block * parity_good_cnt < n + fec_rapid_block_lock)"

These state diagram changes will gracefully set the fec_block_lock as soon as the fec_rapid_block_lock is achieved. Since the fec_rapid_block_lock mechanism will set the correct SLIP, the blocks that follows will also match the parity. Hence the FEC lock state diagram will maintain the fec_block_lock status.

Proposed Response

Cl 49  SC 49.2.13.2.5  P 153  L 14  # 178
Healey, Adam  LSI Corporation

Comment Type  T  Comment Status  D
The definition of rx_tq_timer states that it is started in the RX_QUIET state. Referring to the LPI Receive state diagram (Figure 49-17) it appears that it is actually started in the RX_SLEEP state.

SuggestedRemedy
Update the definition.

Proposed Response  Response Status  W
PROPOSED ACCEPT.

Cl 46  SC 46.1.7  P 125  L 20  # 179
Estes, Dave  UNH - IOL

Comment Type  E  Comment Status  D
"shall remain to be set to" should be "shall remain set to"

SuggestedRemedy
Change "shall remain to be set to" to "shall remain set to"

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

Sentence reworded by comment #24.
Proposed response

Cl 49  SC 49.2.6  P149  L1  # 182
Estes, Dave  UNH - IOL

Comment Type: T  Comment Status: D
scrambler_reset was removed in comment #456

Suggested Remedy
Remove the text "To aid block synchronization in the receiver when the optional LPI function is supported, the registers of scrambler shall be held at logic zero while scrambler_reset is TRUE."

Proposed Response  Response Status: W
PROPOSED ACCEPT IN PRINCIPLE.
See comment #239

Cl 55  SC 55.3.2.2.9  P174  L23  # 183
Estes, Dave  UNH - IOL

Comment Type: T  Comment Status: D
Table 55-1
The 8B/10B codes provided for lp_idle are for the lp_idle used in Clause 36. They should be the 8B/10B codes for lp_idle used in Clause 48.

Suggested Remedy
Change "K28.5/D6.5, K28.5/D26.4" to "K28.0 or K28.3 or K28.5 or D20.5".

Proposed Response  Response Status: W
PROPOSED ACCEPT.

Cl 55  SC 55.3.5.4  P184  L23  # 184
Estes, Dave  UNH - IOL

Comment Type: T  Comment Status: D
Comment #141 was accepted but the text to define ldpc_frame_done was not added.

Suggested Remedy
Add the text from comment #141.

Proposed Response  Response Status: W
PROPOSED ACCEPT.

Cl 55  SC 55.3.5.2.2  P179  L33  # 185
Parnaby, Gavin  Solarflare Communication

Comment Type: ER  Comment Status: D
Separate the eee definitions. Applies to variables, constants, timers, functions, counters.

Suggested Remedy
As comment

Proposed Response  Response Status: W
PROPOSED ACCEPT.

Cl 55  SC 55.3.5.4  P184  L  # 186
Parnaby, Gavin  Solarflare Communication

Comment Type: T  Comment Status: X
Link monitoring and recovery during the LPI state needs more study.

In the current draft the criteria used to drop the link during LPI is not specified. Since PHYs can monitor link quality only during refreshes (and then only for 4 LDPC frames (~1.2us)) and since some PHYs may choose not to wake for all refreshes, it may take multiple refresh cycles before link drop is detected by both sides of the link. Then both sides need to go through a complete training sequence, taking up to 2s, to return to the normal operation mode.

If the link is disturbed during LPI the ability of the PHY to recover is limited by the quiet-refresh signaling since only 4 LDPC frames out of 512 can be used for equalizer/echo training. It would be extremely valuable to include a method by which EEE-10GBASE-T PHYs are able to recover a disturbed link without a full retrain.

Suggested Remedy
See presentation.

Proposed Response  Response Status: O

Proposed Response  Response Status: O
Proposed Response

Comment Type \ TR \ Comment Status \ D

The response to comment #466 (on Clause 55) on draft 2.0 said that the control code for /LI/ in clause 49 would be changed to 0x06.

This was missed in the draft update.

Suggested Remedy

Change the /LI/ control code to 0x06 in clause 49 as agreed in the response to comment #466 on draft 2.0.

Proposed Response \ Response Status \ W

PROPOSED ACCEPT.

Comment Type \ TR \ Comment Status \ D

Submitted on behalf of Todd Thompson, Solarflare.

In Annex 28D.7, it states that extended next pages "may" be used to reduce auto-negotiation time. This statement is not normative. It's an informative note. It's also incorrect. For 10GBASE-T, extended next pages are required.

Suggested Remedy

Option 1 (preferred): Remove this informative note.
Option 2: Clarify that for those technologies requiring XNP's (such as 10GBASE-T), an XNP must be sent which is formatted based on the BASE-T EEE message page/unformatted message page as defined in Clause 78 (as suggested in another comment).

Proposed Response \ Response Status \ W

PROPOSED REJECT.

This text is part of 802.3-2008. It describes the extended next page operation. Specific PHYs may require mandatory use of extended next pages, such mandates are in the respective PHY clauses.
Cl 45  SC 45.2.7.13a  P120  L12  # 190
Parnaby, Gavin  Solarflare Communications

Comment Type: TR  Comment Status: D
Submitted on behalf of Todd Thompson, Solarflare.

Tables 45-157a and 45-157b have multiple bits with the same designation without a clear indication of how the bits map to the pages. For example, in Table 45-157a there are multiple D0, D1 and D2. In Table 45-157b there are multiple U0, U1, and U2 bits. There’s no indication how these bits are mapped to the individual bits in the next pages. It’s not clear how many unformatted pages are being sent nor how multiple bits in the control register map to the same bits in the unformatted page/pages.

Suggested Remedy
Option 1 (preferred): Use existing reserved bits for previously defined Next Pages and Extended Next Pages as defined in Clause 40.5 and 55.6 and remove this new message code/format.
Option 2: Separate the definition of the NP and XNP out of Clause 45.2.7.13a and 45.2.7.14a and put the format of these pages and mapping of these bits into the EEE Clause 78 to make this consistent to the way 1G and 10G has been done previously. Insert tables into Clause 78 which define the number and format of NPs and/or XNPs similar to Clause 40.5 and 55.6.

Proposed Response  Response Status: W
PROPOSED REJECT.

The clause reference denotes whether the bits are sent as part of a Clause 28 or Clause 73 next page.

Cl 40  SC 40.5.1.1  P108  L31  # 191
Parnaby, Gavin  Solarflare Communications

Comment Type: TR  Comment Status: D
Submitted on behalf of Todd Thompson, Solarflare.

Clause 40.5 previously only referred to control/status bits in Clause 22. This section refers to a mixture of Clause 22 and Clause 45.2.7 bits. This require implementation of both Clause 22 registers and the MMD 7 register in Clause 45.2.7 to control the advertisement/status of EEE.

Suggested Remedy
Add EEE control/status bits into Clause 22 and make Clause 40.5 refer to these control/status bits instead of the bits in Clause 45.2.7.

Proposed Response  Response Status: W
PROPOSED REJECT.

The current management structure has been in place since the first Task Force review (July 2008) and subject to multiple subsequent reviews. The rationale behind the current management structure is:

1. There is little usable space in the Clause 22 register to support the control and status bits for 100BASE-TX and 1000BASE-T. The wake error counter requires another 16-bit register.
2. Clause 22 supplies a means to access the Clause 45 management space via registers 13 and 14. Since a EEE-capable PHY is a new PHY, the additional of this feature was expected to contribute little additional disruption.

The commenter does not provide a sufficiently detailed suggested remedy (i.e. specific modifications to the Clause 22 register map) to consider a change to the draft.
Proposed responses

Annex 28C and Clause 45.2.7.13a and Clause 45.2.7.14a require new EEE Next Pages and new message codes adding 1/2 second during autonegotiation. This time is largely wasted as the PHY must send bits for technologies it does not support and send many bits which are unused.

Suggested Remedy

Use existing reserved bits in existing NP's defined in Clause 40.5 (to control EEE for 100M/1G) and XNP defined in Clause 55.6 (to control BASE-T EEE for 100M/1G/10G). Define existing reserved bits in Clause 22 (for 1000BASE-T) and Clause 45.2.7 (for 10GBASE-T) to control the advertising of BASE-T EEE and to report link partner's BASE-T EEE ability.

PROPOSED REJECT.

There was extensive discussion regarding this topic during comment resolution in D2.0 and the conclusion was to define a new message code for next page.

The commenter should prepare a more detailed proposal, along with justification, for discussion during the comment resolution.

Comment Status D

Response Status W

Parnaby, Gavin Solarflare Communicat

Comment Type TR

Proposed Response

There was extensive discussion regarding this topic during comment resolution in D2.0 and the conclusion was to define a new message code for next page.

The commenter should prepare a more detailed proposal, along with justification, for discussion during the comment resolution.

Comment Status D

Response Status W

Parnaby, Gavin Solarflare Communicat

Comment Type TR

Add counters

Add a counter which increments in the RX_W rx wake on error condition and the management to support this counter.

Add a counter which increments in the TX_WE tx wake on error condition and the management to support this counter.

PROPOSED ACCEPT.

Parnaby, Gavin Solarflare Communicat

Comment Type TR

There are no means to monitor RX wake errors in the current draft. Wake errors are monitored in 1000BASE-T.

There are no means to monitor TX wake errors in the current draft.

Suggested Remedy

Add a counter which increments in a delayless state added to the transition between TX_WE and TX_C

Add a counter which increments in a delayless state added to the transition between RX_W and RX_E

PROPOSED ACCEPT.
Proposed responses

Cl 14 SC 14.1.1 P15 L36 #196
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-1 is unchanged from the base text

Suggested Remedy
Delete figure 14-1

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.3.1.2.1 P19 L9 #197
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-8 is unchanged from the base text.

Suggested Remedy
Delete figure 14-8

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.3.1.2.1 P19 L20 #198
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-9 is unchanged from the base text.

Suggested Remedy
Delete figure 14-9

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.1.1 P19 L36 #199
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Table 14-1 is unchanged from the base text

Suggested Remedy
Delete table 14-1

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.4 P21 L10 #200
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-10 is unchanged from the base text

Suggested Remedy
Delete figure 14-10

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.4 P21 L28 #201
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-11 is unchanged from the base text.

Suggested Remedy
Delete figure 14-11

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 14 SC 14.4.1 P22 L13 #202
Chadha, Mandeep Vitesse Semiconductor

Comment Type E Comment Status D
Figure 14-12 is unchanged from the base text.

Suggested Remedy
Delete figure 14-12

Proposed Response Response Status W
PROPOSED ACCEPT.
Proposed responses

IEEE P802.3az D2.1 Energy Efficient Ethernet comments

November 2009

Cl 46 SC 46.3.2.4a P127 L33 #003
Brown, Matt AppliedMicro (AMCC)

Comment Type T Comment Status D

LP_IDLE on XGMII is not always followed by IDLE (4x07h control characters). If the PHY is Clause 55, then LP_IDLE might be followed by Local Fault ordered sets. This section should at least mention this.

Note that another comment requests that error control characters be sent instead or that only idles follow LP_IDLE. A different remedy than specified below may be required.

SuggestedRemedy

Add note that LP_IDLE may be followed by local fault ordered sets rather than IDLE.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Add a second note:

"Note: In some instances, LPI may be followed by characters other than IDLE during the wake time."

Cl 55 SC 55.3.2.2.9 P174 L23 #004
Brown, Matt AppliedMicro (AMCC)

Comment Type T Comment Status D

In Table 55-1, 8B/10B column is for codes used in 10GBASE-X not 1000BASE-X. For instance, the idle row lists K28.0, K28.3, K28.5 which are used in 10GBASE-X for idle as opposed to /K28.5/D5.6/ and /K28.5/D16.2/ used for 1000BASE-X.

SuggestedRemedy

Delete "K28.5/D6.5" and "K28.5/D26.4" and replace with "K28.0, or K28.3, K28.5 with D20.5."

Add idle row and change 8B/10B column to "K28.0, K28.3, or K28.5 without D20.5."

Add footnote to both rows "Use of idle and lp_idle ordered set per 48.2.4.2."

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 55 SC 55.3.2.2.10 P172 L39 #005
Brown, Matt AppliedMicro (AMCC)

Comment Type E Comment Status D

Add reference to figure.

SuggestedRemedy

Change "Receive 64B/65B state diagram" to "64B/65B receive state diagram in Figure 55-16."

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 55 SC 0 P0 L0 #006
Brown, Matt AppliedMicro (AMCC)

Comment Type E Comment Status D

Many instances of both "EEE capability" and "LPI capability", but both have the same meaning. The latter is used only in Clause 55.

SuggestedRemedy

Change "LPI capability" to "EEE capability".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 55 SC 55.3.2.2 P173 L52 #007
Brown, Matt AppliedMicro (AMCC)

Comment Type E Comment Status D

wording

SuggestedRemedy

Change "MAC across the XGMII" to "MAC via the XGMII".

Proposed Response Response Status W

PROPOSED ACCEPT.
Proposed Response

"alert" and "refresh" are signals

Suggested Remedy

"then the alert shall be transmitted in place of the refresh."

To...

"then the alert signal shall be transmitted in place of the refresh signal."

PROPOSED ACCEPT IN PRINCIPLE.

I think we need to be careful with the distinction between the 4 frames of alert signal and the alert sequence. The text shouldn't imply that the 4 frames of refresh are replaced by 4 frames of alert if the overlap is only partial (e.g. refresh starts 3 frames earlier).

Use "then alert signalling shall be transmitted in place of the refresh signalling where the signals overlap."

Comment Type E  Comment Status D

PROPOSED REJECT.
<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Page</th>
<th>Line</th>
<th>Comment ID</th>
<th>Page</th>
<th>Line</th>
</tr>
</thead>
<tbody>
<tr>
<td>212</td>
<td>55</td>
<td>192</td>
<td>212</td>
<td>55</td>
<td>192</td>
</tr>
<tr>
<td>213</td>
<td>55</td>
<td>193</td>
<td>213</td>
<td>55</td>
<td>193</td>
</tr>
<tr>
<td>214</td>
<td>55</td>
<td>192</td>
<td>214</td>
<td>55</td>
<td>192</td>
</tr>
<tr>
<td>215</td>
<td>49</td>
<td>156</td>
<td>215</td>
<td>49</td>
<td>156</td>
</tr>
<tr>
<td>216</td>
<td>49</td>
<td>155</td>
<td>216</td>
<td>49</td>
<td>155</td>
</tr>
<tr>
<td>217</td>
<td>48</td>
<td>140</td>
<td>217</td>
<td>48</td>
<td>140</td>
</tr>
</tbody>
</table>

**Comment ID 212**

**Comment Type**: E

**Comment Status**: D

Brown, Matt (AppliedMicro)

**Comment**: Alert is a 4 frame signals comprised of 3.5 frame periods (7 repeats) of 128-symbol xpr_master or xpr_slave sequence followed by 0.5 frame periods (128 symbols) of zero.

**Suggested Remedy**

Change "(3.5 LDPC ... silence)" to "(3.5 LDPC frame periods of xpr_master or xpr_slave sequence and 0.5 frame periods of zero symbols)"

**Proposed Response**

PROPOSED ACCEPT IN PRINCIPLE.

Change "(3.5 LDPC ... silence)" to "(3.5 LDPC frame periods of xpr_master or xpr_slave sequence and 0.5 frame periods of zero symbols)"

**Comment ID 213**

**Comment Type**: E

**Comment Status**: D

Brown, Matt (AppliedMicro)

**Comment**: Last sentence refers to deleted state diagram.

**Suggested Remedy**

Delete sentence...

"PHYs with the EEE ... figure 55-27a."

**Proposed Response**

PROPOSED ACCEPT.

**Comment ID 214**

**Comment Type**: E

**Comment Status**: D

Brown, Matt (AppliedMicro)

**Comment**: Last sentence refers to deleted state diagram. The functionality was moved to the PCS state diagram.

**Suggested Remedy**

Delete sentence...

"The receive state ... signalling sleep."

**Proposed Response**

PROPOSED ACCEPT.
<table>
<thead>
<tr>
<th>Cl.</th>
<th>SC</th>
<th>P</th>
<th>L</th>
<th>ID</th>
<th>Comment Type</th>
<th>Comment Status</th>
<th>Proposed Response</th>
<th>Response Status</th>
<th>Comment Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>49</td>
<td>48.2.6.2</td>
<td>138</td>
<td>52</td>
<td>218</td>
<td>ER</td>
<td>D</td>
<td>Transitions are on ordered_sets not code groups.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Reject.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>46</td>
<td>46.3.1.5a</td>
<td>126</td>
<td>42</td>
<td>221</td>
<td>ER</td>
<td>D</td>
<td>In Figure 46-7a, it would be instructive to show the LP_IDLE.request that triggers the assertion of LP_IDLE on the XGMII.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Reject.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>46</td>
<td>46.1.3.2</td>
<td>126</td>
<td>12</td>
<td>219</td>
<td>ER</td>
<td>D</td>
<td>Should be more specific about use of 06.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Accept In Principle.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>46</td>
<td>46.3.2.2</td>
<td>127</td>
<td>37</td>
<td>222</td>
<td>ER</td>
<td>D</td>
<td>In Figure 46-8a, it would be instructive to show the LP_IDLE.indication that results upon detection of LP_IDLE on the XGMII.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Reject.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>46</td>
<td>46.4a.1</td>
<td>128</td>
<td>40</td>
<td>223</td>
<td>ER</td>
<td>D</td>
<td>LPI indication goes to LPI client.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Proposed Accept.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Comment ID # 223**
Cl 55  SC 55.2.2.3.1  P171  L47  # 224
Brown, Matt  AppliedMicro (AMCC)

Comment Type  ER  Comment Status  D
Description of pma_unitdata.request is not consistent with ALERT request. Changing the
description will resolve this problem.

Note: This seems like an awkward way to request an action. A more consistent approach
would be to use a request signal, e.g., PCS_TX_ALERT.request(alert). When alert =
TRUE, PMA sends alert, else PMA sends data from PMA_UNITDATA.request.

Suggested Remedy
"During transmission, ... and BI_DD. For EEE capable PHYs, the vector also requests the
PMA to send the ALERT signal during LPI. The tx_symb_vector parameter takes on the
form:"

Proposed Response  Response Status  W
PROPOSED ACCEPT.

Cl 55  SC 55.3.2.2.0  P174  L38  # 226
Brown, Matt  AppliedMicro (AMCC)

Comment Type  ER  Comment Status  D
Use lp_idle to indicate lp idle characters. Also, "/LI/s" seems like bad syntax.

Suggested Remedy
Change "/LI/s may be added following LPI" to "/LI/ control characters may be added
following lp_idle".

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

The "/LI/s" terminology was used to maintain consistent bad-syntax with non-EEE PHYs -
/II/s" are used in 55.3.2.2.9 in the existing standard. The new subclause parallels 55.3.2.2.9
with /II/s' replaced with /LI/s'.

Change the sentence to
"/LI/s may be added follow low power idle control characters".

Cl 55  SC 55.3.2.2.1  P174  L7  # 225
Brown, Matt  AppliedMicro (AMCC)

Comment Type  ER  Comment Status  D
Blocks and frames have as much or as little significance in LPI mode as in any other mode.
Also, LPDC frame boundaries delimit LPI cycles. So retain, legacy wording and change
new sentence.

Suggested Remedy
Change two sentences from "Outside the LPI ... and alert times." to "Blocks and frames are
unobservable and have no meaning outside the PCS. During the LPI mode, LDPC frame
boundaries delimit sleep, wake, refresh, quiet and alert cycles."

Proposed Response  Response Status  W
PROPOSED ACCEPT.
### Proposed Response

#### Comment #227

**Comment Type:** ER  
**Comment Status:** D

It is not clear what these two sentences are saying. Are they saying that there are two wake timer values for the transmitter depending on when the wake is requested? Or are they talking about the maximum time that the receive requires to wake up in each of the two modes. The use of the word maximum seems to have two meanings here.

It would clear things up immensely to give different variable names to the timer values for "during sleep" and "after sleep".

**Suggested Remedy**

On page 175, line 46–48  
Change ...
"The maximum PHY wake time, lpi_wake_timer, is 7.36 us (lpi_wake_timer=Tw_phy as defined by Clause 78), which occurs only when wake is requested before sleep has been transmitted. Typically, wake will be requested after the sleep signal is transmitted and in this case the maximum PHY wake time value is 4.48 us."  
To...  
"Typically, wake will be requested after the sleep signal is transmitted and in this case the maximum PHY wake time, phy_wake_timer, is 4.48 us. When wake is requested before sleep has been transmitted the maximum PHY wake time, is 7.36 us to allow extra time at the receiver for the sleep sequence to complete. In either case, the wake signal will be sent for a minimum time as indicated by phy_wake_timer."

**Proposed Response**

**Response Status:** W  
**Proposed Accept.**

If accepted this changes the response to 229

#### Comment #228

**Comment Type:** ER  
**Comment Status:** D

Header in column 1 is incorrect.

**Suggested Remedy**

Change "lpi_tx_wake_time" to "lpi_wake_time".

**Proposed Response**

**Response Status:** W  
**Proposed Accept.**

#### Comment #229

**Comment Type:** ER  
**Comment Status:** D

Fix wording in headers of columns 2 and 3.

**Suggested Remedy**

Change "lpi_wake_timer during sleep" to "lpi_wake_timer when wake starts before sleep signal is complete".
Change "lpi_wake_time after sleep" to "phy_wake_time when wake starts after sleep signal is complete [or during quiet/refresh]."

**Proposed Response**

**Response Status:** W  
**Proposed Accept.**

(If comment #277 is accepted then  
Change "lpi_wake_timer during sleep" to "phy_wake_timer when wake starts before sleep signal is complete".
Change "lpi_wake_time after sleep" to "phy_wake_time when wake starts after sleep signal is complete [or during quiet/refresh].")

#### Comment #230

**Comment Type:** ER  
**Comment Status:** D

Symmetric low power mode is not defined

**Suggested Remedy**

Change "during the symmetric low power mode" to "when both transmit and receive are in LPI mode."

**Proposed Response**

**Response Status:** W  
**Proposed Accept.**
Proposed responses

Cl 55 SC 55.3.5.4  P 183  L 10  # 231
Brown, Matt  
AppliedMicro (AMCC)

Comment Type  ER  Comment Status  D
What is a sleep block?

Suggested Remedy
Change
"from the time that the 64B/65B receiver detects a sleep block"
To
"from the time that the 64B/65B receiver enters TX_L state"

Proposed Response  Response Status  W
PROPOSED ACCEPT.

Cl 49 SC 49.2.13.3.1  P 157  L 19  # 232
Brown, Matt  
AppliedMicro (AMCC)

Comment Type  T  Comment Status  D
Transition criteria from RX_SLEEP to RX_ACTIVE or RX_SLEEP not consistent with rest of SM. R_TYPE is elsewhere anded with rx_block_lock.

Suggested Remedy
Simple fix.
Change "R_TYPE(rx_coded) = IDLE" to "(R_TYPE(rx_coded) = IDLE) * rx_block_lock". 
Alternately.
Consider/define (R_TYPE(x) = y) being TRUE to include the condition that rx_block_lock = TRUE. In which case, we can clean up the SM by removing the rx_block_lock condition from the following transitions:
RX_WAKE to RX_SLEEP
RX_WAKE to RX_ACTIVE
RX_WTF to RX_SLEEP
RX_WTF to RX_ACTIVE
RX_ACTIVE to RX_SLEEP

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

Use the simple fix.

Cl 55 SC 55.3.2.2.21  P 175  L 9  # 233
Brown, Matt  
AppliedMicro (AMCC)

Comment Type  T  Comment Status  D
Presumably, the scrambler continues to run as well.

Suggested Remedy
Change sentence to:
"After the sleep signal is transmitted, LP_IDLE characters shall be input to the PCS scrambler continuously and the scrambler shall continue to operate until the transmit LPI mode ends."

Proposed Response  Response Status  W
PROPOSED REJECT.
There is no text saying that the scrambler is disabled.

Stating that it runs continuously is not necessary.

Cl 55 SC 55.3.2.2.21  P 176  L 25  # 234
Brown, Matt  
AppliedMicro (AMCC)

Comment Type  T  Comment Status  D
The last two sentences are not very clear and are incorrect.

Suggested Remedy
Change...
"The /I/ ... normal operation." 
To...
"The PHY receive sends /I/ to the XGMII for 9 LDPC frame periods then resumes normal operation decoding received 64B/65B blocks and sending the decoded values to the XGMII."

Proposed Response  Response Status  W
PROPOSED ACCEPT IN PRINCIPLE.

"The PCS receive function sends /I/ to the XGMII for 9 LDPC frame periods then resumes normal operation."
Proposed responses

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Status</th>
<th>CI</th>
<th>SC</th>
<th>Page</th>
<th>Line</th>
<th>#</th>
</tr>
</thead>
<tbody>
<tr>
<td>235</td>
<td></td>
<td>D</td>
<td>55</td>
<td>55.3.5.2.3</td>
<td>181</td>
<td>18</td>
<td>235</td>
</tr>
<tr>
<td>Brown, Matt</td>
<td>AppliedMicro (AMCC)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>T</td>
<td>Comment Status</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Definition of &quot;lpi_rx_wake_timer&quot; does not match SM.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Suggested Remedy
Change definition to:
"This timer defines the time the receiver continues to send IDLE and/or LF blocks after the ALERT signal is detected."

Proposed Response Response Status W
PROPOSED ACCEPT.

See also 242 as that may change whether LF are transmitted in the TX_WE state

--

<table>
<thead>
<tr>
<th>Comment ID</th>
<th>Type</th>
<th>Status</th>
<th>CI</th>
<th>SC</th>
<th>Page</th>
<th>Line</th>
<th>#</th>
</tr>
</thead>
<tbody>
<tr>
<td>236</td>
<td></td>
<td>D</td>
<td>55</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>236</td>
</tr>
<tr>
<td>Brown, Matt</td>
<td>AppliedMicro (AMCC)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Comment Type</td>
<td>T</td>
<td>Comment Status</td>
<td>D</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>In many figures, there is a statement &quot;... mandatory for EEE.&quot; This doesn't say that its not required by non-EEE PHYs and might be interpreted as saying that its optional for non-EEE PHYs.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Suggested Remedy
Wherever there is statement "...mandatory for EEE capability" or similar statement also indicate something like ":... mandatory for EEE-capable PHYs and is not required for non-EEE PHYs."

Proposed Response Response Status W
PROPOSED REJECT.

Adding 'not required for non-EEE PHYs' does not change the normative requirements of the text so it is not necessary.

However, the wording will be changed to match that in other clauses (see e.g. page 89 of draft 2.1), for consistency (though this doesn't seem to address the meat of the comment, which is why the proposed response is reject):

NOTE: Signals and functions shown with dashed lines are only required for the EEE capability.

The editor also notes that in at least one state diagram a new variable has been added which are not defined for non-EEE PHYs - in Figure 55-14 lpi_rx_wake_timer_done is used. Add a note to this figure that states

'NOTE: The variable lpi_rx_wake_timer_done is only required for the EEE capability and should be treated as if the value of this variable is TRUE otherwise.'
Cl  49  SC  49.2.13.3.1  P  156  L  43  #  [238]  Brown, Matt  AppliedMicro (AMCC)

**Comment Type**  TR  **Comment Status**  D  
Transition from RX_REF_SCR_BYPASS or RX_REF_SCR_ON to TX_WAKE will cause result in far end receiver transiting to RX_ACTIVE state the receiving random behaviour when local TX is in SCR_BYPASS state (should be labelled TX_WAKE_SCR_BYPASS).

**SuggestedRemedy**

Change SM as follows:

1. change transition "TX_REFRESH_SCR_BYPASS-TX_WAKE" to TX_REFRESH_SCR_BYPASS-TX_ACTIVE
2. For (1) change criteria from "T_TYPE(tx_raw)=I" to "T_TYPE(tx_raw)=I one_us_timer_done"
3. change transition "TX_REFRESH_SCR_ON-TX_WAKE" to TX_REFRESH_SCR_ON-TX_ACTIVE"

**Proposed Response**  Response Status  W  
PROPOSED ACCEPT IN PRINCIPLE.

See comment #132

Cl  49  SC  49.2.6  P  149  L  1  #  [239]  Brown, Matt  AppliedMicro (AMCC)

**Comment Type**  TR  **Comment Status**  D  
I think this sentence was unintentionally retained. Scrambler reset is no longer required and has been replaced by scrambler bypass.

**SuggestedRemedy**

Replace sentence with...

"To aid block synchronization in the receiver when optional LPI function is supported, a scrambler bypass will be provided. When scrambler_bypass = true the scrambler bypass is used and the scrambler will otherwise continue to operate normally."

**Proposed Response**  Response Status  W  
PROPOSED ACCEPT IN PRINCIPLE.

The change to draft 2.0 was implemented incorrectly. The paragraph on p. 149 l. 1 should have been replaced by the paragraph that was placed on p. 149 l. 15.

The paragraph that is in d2.0 p.141 .15 was incorrectly replaced.

Move paragraph from p.149 l.15 to l.1 (replacing the current). Move paragraph from D2.0 p.151 l.15 to p.149 l.15 (to repair the error).

Cl  46  SC  46.3.1.5a  P  126  L  21  #  [240]  Brown, Matt  AppliedMicro (AMCC)

**Comment Type**  TR  **Comment Status**  D  
Throughout this sub-clause there are references to the LPI client. The LPI client is the MAC and this section describes RS Transmit functionality.

The LPI client indicates LPI request through LP_IDLE.request. This section describes LPI request through the XGMII.

**SuggestedRemedy**

Change all instances of "LPI Client" to "RS".

**Proposed Response**  Response Status  W  PROPOSED ACCEPT.

Cl  46  SC  46.3.2.4a  P  127  L  18  #  [241]  Brown, Matt  AppliedMicro (AMCC)

**Comment Type**  TR  **Comment Status**  D  
Throughout this sub-clause there are references to the LPI client. The LPI client is the MAC and this section describes RS Receive functionality.

The LPI client receives LPI indication through LP_IDLE.indication. This section describes LPI indication through the XGMII.

**SuggestedRemedy**

Change all instances of "LPI Client" to "RS".

**Proposed Response**  Response Status  W  PROPOSED ACCEPT.
Comment Type  TR  Comment Status  D

In Figure 55-15a state TX_WE, local fault blocks are sent to indicate that the link has failed. It is previously sent only form transmit when transmit is in reset mode and from receive when receive is in reset or the input has failed (e.g., loss of block lock). A stream of local faults generates a local fault alarm at the RS and indicates that a link is failed and triggers re-calculation of routing tables at higher layers. Also, the state is wake error not wake fault :).

Normally, error characters or blocks are used to convey that an error event has occurred. In TX_WE state, send error blocks instead of local faults.

Suggested Remedy
In TX_WE state, change "tx_coded <= LBLOCK_T" to "tx_coded <= EBLOCK_T".

On page 175, line 42, change "local fault 64B/65B blocks" to "64B/65B error blocks".

Proposed Response  Response Status  W

Local faults sent by the LPI state machine will be converted to errors by the rx state machine in the link partner, so local faults will not be seen at higher system layers with the present state diagrams.

Neither error or local fault helps the link in any way - they force the receiving LPI PHY to exit the LPI state with an error condition due to a problem with MAC-PHY signaling on its link partner, which is undesirable.

A better solution is to transmit IDLEs in the WE state. Then the PHY enforces 9 frames of IDLEs which gives the receiving PHY the best chance to return to the normal operational mode without errors. Also use a counter to count error events on wake on both tx and rx sides.

See comment 195

Proposed responses

Comment Type  TR  Comment Status  D

Loop timing in slave mode is never explicitly stated as a requirement for EEE.

Suggested Remedy
Change ...

"Non-loop timed links are not supported by EEE."

To...

"An EEE capable PHY shall support loop timing and loop timing shall be enabled."

Proposed Response  Response Status  W

PROPOSED ACCEPT.

Comment Type  TR  Comment Status  D

LI and LII are defined as RBLOCKS not TBLOCKS.

Suggested Remedy
Redefine LI and LII T_BLOCK types for XGMII.

Proposed Response  Response Status  W

PROPOSED ACCEPT IN PRINCIPLE.

See comments 253, 251
Proposed responses

Comment: The frequency variation should apply when changing to and from low power mode as well.

Suggested Remedy:
Add sentence...
The short-term frequency variation limit shall also apply when switching to and from LPI mode.

Proposed Response
PROPOSED ACCEPT.

Comment: Simplify the definition of R_BLOCK_TYPE C to be consistent with the new definition for T_BLOCK_TYPE C proposed in another comment.

Suggested Remedy:
Change:
C: The vector contains a data/ctrl header of 1 and one of the following:
a) A block type field of 0x1E and eight valid control characters, none of which are /E/ and, if the low power idle function is supported, none of which are /LI/;
To:
C: The vector contains a data/ctrl header of 1 and one of the following:
a) A block type field of 0x1E and eight valid control characters other than /E/ and /LI/;

Proposed Response
PROPOSED ACCEPT.

Comment: Clock stop capable is a status bit and therefore should be RO not R/W.

Suggested Remedy:
Change: "...one of the five types..." to: "...one of the first five types..."

Proposed Response
PROPOSED ACCEPT.

Comment: The definition of LI needs to be consistent with the wording for a 72-bit tx_raw vector (as opposed to 65-bit RX block).

Suggested Remedy:
Change:
LI: If the optional Low Power Idle function is supported then the LI type occurs when the vector contains a data/ctrl header of 1, a block type field of 0x1e, and eight control characters of /LI/.
To:
LI: If the optional Low Power Idle function is supported then the vector contains eight control characters of /LI/.

Proposed Response
PROPOSED ACCEPT.
In the T_BLOCK_TYPE definition, type C conflicts with LII. Redefine type C to eliminate conflict (another comment addresses LII by redefining it).

**Suggested Remedy**

Change:

C: The vector contains one of the following:
   a) eight valid control characters other than /O/, /S/, /T/, and /E/; and, if the LPI function is supported, less than eight valid control characters of /LI/ and less than eight valid control characters of /LII/;

   To:

C: The vector contains one of the following:
   a) eight valid control characters other than /O/, /S/, /T/, /E/, and /LI/.

**Proposed Response**

PROPOSED ACCEPT.

---

When LPI is supported, valid sets of control characters and should not trigger transitions to TX_E and subsequent transmission of the Error control block. Currently, 4/LI/ followed by 4/I/ causes transitions to TX_E.

**Suggested Remedy**

Eliminate LII from the following transitions:

- TX_INIT to TX_E
- TX_C to TX_E
- TX_E to TX_E
- TX_L to TX_E

Add LII to the following transitions: (Outside of TX_L), act upon LII exactly as C

- TX_INIT to TX_C
- TX_C to TX_C
- TX_E to TX_C
- TX_T to TX_C

**Proposed Response**

PROPOSED ACCEPT.
Proposed responses

Comment ID # 255

Comment Status D

Comment Type TR

Grimwood, Michael Broadcom

In the existing PCS state diagram, certain normally occurring control blocks are considered as error (e.g., 4/I/ followed by 4/LI/). Redefine LII and add this type to state transitions currently conditioned on C.

Suggested Remedy

Redefine the LII block type as follows:

LII: If the optional Low Power Idle function is supported then the vector contains a data/ctrl header of 1, a block type field of 0x1E, and one of the following:
   a) four control characters of /LI/ followed by four control characters of /I/;
   b) four control characters of /I/ followed by four control characters of /LI/.

In Figure 55-16 on page 187 add LII to the following state transitions:

RX_INIT to RX_C: Change C to C + LII
RX_C to RX_C: Change C to C + LII
RX_D to RX_T: Change (S + C + Li) to (S + C + LI + LII)
RX_D to RX_E: Change (E + C + LI + S) to (E + C + LI + LII + S)
RX_E to RX_T: Change (S + C) to (S + C + LI + LII)
RX_T to RX_C: Change C to C + LI
RX_C to RX_C: Change C to C + LII

Note that the change to the transition from RX_E to RX_T also includes LI in order to be consistent with allowing LI to follow T, such that the transition from RX_E to RX_T should include LI in the R_TYPE_NEXT.

Proposed Response Response Status W

PROPOSED ACCEPT.

Comment ID # 256

Comment Status D

Comment Type TR

Horner, Rita Avago

Figure 48-9b transitions from RX_SLEEP are ambiguous.

Suggested Remedy

Change criteria for RX_SLEEP to RX_SLEEP, to "[LPIDLE] * !rx_tq_timer_done *(sig_detect=OK)"
Change criteria for RX_SLEEP to RX_ACTIVE, to "[IDLE] * !rx_tq_timer_done *(sig_detect=OK)"

Proposed Response Response Status W

PROPOSED ACCEPT.

Comment ID # 257

Comment Status D

Comment Type TR

Horner, Rita Avago

Figure 48-9b transitions from RX_WAKE are ambiguous.

Suggested Remedy

Change criteria for RX_WAKE to RX_QUIET, to "(signal_detect=FAIL) * !rx_tw_timer_done"
Change criteria for RX_WAKE to RX_ACTIVE, to "(signal_detect=OK) * !rx_tw_timer_done * deskew_align_status=OK * [IDLE]"
Change criteria for RX_WAKE to RX_SLEEP, to "(signal_detect=OK) * !rx_tw_timer_done * deskew_align_status=OK * [LPIDLE]"

Proposed Response Response Status W

PROPOSED ACCEPT.

Comment ID # 258

Comment Status D

Comment Type TR

Horner, Rita Avago

The convention is to have similar register map for PCS, PHY XS and DTE XS. PHY and DTE LPI management registers are not defined in AZ.

Suggested Remedy

Add PHY XS LPI management registers 4.1.11, 4.1.10, 4.1.9, 4.1.8, 4.22
Add DTE XS LPI management registers 5.1.11, 5.1.10, 5.1.9, 5.1.8, 5.22

Proposed Response Response Status W

PROPOSED REJECT.

There is no current definition for the use of LPI along with XGXS.
check_end function is not defined in 802.3az. When LPI is enabled in the device, there is a possibility that /D20.5/ will appear in the column following ||T||.

Prescient Terminate function used by the PCS Receive process to set the RXD<31:0> and RXC<3:0> signals to indicate Error if a running disparity error was propagated to any idle code-groups in ||T||, or to the column following ||T||. The XGMII Error control character is returned in all lanes less than n in ||T||, where n identifies the specific Terminate ordered-set ||Tn||, for which a running disparity error or any code-groups other than /A/ or /K/ or /D20.5/ are recognized in the column following ||T||. The XGMII Error control character is also returned in all lanes greater than n in the column prior to ||T||, where n identifies the specific Terminate ordered-set ||Tn||, for which a running disparity error or any code group other than /K/ is recognized in the corresponding lane of ||T||. For all other lanes the value set previously is retained.

rx_tq_timer is not precise. Not clear about the "enter RX_SLEEP" state.

rx_tq_timer: This timer is started when the PMD's receiver enters the RX_SLEEP state. The timer is restarted everytime ||LPIDLE|| is received, sig_detect=1 and rx_tq_timer_done while in RX_SLEEP state. The timer terminal counter is set to TQR. When the timer reaches terminal count it will set the rx_tq_timer_done=TRUE.

The definition that is in the table is accurate.

The state diagram conventions make it clear that the action (start rx_tq_timer) is performed each time the state is entered (or re-entered).
Proposed responses

D'Ambrosia, John Freec10 Networks

Comment Type: ER  Comment Status: D

P802.3ba will be adding the objective "a 4 lane 40Gb/s PHY. The addition by 802.3az of "Optionally support ENergy Efficient Ethernet will imply that 40GBASE-KR4 will support EEE.

Suggested Remedy

Change added objective text to "Optionally support Energy Efficient Ethernet for PHYs that support MAC rates of 10 Gb/s or lower."

Proposed Response:  Response Status: W

PROPOSED ACCEPT IN PRINCIPLE.

See response to comment 26.

Frazier, Howard Broadcom Corporation

Comment Type: TR  Comment Status: R

A state diagram in the MII clause. Wow. Why can't the PHY assert/deassert the CRS signal to indicate when the transmit path is in LPI?

Suggested Remedy

Take out the state diagram. The 100BASE-TX PHY with LPI should be responsible for asserting and deasserting CRS, and then implement the Suggested Remedy in my general comment concerning the structure of the draft amendment.

Response:  Response Status: U

REJECT.

In favor of accepting the proposed reject:

Yes: 15
No: 0
Abstain: 7

The state machine in the Reconciliation Sublayer was the cornerstone of the baseline (law_01_1108) that was adopted by the Task Force.

It was considered advantageous to have the control of the PLS_CARRIER.indication in the RS for a number of reasons:

1. It keeps the PHY receive and transmit paths separate (the PHY considers CRS to be part of the receive path).
2. It allows the PHY to go to sleep without having to maintain state & control the wake process.
3. It keeps the "data holdback" function close to the MAC and egress buffers, where it would be implemented in most designs.
4. It frees the PHY from having to participate in the wake time negotiation process (that is controled using LLDP frames).
5. It works for PHYs that operate at speeds greater than 1Gbps, so the same mechanism can be used for all speeds.

The state diagram would be present (or deleted according to the comment) whether the proposed changes to the document are accepted or not.
What do the little triangles in Figure 22-6a represent? The figure presents what appears to be a timing diagram that shows the relationship between various logical signals. How does an abstract service primitive fit into a logical timing diagram, and what does a triangle indicate?

Suggested Remedy

Remove the abstract service primitive from the timing diagram, and then implement the Suggested Remedy in my general comment concerning the structure of the draft amendment.

Response

REJECT.

The diagram is based on the proposal "law_01_1108" that was adopted as the baseline for this section.

The representation of PLS_CARRIER.indication adds clarity to the diagram without any ambiguity.

This diagram would be present regardless of the document structure chosen.

This is a general comment regarding the structure of the draft amendment.

As an amendment to IEEE Std 802.3, the material in this draft will eventually be folded into the base standard. When this happens, the definitions for the 100BASE-X and 1000BASE-X Physical Coding Sublayers will be substantially changed, and the changes will be difficult to discern. The definitions for the MII and GMII will also be substantially changed.

The 100BASE-X and 1000BASE-X PCSs are used for many other port types besides 100BASE-TX and 1000BASE-KX. Among these are 100BASE-FX, 100BASE-LX10, 100BASE-BX10, 1000BASE-SX, 1000BASE-LX, 1000BASE-CX, 1000BASE-LX10, 1000BASE-BX10, 1000BASE-PX10, 1000BASE-PX20, 10G/1GBASE-PRX-D/U1, 10G/1GBASE-PRX-D/U2, and 10G/1GBASE-PRX-D/U3.

These port types are not included in the set of objectives for P802.3az, and the specifications for the PCS and MII for these port types must not be changed or effected in any way by P802.3az. Each of these port types must have a current IEEE Std 802.3 PCS and MII to reference.

Suggested Remedy

There are many ways to solve this problem. I prefer the following approach:

1. Preserve the definitions for the MII, GMII, 100BASE-X PCS, and 1000BASE-X PCS without change.

2. Define the changes required to support EEE in a set of normative annexes, i.e. Annex 24A for Clause 24, and Annex 25A for Clause 25, etc. Example text for Annex 24A and Annex 25A have been provided by me to the task force chair.

3. Refer to these normative annexes from the body of Clause 78.

Response

ACCEPT IN PRINCIPLE.

See response to Comment #410
The table 45-83 and other tables in Clause 45 have been modified by P802.3ba. So the editing instructions should include the appropriate source document where the source is other than IEEE Std 802.3-2008. Also the table numbers should be changed to indicate the latest renumbered table numbers from previous amendment(s).

Also other PCS registers have been modified by the P802.3ba document (or other amendments e.g. P802.3av). So update the editing instructions and the change text as per the draft P802.3ba/D2.2.

For example change editing instruction as follows:

45.2.3.1 PCS control 1 register
Change Table 45-83 (IEEE P802.3ba/D2.2) for LPI clock control:
Update the table such that the base text is from the above source.

Suggested Remedy
Update the Editing instructions and Table numbers to indicate appropriate source for base text and use the renumbered table number from appropriate amendment to 802.3-2008. Also update the base text as appropriate as per the source document (for example IEEE P802.3ba/D2.2).

Response
ACCEPT IN PRINCIPLE.

See comments #39, 40, 41, 42, 43

Clause 74 is also being amended by P802.3ba. So where appropriate update the editing instructions to indicate the appropriate base text (IEEE Std 802.3-2008 or P802.3ba/D2.2).

Suggested Remedy
As per comment

Response
ACCEPT.

Clause 74 is also being amended by P802.3ba. Update the editing instructions and base text to indicate appropriate source (IEEE Std 802.3-2008 or P802.3ba).

Suggested Remedy
As per comment

Response
ACCEPT IN PRINCIPLE.

There doesn't appear to be any conflicting or overlapping changes.

But editor will add editor’s note to indicate P802.3ba may also affect clause 69 and, in parenthesis, and identify draft if the edit is based on a draft

Suggested Remedy

Response
ACCEPT IN PRINCIPLE.

Please refer to comments
364 and 8
Clause 72 supports digital signal detect mechanisms. Analog signal detect (or energy detect) was not part of this clause as it was felt that robust analog signal detect functions are difficult to define/implement in the backplane environment. (see thaler_01_0505.pdf, minutes_01_0505.pdf). Hence define a suitable digital signaling mechanism to exit from the low power idle state.

Suggested Remedy
As per comment

Response
ACCEPT IN PRINCIPLE.

At this point there is no clear alternative to a basic energy detect to waking up the PHY from sleep.

The receiver is just required to wake up within a certain time after detecting the electrical energy on the diff signal pair from a compliant, enabled transmitter.

The original KR signal detect would not work for EEE because it requires that training be complete before it could wake up the receiver. This was believed to be too long and we needed something to wake the PHY’s receiver prior to that.

For EEE, the KR’s transmit coefficients and receive equalization state are assumed to be saved before going quiet and quickly restored after wake so it can sync and lock much more quickly.

Changes were made to the state diagrams (see response to comment #425) to fix the observable behavior that may be caused by false detection. There is concern that the energy detect threshold level and detection circuitry could cause unnecessary activity in the receiver (due to noise and cross-talk).
Proposed responses

The style manual 21.2.1 isn't followed for numbering inserts, where for example, 22.2.2.6A would follow 22.2.2.6, it doesn't precede it and the draft insert instructions do not indicate a convention other than that of the style manual.

Suggested Remedy

Don't insert a TX subclause in the middle of receive subclauses. If the style manual convention is being used, what is currently 22.2.2.6a should be 22.2.2.5A. If not following the style manual all change instructions need to be clear about the insertion point. Fix all inserts consistently.

Response

ACCEPT IN PRINCIPLE.

Use explicit insert instructions. When the base text is from an approved amendment indicate the amendment in parenthesis.

Use lowercase alphabetic indication for a new subclause, table or figure to avoid disrupting the numbering of subsequent amendments.

When inserting a new subclause at a level it is x.x.0a

Coordinate numbering with 802.3ba. WG chair will help resolve any issues that arise from the coordination.

Primitives are not signals, and as I recall, timing requirements can't be placed on the primitive, only on the layers causing generation of a primitive.

Suggested Remedy

Needs thought and proper specification on the timing in multiple places in the standard.

All text (e.g., assert and deassert functions) related to service primitives needs to be reviewed for any language that reflects continuous visibility of a primitive value between (sub)layers to only a change in value being signaled by a primitive.

Response

ACCEPT IN PRINCIPLE.

Change the two sentences on lines 17 and 18, page 228 from:

"LPI_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link_status = OK, see 28.2.6.1.1). LP_IDLE.request shall remain set to DEASSERT for 1 second following the change of link_status to OK."

to:

"The effect of receipt of this primitive is undefined if link_status is not OK (see 28.2.6.1.1) or if LPI_REQUEST=ASSERT within 1 second of the change of link_status to OK."
Is signaling of LPI between an RS and its link partner, or between the RS and the lower parts of the PHY? If the PHY has no option to signal the request, then the language is appropriate, but it seems inconsistent with MII text describing the xMII signals. The effect of the primitive is to generate signals on the MII and that isn't specified here, but should be.

Suggested Remedy
Assure MII clause are consistent in what layer is signaling to what peer layer, and that any additional requirements on conveying the LPI request in lower sublayers is properly represented. Add generic text that covers the three MII types – how the assert or deassert is signaled, can probably be generic using the MII definition of assert low power idle.

ACCEPT IN PRINCIPLE.

The PHY has no option to signal the request so the language is appropriate however editor will look into adding clarifying text as in the suggested remedy.

Editor to check if that this is clear in the xMII clauses.

This new text is in the wrong place. It is not "overview" text. (I do recognize that it was "stuck" here in order to avoid the sticky issue of restructuring and renumbering sub-clauses.)

Suggested Remedy
Move to within the context of 14.4.2. I recognize that there may be restructuring necessary in order for this to end up as a clean, well-structured clause.

REJECT.

The text is consistent with the rest of the overview clause. Also, the text was revised based on resolution of comment #356 on D2.0.
14.5.2 mandates that any port that offers MDI-X connectivity shall be marked with an "X". That mandate makes no allowance for current technology in which many PHY implementations are not of a fixed configuration with respect to the cross-over function. I expect many implementations of 10BASE-Te to have automatic MDI-X correction.

Suggested Remedy
Revise text so that the X labeling requirement only applies to ports with fixed MDI/MDI-X configuration. It would be nice if we could all agree on a single character width symbol for auto-correction.

Response
REJECT.

This comment requests a change to the base standard that is not impacted by the changes made for 10BASE-Te.

It should be submitted as a maintenance request to the base standard.

There is mention of an "LPI agent" in this clause as the active element that causes the 100BASE-X PHY to go back and forth between LPI and normal operation. I find it strange that (a) there is no definition or specification of an LPI agent nor even any mention of it anywhere else in the draft, not even in the other clauses where one would expect a parallel use of such an agent to cause the same sort of switch for the other LPI PHYs (except 10BASE-Te).

Suggested Remedy
Fully define and specify the operation and service interfaces for the activating function for LPI (be it an "LPI agent" or other mechanism). Further, have that mechanism act on each of the LPI PHY's in a manner that is architecturally consistent across the entire standard.

Response
ACCEPT IN PRINCIPLE.

This comment was not considered by the BRC and the above response is a proposed response. This comment will be re-submitted for consideration at the Nov plenary along with all other comments received on D2.1.
The note is a bit confusing. It appears to be talking about implementation strategies rather than conformance issues. The critical issue the note needs to call to attention is conformance and interoperability.

**Suggested Remedy**

Change note to read:

NOTE - A 10BASE-Te PHY may not support operation with a 10BASE-T PHY unless the minimum cabling requirements for 10BASE-Te are met.

**Proposed Response**

This comment was not considered by the BRC and the above response is a proposed response.

This comment will be re-submitted for consideration at the Nov plenary along with all other comments received on D2.1.

The note was changed to clarify that support for 10BASE-T and 10BASE-Te in a single device is not expected. Interoperability between 10BASE-T and 10BASE-Te is addressed in 14.1.1.1 (i).

**Comment Type** TR  **Comment Status** D

**Comment Type** ER  **Comment Status** D

Inconsistent use of the term low power idle. For example, in 22.2.1 it is all in lower case. In 22.7a, it is Low Power Idle.

**Suggested Remedy**

Scrub the draft to use low power idle in a consistent manner.

**Proposed Response**

This comment was against D2.0 - it should be resolved by responses to D2.0 comment # 260.
Proposed Response

Comment Type TR  Comment Status D

There is a *LPI capability that is defined. This capability has a direct impact on the functions performed by the PCS and PMA, yet the only new PICS are for the timers.

Suggested Remedy
Shalls are needed to help define the way the PCS and PMA functions operate in LPI mode. Scrub the clause to make sure that functions modified or impacted by LPI have a corresponding PICS capability entry.

Proposed Response  Response Status W

PROPOSED ACCEPT.

See the response to comment #114.

This comment was not considered by the BRC and the above response is a proposed response.

This comment will be re-submitted for consideration at the Nov plenary along with all other comments received on D2.1.

Comment Type TR  Comment Status D

25.4.6 has three shall statements and only one PICS entry.

Suggested Remedy
Add other PICS entries or delete unnecessary shalls.

Proposed Response  Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See the response to comment #107.

This may be partly resolved by changes being made to satisfy the response to comment #410 but clause 25 still needs to be scrubbed for consistency between the Shall statements and the PICS.

This comment was not considered by the BRC and the above response is a proposed response.

This comment will be re-submitted for consideration at the Nov plenary along with all other comments received on D2.1.

Comment Type TR  Comment Status D

Sentence calls the subclause a clause and labels as optional. Given the volume of information and the need to conform with the information in 25.4.11, there should be a PICS entry associated with this.

Suggested Remedy
Change sentence to read: This subclause only applies to the optional low power idle is implemented. If implemented, the operation of the PMD shall comply with the requirements in this subclause.

Proposed Response  Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See the response to comment #104.

This may be partly resolved by changes being made to satisfy the response to comment #250.

The response to #250 does not explicitly call out the needed shall.

This comment was not considered by the BRC and the above response is a proposed response.

This comment will be re-submitted for consideration at the Nov plenary along with all other comments received on D2.1.
Proposed Response

Comment Type: TR
Comment Status: D

This comment reports an issue similar to that reported in comment #93 in CL 55. It relates to the state machine in Figure 49-14 and the definition of T_BLOCK_TYPE LI on pages 142 and 143. T_BLOCK_TYPE LI is specified as including cases with either 8 /LI/ or 4x/LI/+4x/I/. As the state machine in Figure 49-14 is currently defined this allows and requires transition to low power mode (TX_LI state) if either is detected. Transition to low power mode upon detection of 4x/LI/+4x/I/ should not be permitted. However, provision is required to allow for this special case while in the TX_LI state.

Suggested Remedy

Define LII as...

"LII: If the optional Low Power Idle function is supported then LII occurs when the vector contains four /LI/ control characters followed by four /I/ control characters."

Re-define LI as...

"LI: If the optional Low Power Idle function is supported then the LI type occurs when the vector contains eight control characters of /LI/.")

In Figure 49-14...

Change the criteria for transition for the following transition to include LII:

TX_C to TX_E
TX_INIT to TX_E
TX_D to TX_E
TX_E to TX_E
TX_T to TX_E

Change the criteria for transition from TX_LI to TX_LI (loop) to "T_TYPE(tx_raw)=(LI+LII)". Alternately, change the criteria for transition from TX_L to TX_C to "T_TYPE(tx_raw)=(I+LII)".

Proposed Response

This comment against draft 2.0 was received late and not processed at the task force meeting.

This should be resolved by the response to D2.0 comments #99 and #456