Logic Sub-Task Force
Agenda

IEEE P802.3ba
New Orleans January 2009

Mark Gustlin - Cisco
• Welcome and Introductions
• Approve the agenda
• Appoint / Volunteer Recording Secretary
• Resolve comments received for clauses
Logic Sub-Task Force Organisation

• Sub-Task Force Chair, Editor MII/RS and PCS Clauses
  Mark Gustlin

• Sub-Task Force Recording Secretary
  ??

• Editor Front Matter Clauses
  Ilango Ganga

• Editor Maintenance Clauses
  Hugh Barrass

• Editor PMA Clause
  Steve Trowbridge

• Editor FEC Clause
  Arthur Marris
All participants in this meeting have certain obligations under the IEEE-SA Patent Policy. Participants:

“Shall inform the IEEE (or cause the IEEE to be informed)” of the identity of each “holder of any potential Essential Patent Claims of which they are personally aware” if the claims are owned or controlled by the participant or the entity the participant is from, employed by, or otherwise represents

“Personal awareness” means that the participant “is personally aware that the holder may have a potential Essential Patent Claim,” even if the participant is not personally aware of the specific patents or patent claims

“Should inform the IEEE (or cause the IEEE to be informed)” of the identity of “any other holders of such potential Essential Patent Claims” (that is, third parties that are not affiliated with the participant, with the participant’s employer, or with anyone else that the participant is from or otherwise represents)

The above does not apply if the patent claim is already the subject of an Accepted Letter of Assurance that applies to the proposed standard(s) under consideration by this group

**Quoted text excerpted from IEEE-SA Standards Board Bylaws subclause 6.2**

- Early identification of holders of potential Essential Patent Claims is strongly encouraged
- No duty to perform a patent search
All participants should be familiar with their obligations under the IEEE-SA Policies & Procedures for standards development.

Patent Policy is stated in these sources:

IEEE-SA Standards Boards Bylaws

http://standards.ieee.org/guides/bylaws/sect6-7.html#6

IEEE-SA Standards Board Operations Manual


Material about the patent policy is available at

http://standards.ieee.org/board/pat/pat-material.html

If you have questions, contact the IEEE-SA Standards Board Patent Committee Administrator at patcom@ieee.org or visit
http://standards.ieee.org/board/pat/index.html

This slide set is available at http://standards.ieee.org/board/pat/pat-slideset.ppt
Call for Potentially Essential Patents

• If anyone in this meeting is personally aware of the holder of any patent claims that are potentially essential to implementation of the proposed standard(s) under consideration by this group and that are not already the subject of an Accepted Letter of Assurance:

  Either speak up now or

  Provide the chair of this group with the identity of the holder(s) of any and all such claims as soon as possible or

  Cause an LOA to be submitted
Other Guidelines for IEEE WG Meetings

• All IEEE-SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.

  Don’t discuss the interpretation, validity, or essentiality of patents/patent claims.

  Don’t discuss specific license rates, terms, or conditions.

  **Relative costs, including licensing costs of essential patent claims, of different technical approaches may be discussed in standards development meetings.**

  **Technical considerations remain primary focus**

  Don’t discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.

  Don’t discuss the status or substance of ongoing or threatened litigation.

  Don’t be silent if inappropriate topics are discussed … do formally object.

See *IEEE-SA Standards Board Operations Manual*, clause 5.3.10 and “Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association's Antitrust and Competition Policy” for more details.
## Logic Sub-Task Force Schedule

<table>
<thead>
<tr>
<th>Time</th>
<th>Wed</th>
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We will only cover the T and TRs in the logic track, the E and ERs will be resolved by the editors.

Comments will be covered in the order as shown in the table, an exception will likely be made for the management clauses based on Hugh availability.
Delays:
Clause 80 Comment #652 Add a row for 100GBASE-R PMA to delay table (affects Clause 83)
Clause 80 Comment #539 Add rows for FEC and AN to Delay constraints table (may affect clauses 73 and 74)
Clause 80 Comment #540 Provide “absolute time” in “ns” for delay constraints (Affects all clauses)
Clause 80 Comment #168 Delay constraints for 40G and 100G PMA (affects clauses 80 and 83)
Clause 80 Comment #166 Delete Editor’s note b under Table 80-2

Skew (most covered on Tuesday)
Clause 80 Comment #625 Change limits for Dynamic Skew at SP1 and SP2 (affects other clauses)
Clause 80 Comment #177 Add dynamic skew value for 100G-R PCS Table 80-4
Clause 00 Comment #622: Skew definition/terminology: Applies to all PMD/PCS/PMA clauses
Clause 80 Comment #411 Fig 80-2 and 80-3 very complex, need clarity on which skew to be measured for which options
Clause 80 Comment #553 Show XLAUI/CAUI optional in Fig 80-2 and 80-3, related to comment #411
Clause 80 Comment #653 Better clarification of dynamic skew description
Clause 80 Comment #430 Better clarification needed for skew description, measurement for MMF (may affect clauses 82 and 86)
Clause 80 Comment #280 Revise skew values in SP3 and SP4 (affects other clauses)
Clause 80 Comment #445 Revise skew values in SP3 and SP4, similar to comment #280 (affects other clauses)
Clause 80 Comment #642 (Editorial comment to change significant digits for 1 UI for 100GBASE-R) easy comment better to review if time permits

Layer diagrams:
Clause 00 Comment #537: Delete MEDIUM from abbreviation for S, C, K, L, E in layer diagrams
Clause 80 Comment #394: Include “Optional” for FEC sublayer

The remaining comments to be covered in page order
Clause 30
Comments: #345, 592, 346

Clause 45
#128, 130, 588, 658(TR), 132, 133, 317, 311, 193 (effects PMA)
#344, 316, 126(E) – PMA structure
Dependent on PMA decisions:
#131/659/660 - PMA test modes
#1 – loopback in PMA

All remaining comments to be covered in page order.
Comment #351: 74.7.4 Functions within the FEC sublayer, should be updated to include the operation of a bit serial interface for 40GBASE-R and 100GBASE-R PHYs.

Presentation: ganga_03_0109

The remaining comments to be covered in page order.
Comments #639, 289, 290, 272, 273, 274: All about how ordered sets are aligned now that we change the 64b/66b encoding.

Comment #318, 461, 199: All about test patterns.

The remaining comments to be covered in page order.
Test Patterns and Loopback (many covered on Tuesday)
Presentations: trowbridge_02, gustlin_04, chang_03, dawe_02, dawe_01 (optical track)

Layering and physical instantiations
Comments: 178, 179, 323, 324, 327, 369, 370, 395, 401, 508, 654
Presentations: trowbridge_01, dambrosia_01

Skew (covered on Tuesday)
Comments: 400, 616

Management related
Comments: 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 147, 148, 149, 150, 151, 152, 153, 154, 155, 180, 312, 320, 593, 598

The remaining comments to be covered in page order.
Thanks!