Logic Sub-Task Force
Agenda

IEEE P802.3ba
New Orleans   January 2010

Mark Gustlin - Cisco
Welcome and Introductions  
Approve the agenda  
Appoint / Volunteer Recording Secretary  
Call for Patents  
Resolve comments received for clauses
Logic Sub-Task Force Organization

- **Sub-Task Force Chair, Editor MII/RS and PCS Clauses**
  Mark Gustlin

- **Sub-Task Force Recording Secretary**
  Farhad Shafai

- **Editor Front Matter Clauses**
  Ilango Ganga

- **Editor Maintenance Clauses**
  Hugh Barrass

- **Editor PMA Clause**
  Steve Trowbridge

- **Editor Copper related Clauses**
  Arthur Marris
Attendance

• Don’t forget to log your attendance today

https://seabass.ieee.org/imat/

Plus the secret code….xxxxxxxx
All participants in this meeting have certain obligations under the IEEE-SA Patent Policy. Participants:

“Shall inform the IEEE (or cause the IEEE to be informed)” of the identity of each “holder of any potential Essential Patent Claims of which they are personally aware” if the claims are owned or controlled by the participant or the entity the participant is from, employed by, or otherwise represents

“Personal awareness” means that the participant “is personally aware that the holder may have a potential Essential Patent Claim,” even if the participant is not personally aware of the specific patents or patent claims

“Should inform the IEEE (or cause the IEEE to be informed)” of the identity of “any other holders of such potential Essential Patent Claims” (that is, third parties that are not affiliated with the participant, with the participant’s employer, or with anyone else that the participant is from or otherwise represents)

The above does not apply if the patent claim is already the subject of an Accepted Letter of Assurance that applies to the proposed standard(s) under consideration by this group

Quoted text excerpted from IEEE-SA Standards Board Bylaws subclause 6.2

• Early identification of holders of potential Essential Patent Claims is strongly encouraged
• No duty to perform a patent search
All participants should be familiar with their obligations under the IEEE-SA Policies & Procedures for standards development.

Patent Policy is stated in these sources:

IEEE-SA Standards Boards Bylaws

http://standards.ieee.org/guides/bylaws/sect6-7.html#6

IEEE-SA Standards Board Operations Manual


Material about the patent policy is available at

http://standards.ieee.org/board/pat/pat-material.html

If you have questions, contact the IEEE-SA Standards Board Patent Committee Administrator at patcom@ieee.org or visit
http://standards.ieee.org/board/pat/index.html

This slide set is available at http://standards.ieee.org/board/pat/pat-slideset.ppt
Call for Potentially Essential Patents

If anyone in this meeting is personally aware of the holder of any patent claims that are potentially essential to implementation of the proposed standard(s) under consideration by this group and that are not already the subject of an Accepted Letter of Assurance:

- Either speak up now or
- Provide the chair of this group with the identity of the holder(s) of any and all such claims as soon as possible or
- Cause an LOA to be submitted
Other Guidelines for IEEE WG Meetings

• All IEEE-SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.

  Don’t discuss the interpretation, validity, or essentiality of patents/patent claims.

  Don’t discuss specific license rates, terms, or conditions.

  Relative costs, including licensing costs of essential patent claims, of different technical approaches may be discussed in standards development meetings.

  Technical considerations remain primary focus

  Don’t discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.

  Don’t discuss the status or substance of ongoing or threatened litigation.

  Don’t be silent if inappropriate topics are discussed … do formally object.

See IEEE-SA Standards Board Operations Manual, clause 5.3.10 and “Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association’s Antitrust and Competition Policy” for more details.
Logic Sub-Task Force Schedule

- Schedule is subject to change depending on comment resolution progress

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We will cover all TR, T, ER, GR and G comments in the logic track. All E comments are to be resolved by the editors (unless someone wants a particular E reviewed).

289 comments to cover in the logic sub task force (minus a few that are covered in the common time or moved to other groups).

Comments will be covered in the order as shown in the table (Management might be an exception depending on editor availability).
The comments will be covered in page order.
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RS/MII/PCS (81 and 82)

#447, 810, 277: Approximate values
#75, 278: Link fault SM
#748, 459: Register numbering
#106, 464: Counter size
#266, 752: PCS lane naming on receive, gustlin_04_0110
#189, 192: C code
#259, 457: Labeling of PCS lanes
#165, 472: PICS separation
#279, 104: am_reset_cnt

Remaining comments to be covered in page order.
Comments to be covered in page order.
Thanks!