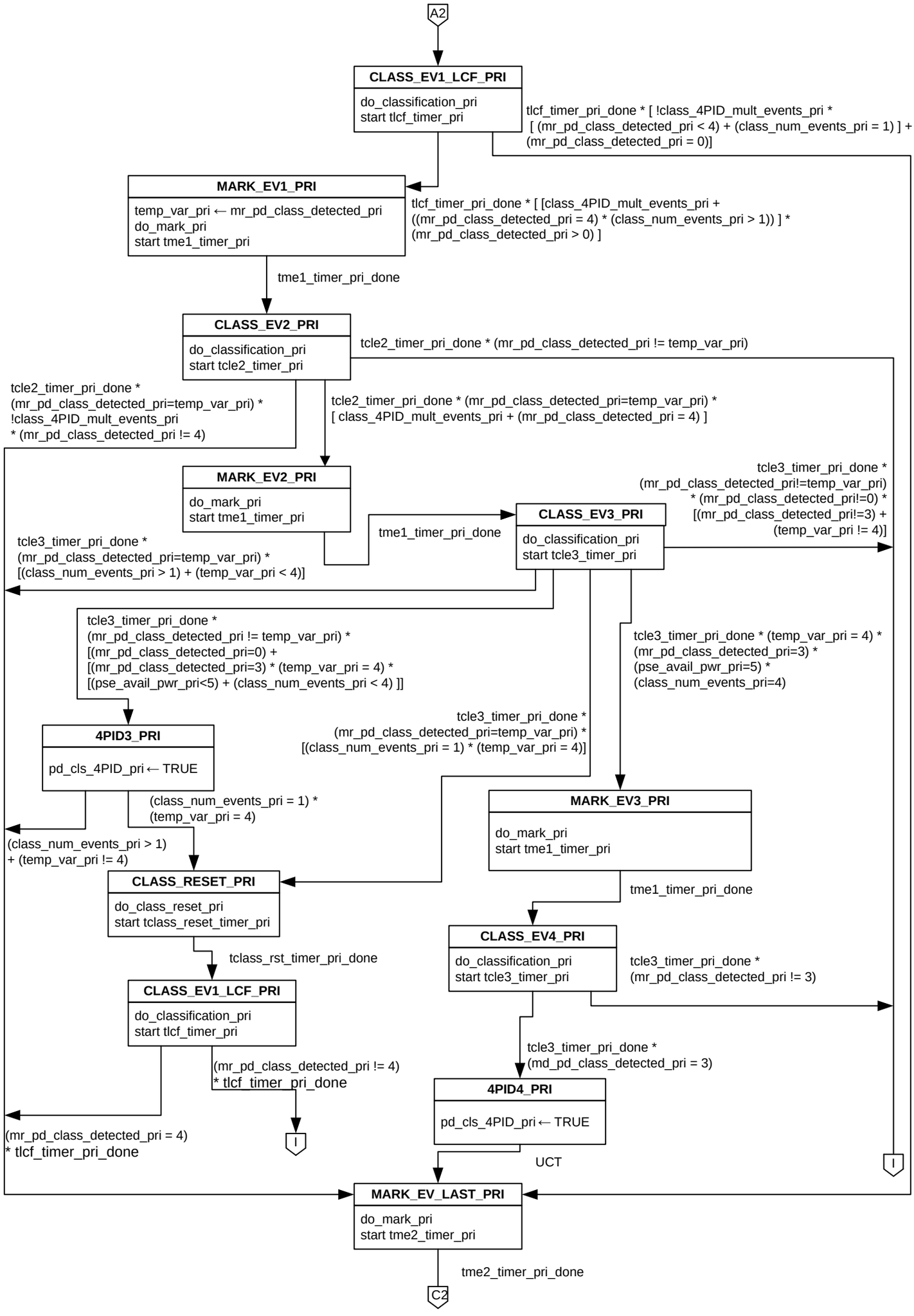
A2
CLASS_EV1_LCF_PRI
do_classification_pri
start tlcf_timer_pri
tlcf_timer_pri_done * [!class_4PID_mult_events_pri *
[(mr_pd_class_detected_pri < 4) + (class_num_events_pri = 1)] +
(mr_pd_class_detected_pri = 0)]
MARK_EV1_PRI
temp_var_pri ← mr_pd_class_detected_pri
do_mark_pri
start tme1_timer_pri
tlcf_timer_pri_done * [[class_4PID_mult_events_pri +
((mr_pd_class_detected_pri = 4) * (class_num_events_pri > 1))] *
(mr_pd_class_detected_pri > 0)]
tme1_timer_pri_done
CLASS_EV2_PRI
do_classification_pri
start tcle2_timer_pri
tcle2_timer_pri_done * (mr_pd_class_detected_pri != temp_var_pri)
tcle2_timer_pri_done *
(mr_pd_class_detected_pri=temp_var_pri) *
!class_4PID_mult_events_pri
* (mr_pd_class_detected_pri != 4)
tcle2_timer_pri_done * (mr_pd_class_detected_pri=temp_var_pri) *
[class_4PID_mult_events_pri + (mr_pd_class_detected_pri = 4)]
MARK_EV2_PRI
do_mark_pri
start tme1_timer_pri
tme1_timer_pri_done
CLASS_EV3_PRI
do_classification_pri
start tcle3_timer_pri
tcle3_timer_pri_done *
(mr_pd_class_detected_pri!=temp_var_pri)
* (mr_pd_class_detected_pri!=0) *
[(mr_pd_class_detected_pri!=3) +
(temp_var_pri != 4)]
tcle3_timer_pri_done *
(mr_pd_class_detected_pri=temp_var_pri) *
[(class_num_events_pri > 1) + (temp_var_pri < 4)]
tcle3_timer_pri_done *
(mr_pd_class_detected_pri != temp_var_pri) *
[(mr_pd_class_detected_pri=0) +
[(mr_pd_class_detected_pri=3) * (temp_var_pri = 4) *
[(pse_avail_pwr_pri<5) + (class_num_events_pri < 4)]]
tcle3_timer_pri_done * (temp_var_pri = 4) *
(mr_pd_class_detected_pri=3) *
(pse_avail_pwr_pri=5) *
(class_num_events_pri=4)
tcle3_timer_pri_done *
(mr_pd_class_detected_pri=temp_var_pri) *
[(class_num_events_pri = 1) * (temp_var_pri = 4)]
4PID3_PRI
pd_cls_4PID_pri ← TRUE
(class_num_events_pri = 1) *
(temp_var_pri = 4)
(class_num_events_pri > 1)
+ (temp_var_pri != 4)
MARK_EV3_PRI
do_mark_pri
start tme1_timer_pri
CLASS_RESET_PRI
do_class_reset_pri
start tclass_reset_timer_pri
tme1_timer_pri_done
CLASS_EV4_PRI
do_classification_pri
start tcle3_timer_pri
tcle3_timer_pri_done *
(mr_pd_class_detected_pri != 3)
tclass_rst_timer_pri_done
CLASS_EV1_LCF_PRI
do_classification_pri
start tlcf_timer_pri
tcle3_timer_pri_done *
(md_pd_class_detected_pri = 3)
(mr_pd_class_detected_pri != 4)
* tlcf_timer_pri_done
4PID4_PRI
pd_cls_4PID_pri ← TRUE
(mr_pd_class_detected_pri = 4)
* tlcf_timer_pri_done
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MARK_EV_LAST_PRI
do_mark_pri
start tme2_timer_pri
tme2_timer_pri_done
C2