

IEEE P802.3cq D3p1 2-Pair PoE Maintenance 1st Sponsor recirculation ballot comments

Cl 0 SC 0 P L # r01-1

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Comment Type E Comment Status A

This draft meets all editorial requirements.

SuggestedRemedy

Response Response Status C

ACCEPT.

Cl 0 SC 0 P0 L0 # r01-2

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Comment Type TR Comment Status A

MR 1322 (see [http://www.ieee802.org/3/maint/requests/maint\\_1322.pdf](http://www.ieee802.org/3/maint/requests/maint_1322.pdf)) still needs to be addressed.

As a result .3cq deprecated 33.5: (NOTE--33.5 has been deprecated. Since May 2019, maintenance changes are no longer being considered for this sub- clause. ).

However, 802.3.2 YANG was written against a version of 802.3 that predates the AT amendment (so pre-2009) and contains pointers to things that no longer exist in the Clause 33 state diagram, specifically ERROR\_DELAY\_OVER and ERROR\_DELAY\_SHORT. Clause 33 incorrectly calls these non-existent states. The MR was merely helping .3cq identify orphaned references and how to fix them, yet the comments submitted against D1.0 of .3cq were rejected.

SuggestedRemedy

Please fix references per MR 1322.

Response Response Status C

ACCEPT IN PRINCIPLE.

Changes requested in MR 1322 are:

In 30.9.1.1.9, change:

"This counter is incremented when the PSE state diagram (Figure 33–9) enters the state ERROR\_DELAY\_OVER." to:

"This counter is incremented when the PSE state diagram (Figure 33–9) enters the state ERROR\_DELAY due to the ovid\_detected variable being TRUE."

In 30.9.1.1.10, change:

"This counter is incremented when the PSE state diagram (Figure 33–9) enters the state ERROR\_DELAY\_SHORT." to:

"This counter is incremented when the PSE state diagram (Figure 33–9) enters the state ERROR\_DELAY due to the short\_detected variable being TRUE."

In 33.5.1.2.7, change:

"This bit shall be set to one when the PSE state diagram (Figure 33–9) enters the state 'ERROR\_DELAY.'" to:

"This bit shall be set to one when the PSE state diagram (Figure 33–9) enters the state 'ERROR\_DELAY' due to the short\_detected variable being TRUE."

In 33.5.1.2.8, change:

"This bit shall be set to one when the PSE state diagram (Figure 33–9) enters the state 'ERROR\_DELAY\_OVER.'" to:

"This bit shall be set to one when the PSE state diagram (Figure 33–9) enters the state 'ERROR\_DELAY' due to the ovid\_detected variable being TRUE."

In 33.8.3.7 Management function requirements, item MF28, change:

"Bit indicates a short circuit condition has been detected. Set to one entering ERROR\_DELAY state." to:

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“Bit indicates a short circuit condition has been detected. Set to one entering ERROR\_DELAY state due to the short\_detected variable being TRUE.”

In 33.8.3.7 Management function requirements, item MF30, change:

“Bit indicates an overload condition has been detected. Set to one when entering the ERROR\_DELAY\_OVER state” to:

“Bit indicates an overload condition has been detected. Set to one when entering the ERROR\_DELAY state due to the ovd\_detected variable being TRUE”