



May 13, 2015

To: John D'Ambrosia and members of the IEEE P802.3bs 400 Gb/s Ethernet Task Force

CC: David Law (IEEE 802.3 WG Chair), Pete Anslow (IEEE 802.3 WG Secretary) and members of IEEE 802.3

Subject: Progress on OIF CEI-56G projects

From: Nathan Tracy, OIF Technical Committee Chair (ntracy@te.com)

Dear Mr. D'Ambrosia,

Thank you for your liaison statement concerning the P802.3bs adopted baselines through the Berlin plenary. Given the decision to adopt PAM4 electrical modulation for the CDAUI-8 chip-to-chip and chip-to-module interfaces, we believe that OIF activity that may be of most relevance for your work are the CEI-56G-VSR-PAM4 (oif2014.230.03, attached) and CEI-56G-MR-PAM4 (oif2014.245.03, attached) implementation agreements.

At our 2Q2015 meeting in Lisbon, we have completed resolution of comments received on the previous ballots and agreed to issue the updated draft text for a third straw ballot.

As you review the drafts, please consider that the solutions are in the process of iterative review and technical changes may still occur prior to finalization of these implementation agreements.

We request that you acknowledge the OIF in any derivative work.

Sincerely,



Nathan Tracy,
OIF Technical Committee Chair (ntracy@te.com)

Attach:
oif2014.230.03
oif2014.245.03