

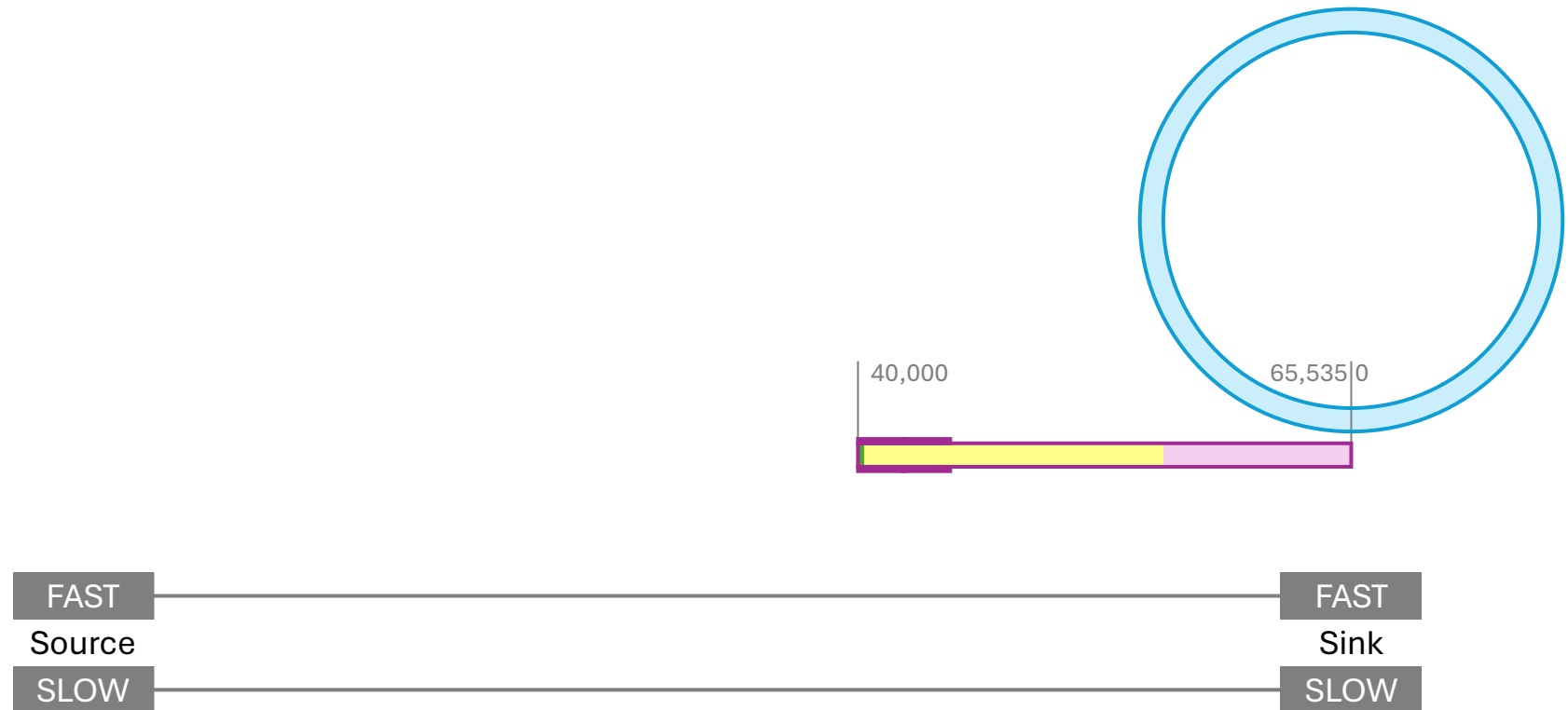
802.1CBec FRER Sequence Recovery State Diagrams

Single state machine covering both INIT & NORM sequence spaces

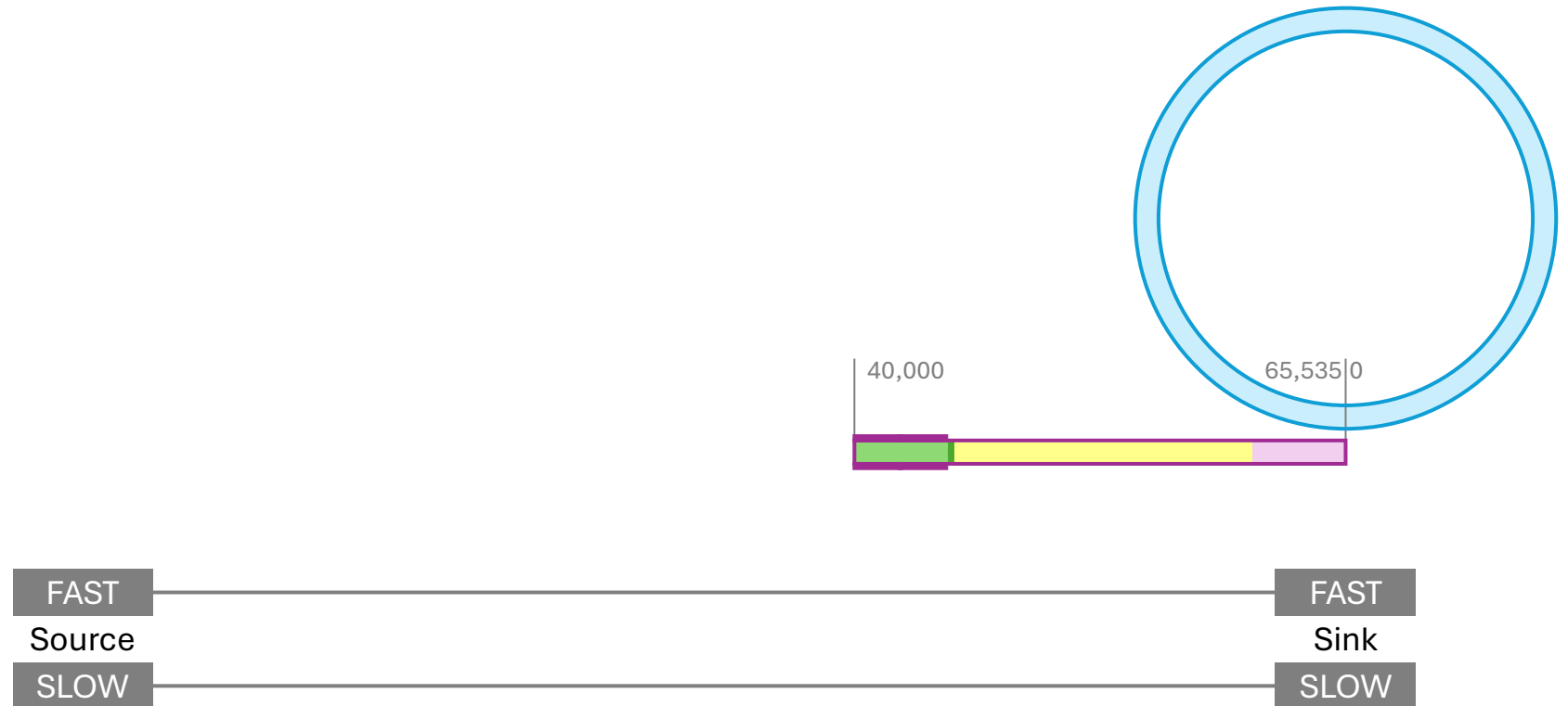
David McCall – Intel Corporation

16 September 2026

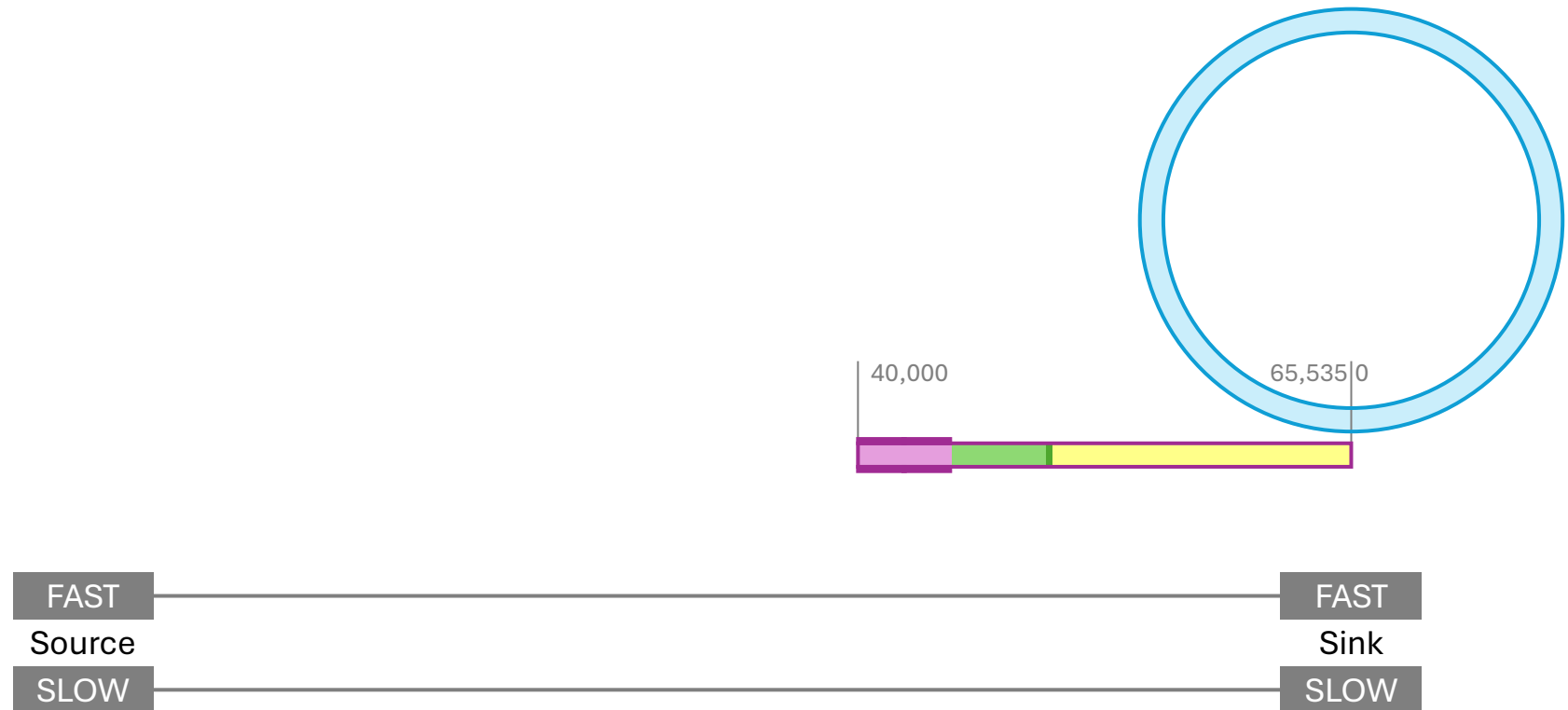
Proposed Vector Recovery Algorithm



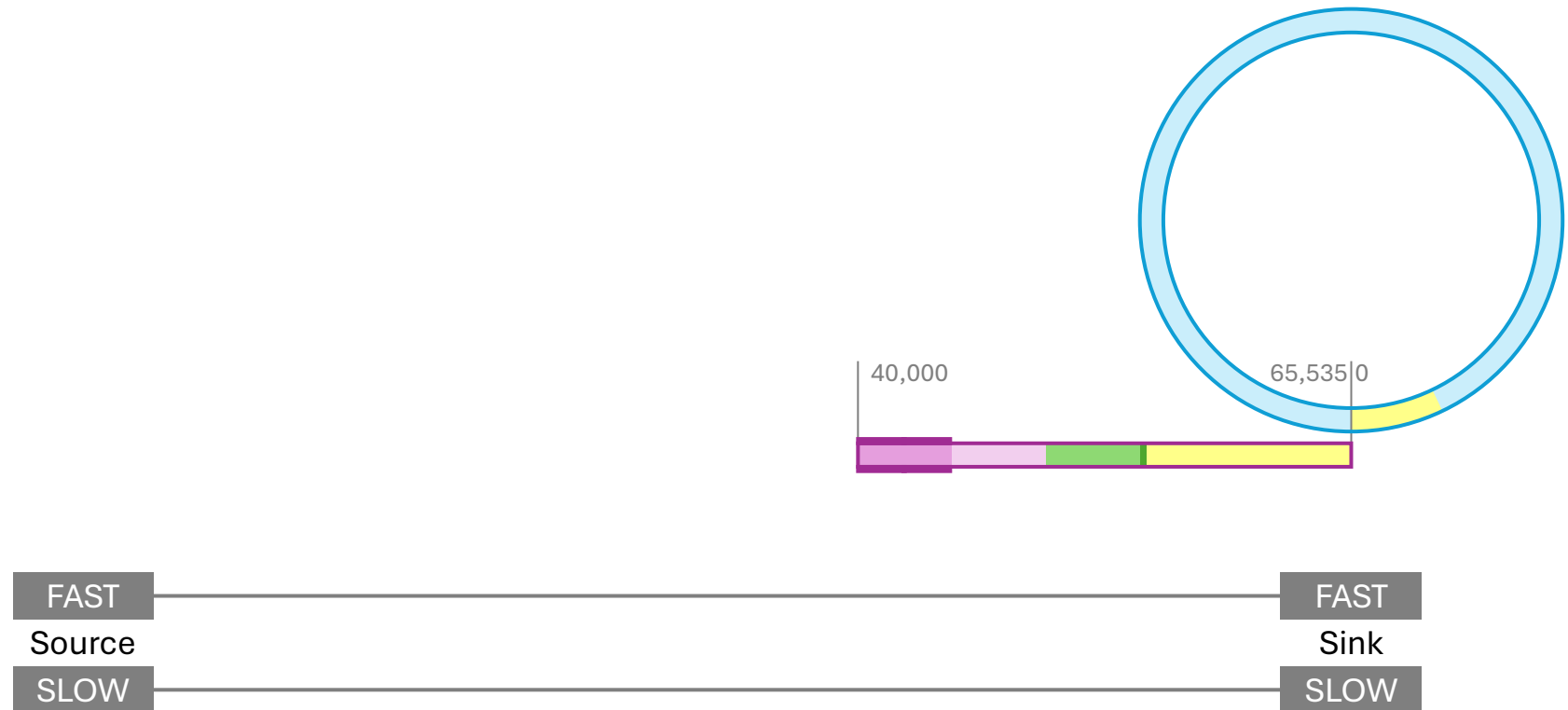
Proposed Vector Recovery Algorithm



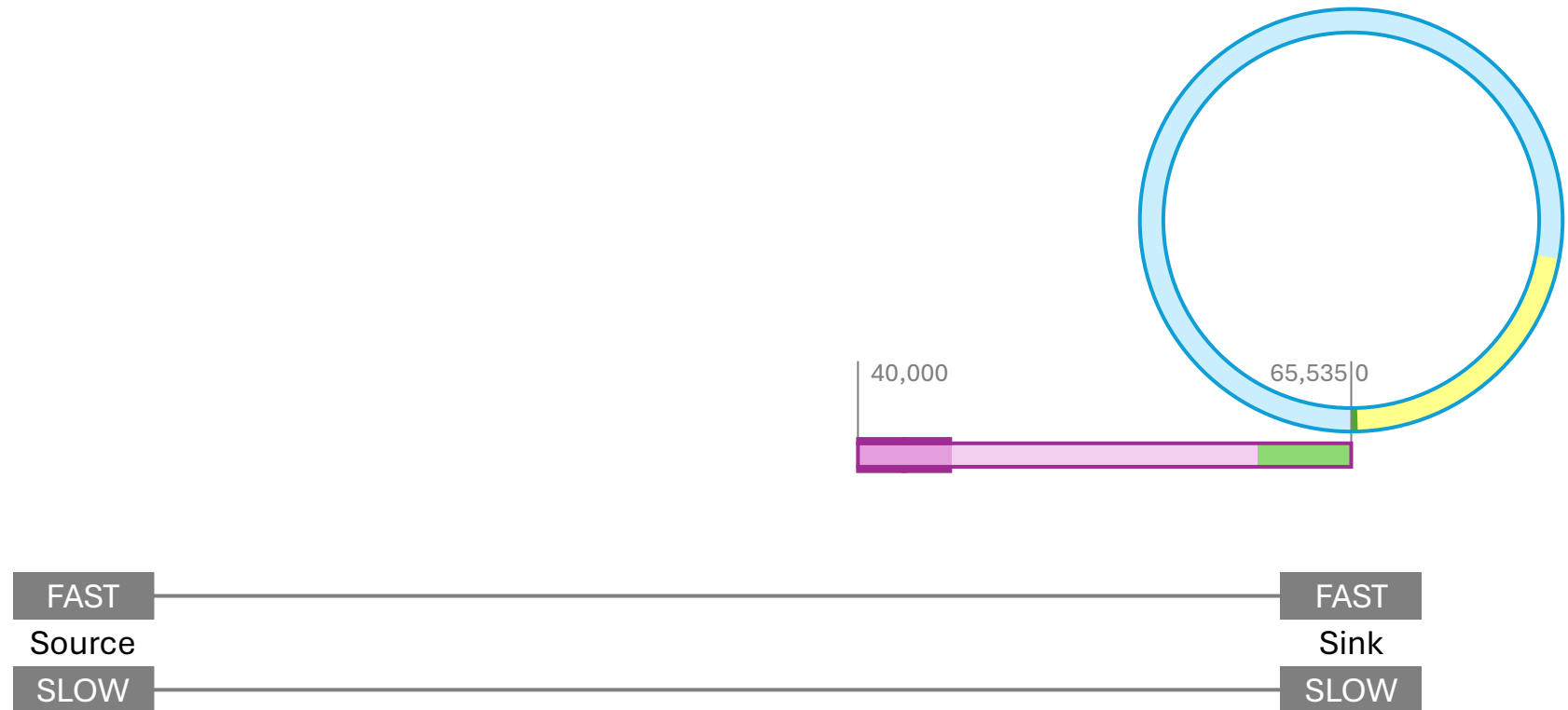
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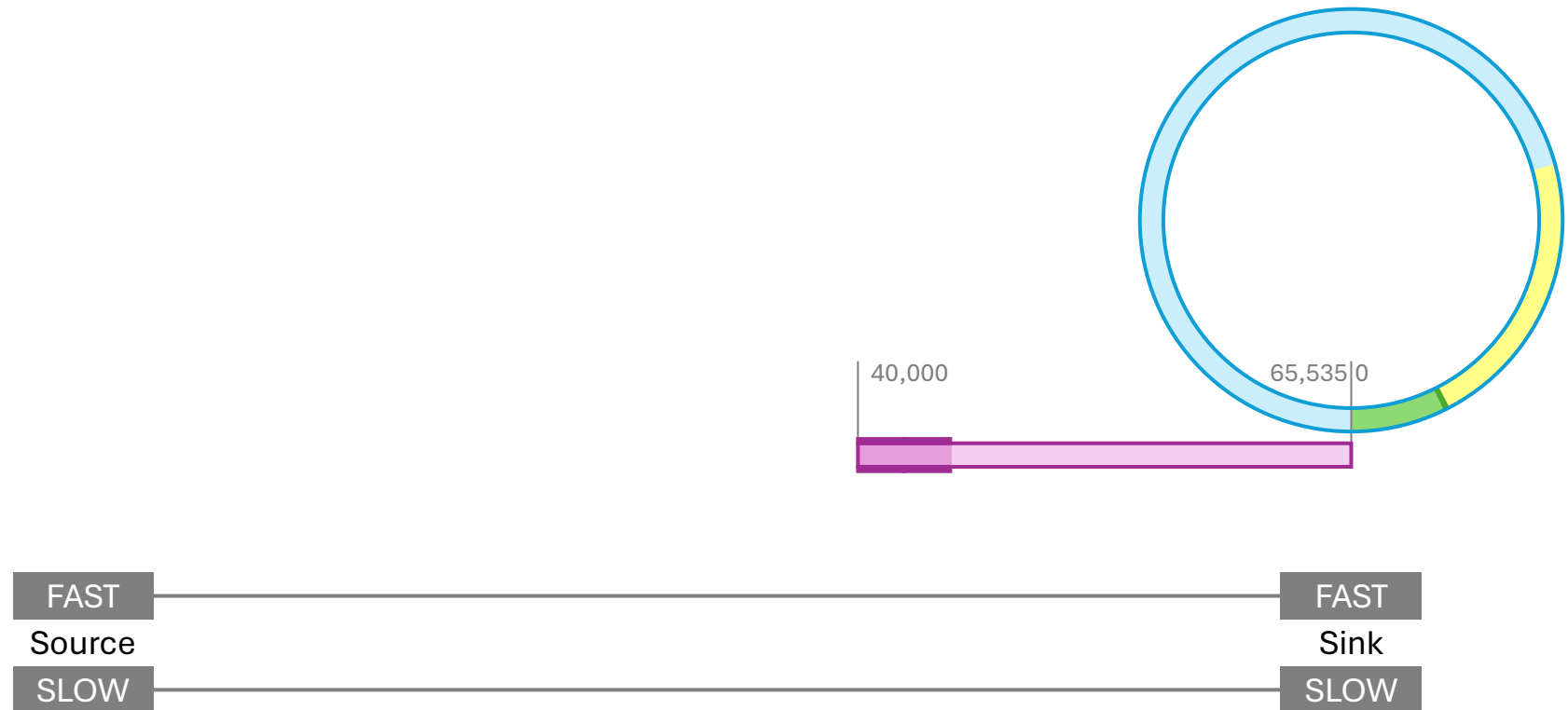
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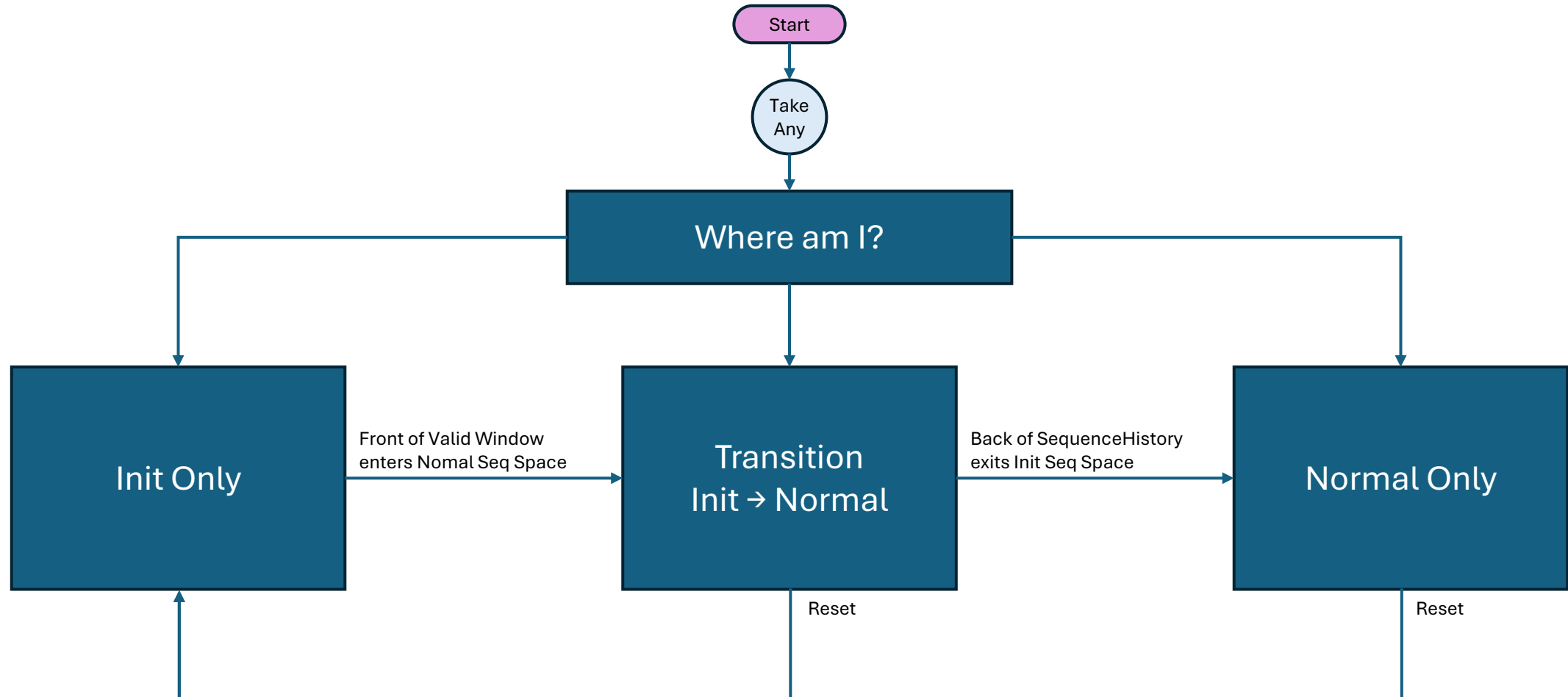
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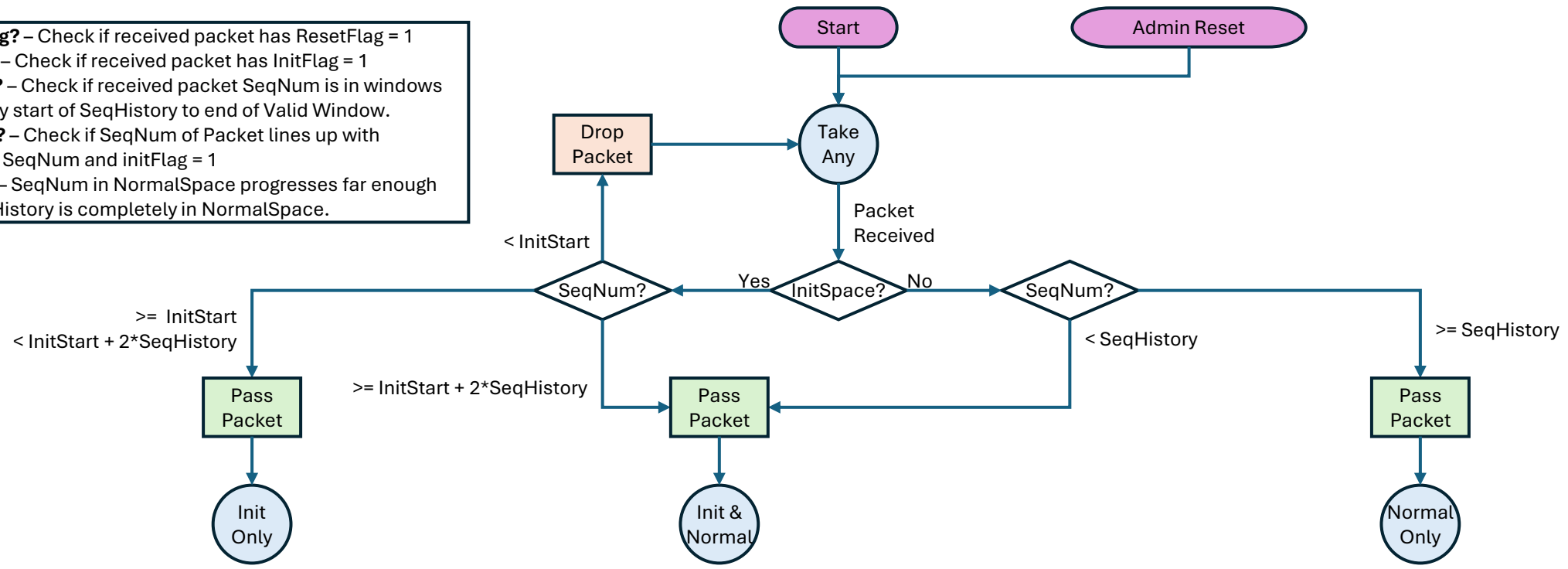
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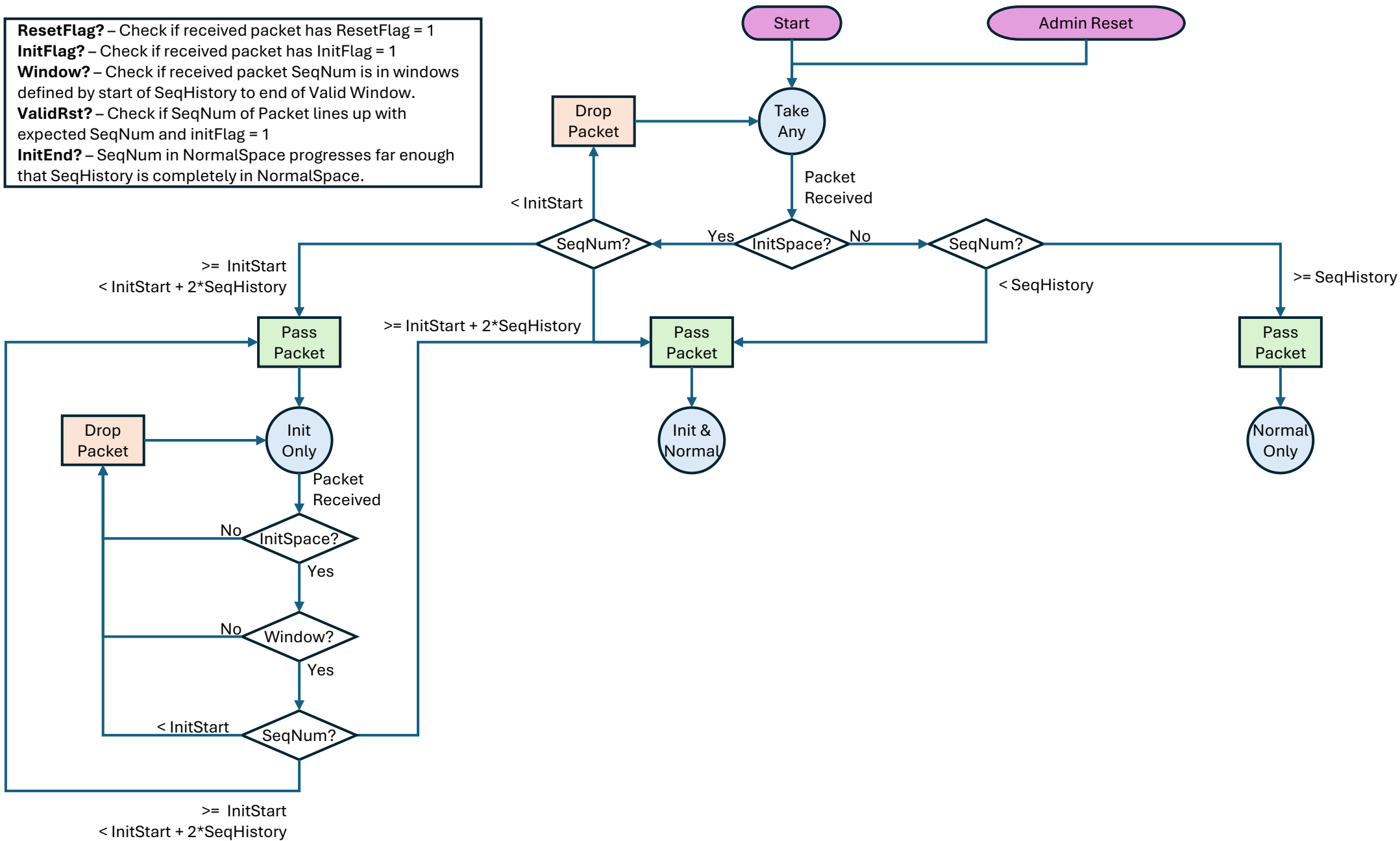
Sequence Recovery - Single State Diagram Using Init Space



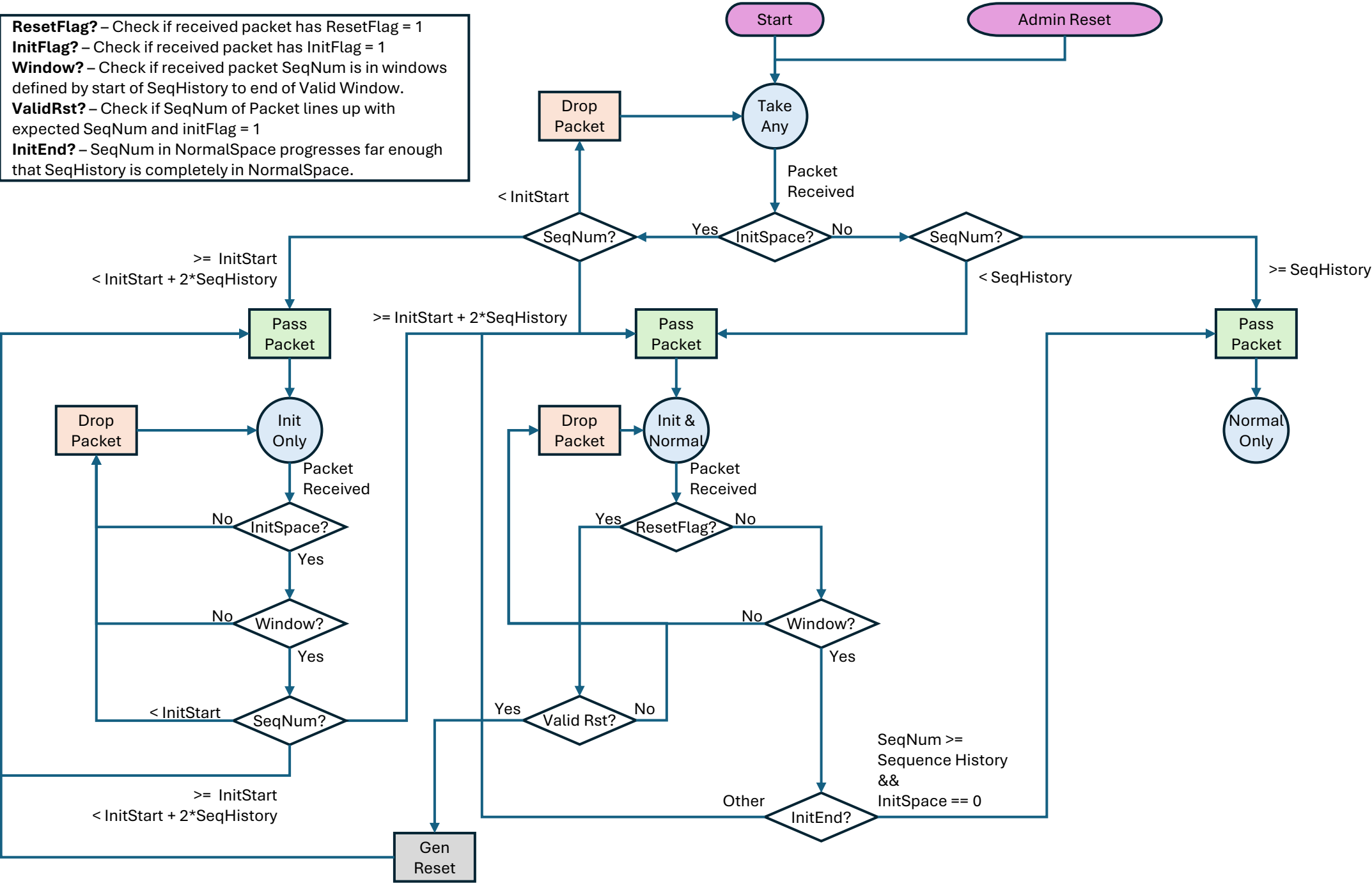
ResetFlag? – Check if received packet has ResetFlag = 1
InitFlag? – Check if received packet has InitFlag = 1
Window? – Check if received packet SeqNum is in windows defined by start of SeqHistory to end of Valid Window.
ValidRst? – Check if SeqNum of Packet lines up with expected SeqNum and initFlag = 1
InitEnd? – SeqNum in NormalSpace progresses far enough that SeqHistory is completely in NormalSpace.



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