

February 1998

Doc: IEEE P802.11-98/74

IEEE P802.11 Wireless LANs

Comparison DATA of QPSK modulation

February, 1998

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General Description

Parameter	Value(s)
Data Rates Supported	25Mbps, 20.3125Mbps
Channel Spacing	18.75Mhz
Center Frequencies	5162.5, 5181.25, 5200.0, 5218.75, 5237.5 for loewer band [MHz] 5262.5, 5282.25, 5300.0, 5318.75, 5337.5 for middle band [MHz] 5746.875, 5765.625, 5784.375, 5803.125 for upper band [MHz]
Power Levels	50mW for 5162.5, 5181.25, 5200.0, 5218.75, 5237.5 MHz 250mW for 5262.5, 5282.25, 5300.0, 5318.75, 5337.5 MHz 1W for 5746.875, 5765.625, 5784.375, 5803.125 MHz
Sensitivities	-76dbm for 20.3125Mbps, -74dbm for 25Mbps
CCA threshold	-78dbm
Clock Rate accuracy	+/- 5ppm
Carrier Frequency accuracy	+/- 10ppm
Waveform implementation accuracy specification method	RMS value of error signal power between actual tranmitted signals around ideal signal points sampled at an ideal sample timing
Power Backoff in RF PA	3dB for 25Mbps and 20.3125Mbps
Implementation Complexity	depending on receiver architecture 30k gates for modems except equalizer 40k gates for simple equalizer 400k gates for complex equalizer

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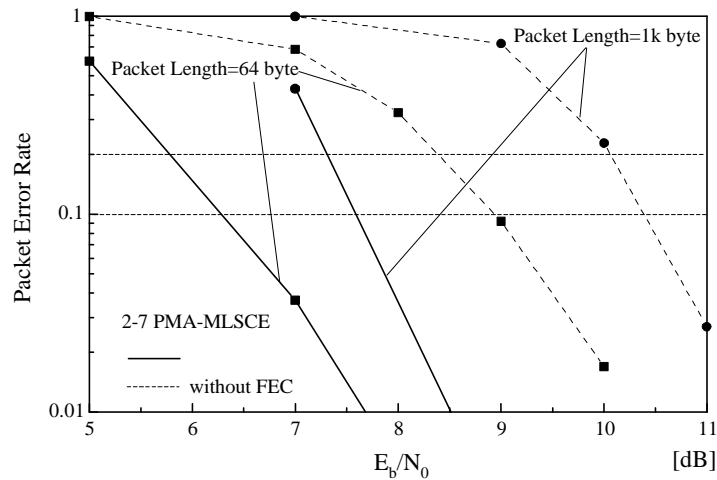
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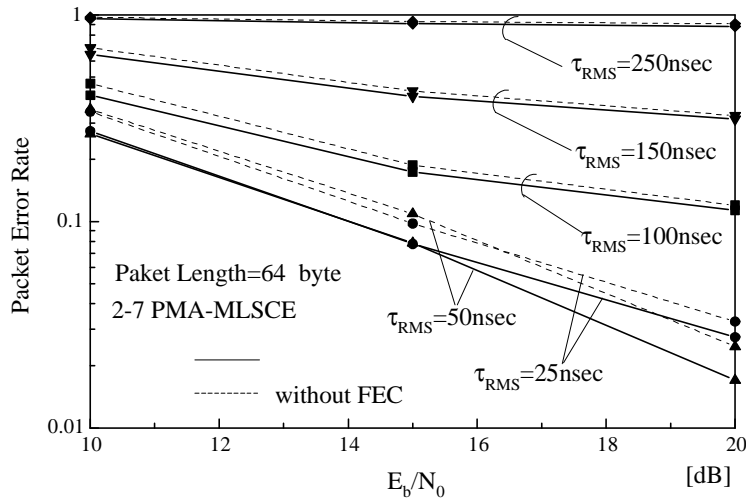
Per-Rate Feature Summary

Parameter	Rate A	RateB
Data rate	25Mbps	20.3125Mbps
ECC method	No	(31,26) expanded Hamming code
Interleaving method	No	No
Suggested minimal sensitivity	-74dbm	-76dbm
Suggested Co-Channel rejection	DUR more than or equal to 10dB	DUR more than or equal to 10dB
Suggested Adjacent Channel rejection	DUR more than or equal to -10dB	DUR more than or equal to -10dB
Suggested Alternate Channel rejection	DUR more than or equal to -25dB	DUR more than or equal to -25dB
Implementation Accuracy	RMS of the error signal power < 0.125	RMS of the error signal power < 0.125

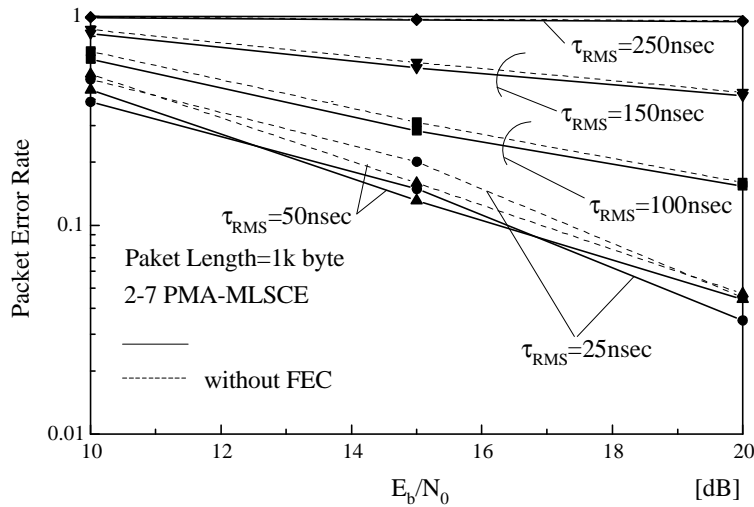
PER in AWGN channel



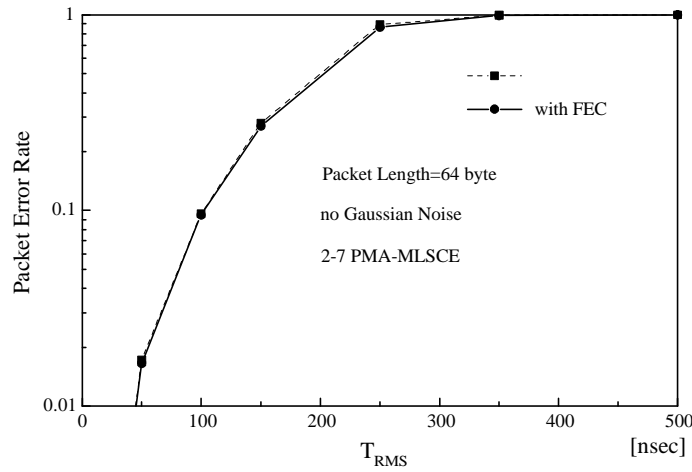
PER in Fading with Gaussian Noise (64byte-packet) (Simple Implementation)



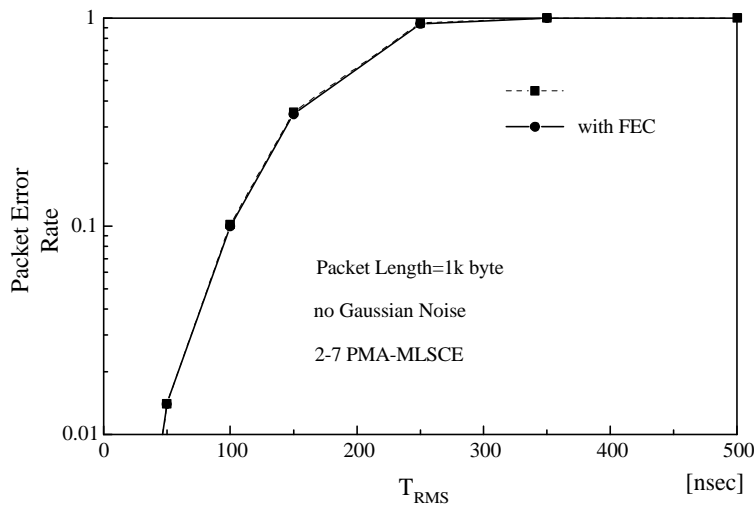
PER in Fading with Gaussian Noise (1kbyte-packet) (Simple Implementation)



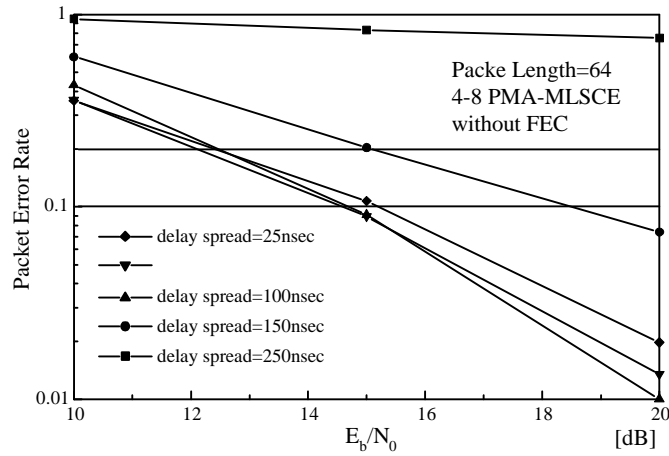
PER versus T_{RMS} (64byte-Packet) (Simple Implementation)



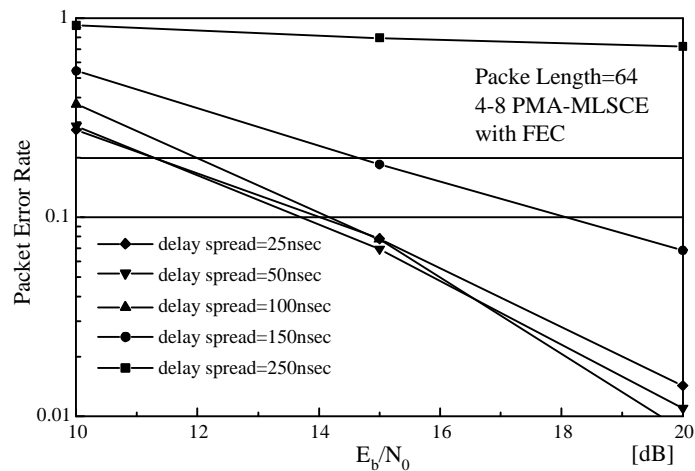
PER versus T_{RMS} (1kbyte-Packet) (Simple Implementation)



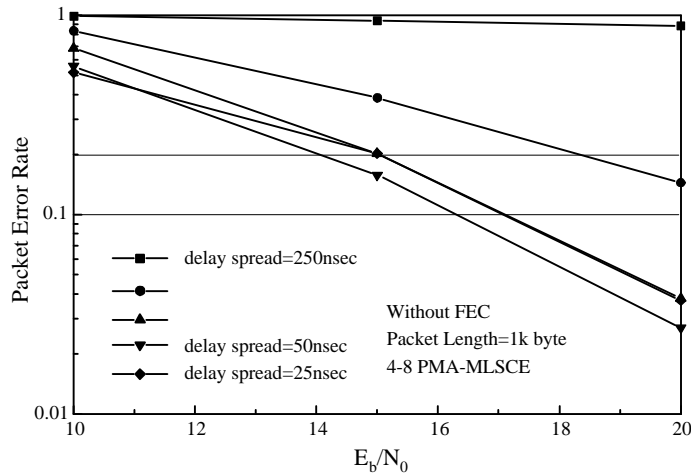
PER in Fading with Gaussian Noise without FEC (64byte-packet) (Complex Implementation)



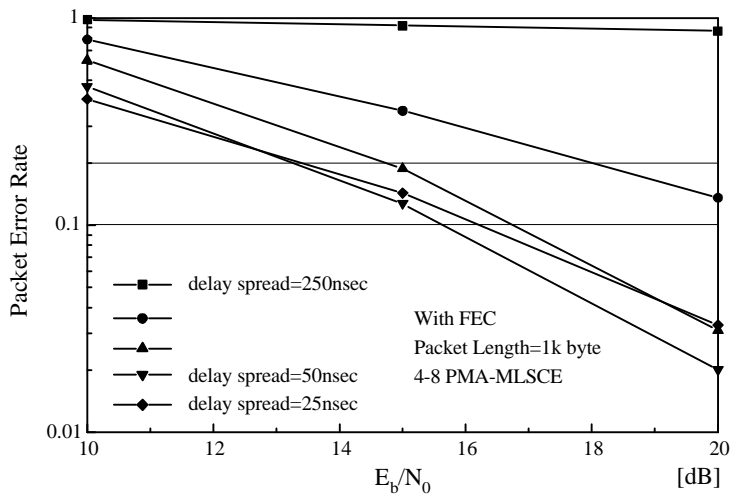
PER in Fading with Gaussian Noise with FEC (64byte-packet) (Complex Implementation)



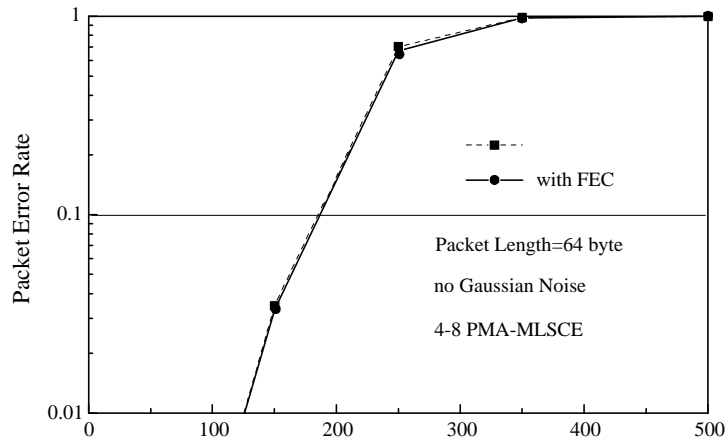
PER in Fading with Gaussian Noise without FEC (1kbyte-packet) (Complex Implementation)



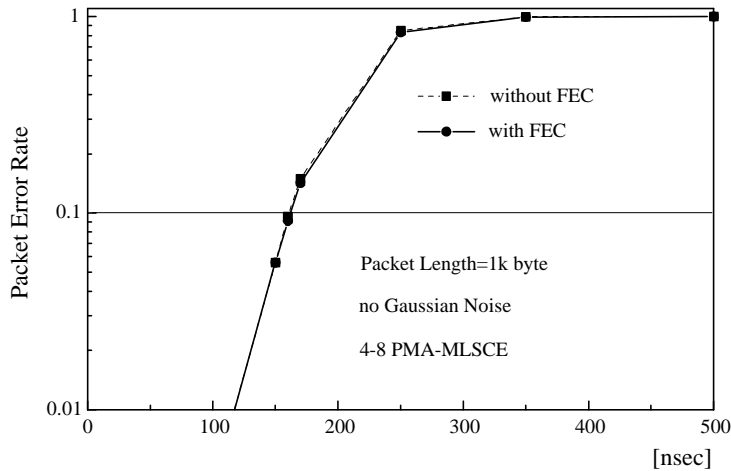
PER in Fading with Gaussian Noise with FEC (1kbyte-packet) (Complex Implementation)



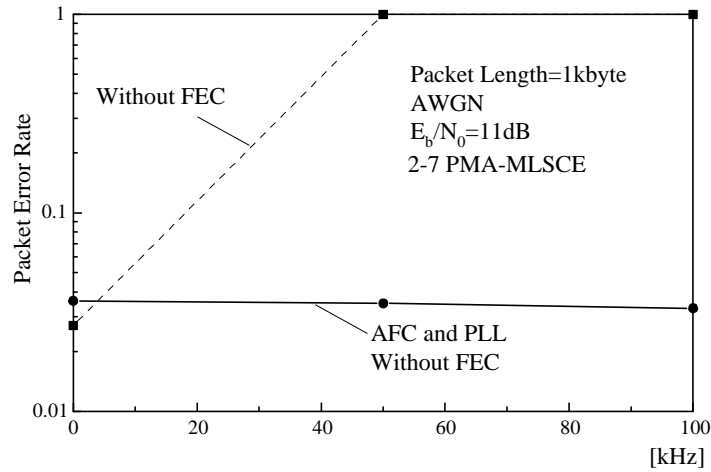
PER versus T_{RMS} (64byte-Packet) (Complex Implementation)



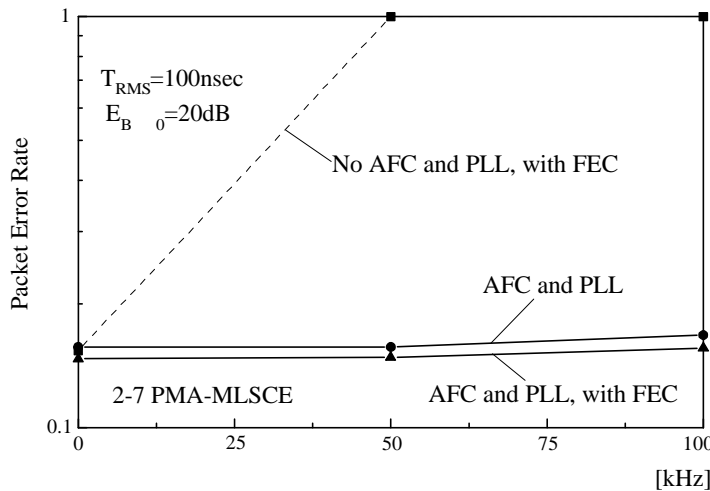
PER versus T_{RMS} (1kbyte-Packet) (Complex Implementation)



PER versus Frequency offset (AWGN)



PER versus Frequency offset (Fading+G-Noise)



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Per-Rate Performance Summary (Simple Implementation)

Parameter	25Mbps	20.3125Mbps
Eb/No at PER=10%, AWGN, 64b	9dB	6.5dB
Trms at PER=10%, noise free, 64b	100nsec	100nsec
Eb/No @ 20%, with Trms @ 10%, 64b	20dB	20dB
Eb/No at PER=10%, AWGN, 1000b	10.5dB	8dB
Trms at PER=10%, noise free, 1000b	100nsec	100nsec
Eb/No @ 20%, with Trms @ 10%, 1000b	20dB	20dB
CCI immunity [dB]	10	10
ACI immunity [dB]	-12	-11
CW jammer immunity [dB]	28	28
Narrowband Gaussian noise immunity [dB]	9	< 7
Phase noise tolerance, (BW=50 kHz), rad ² [dBc] at which PER becomes 10%	7($\Psi_{RMS}=0.2$)	7($\Psi_{RMS}=0.2$)

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Per-Rate Performance Summary (Complex Implementation)

Parameter	25Mbps	20.3125Mbps
Eb/No at PER=10%, AWGN, 64b	9dB	6.5dB
Trms at PER=10%, noise free, 64b	170nsec	170nsec
Eb/No @ 20%, with Trms @ 10%, 64b	20dB	20dB
Eb/No at PER=10%, AWGN, 1000b	10.5dB	8dB
Trms at PER=10%, noise free, 1000b	160nsec	160nsec
Eb/No @ 20%, with Trms @ 10%, 1000b	20dB	20dB
CCI immunity [dB]	10dB	10dB
ACI immunity [dB]	-11dB	-11dB
CW jammer immunity [dB]	28dB	28dB
Narrowband Gaussian noise immunity [dB]	9dB	9dB
Phase noise tolerance, (BW=50 kHz), rad ² [dBc] at which PER becomes 10%	7($\Psi_{RMS}=0.2$)	<7($\Psi_{RMS}=0.2$)

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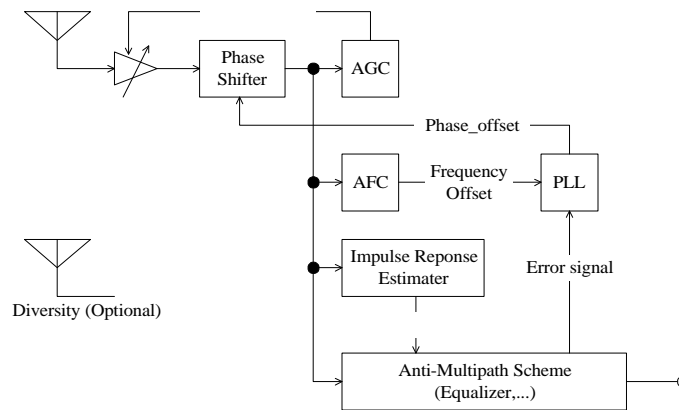
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Timing and Overhead related Summary

Attribute	Suggested Value
aSlotTime	26.5μsec
aCCATime	4.5μsec
aRxTxTurnaroundTime	9 μsec
aTxPLCPDelay	1 μsec
aRxTxSwitchTime	5 μsec
aTxRampOnTime	2μsec
aTxRFDelay	1μsec
aSIFSTime	22.5μsec
aRxRFDelay	4μsec
aRxPLCPDelay	2.5μsec
aMACProcessingDelay	2μsec
aTxRampOffTime	2μsec
aPreambleLength	10.56μsec
aPLCPHdrLength	1.28μsec
aMPDUDurationFactor	1.0 for 25Mbps, 1.23077 for 20.3125Mbps
aAirPropagationTime	0.5μsec
aCWmin	15
aCWmax	1023

Receiver Structure (1) -Basic structure-



Receiver Structure (2) -Example of Anti-Multipath scheme -

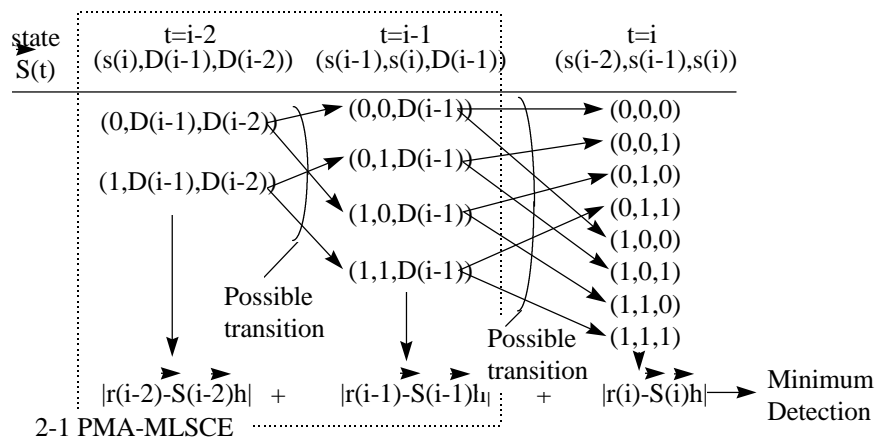
Past Metric Addition MLSCE (PMA-MLSCE)

- Symbol-by-Symbol detection based on Maximum Likelihood Sequence Estimation during Channel Impulse Response duration
- Low calculation amount by using decision results

Receiver Structure (3)

-State Transition of PMA-MLSCE -

Example for channel impulse response(h) of length 3 and binary symbol

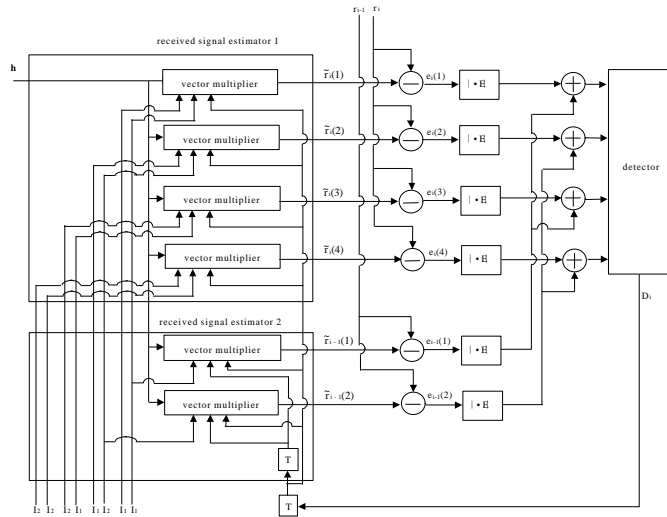


$s(i)$: symbol candidate

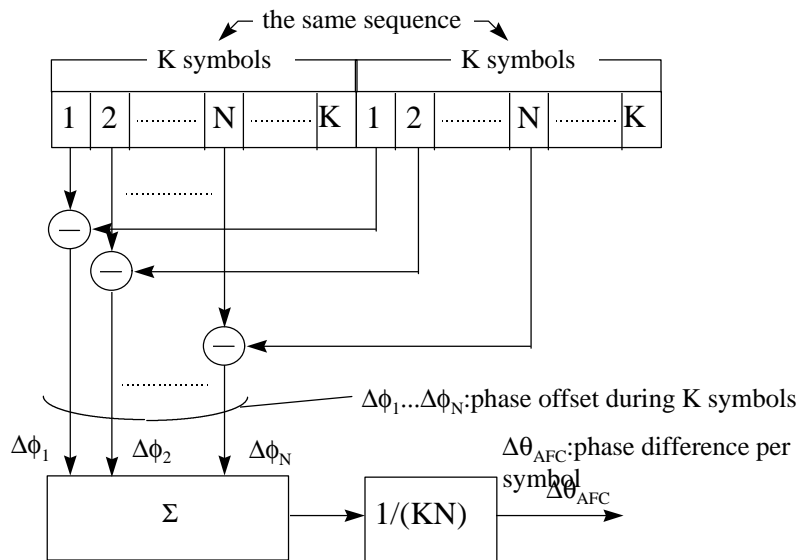
$D(i-k)$: Decision result at $t=i-k$

$r(i)$: received signal at $t=i$

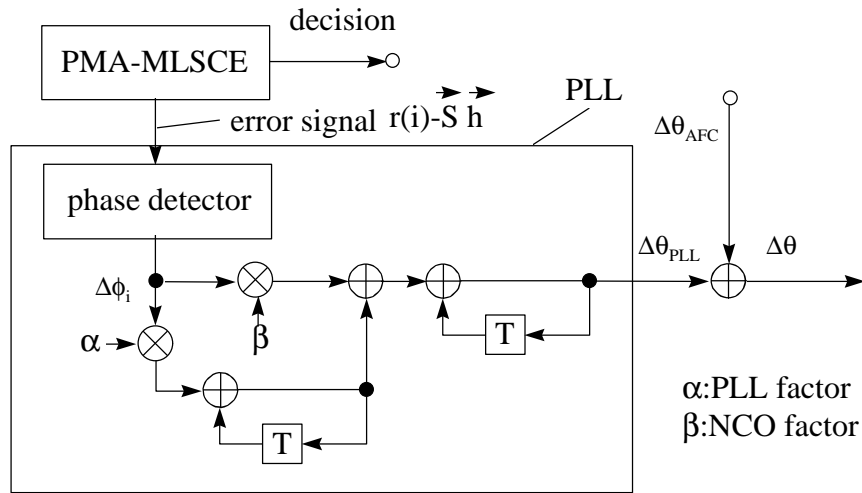
Receiver Structure(4) - 2-1 PMA-MLSCE-



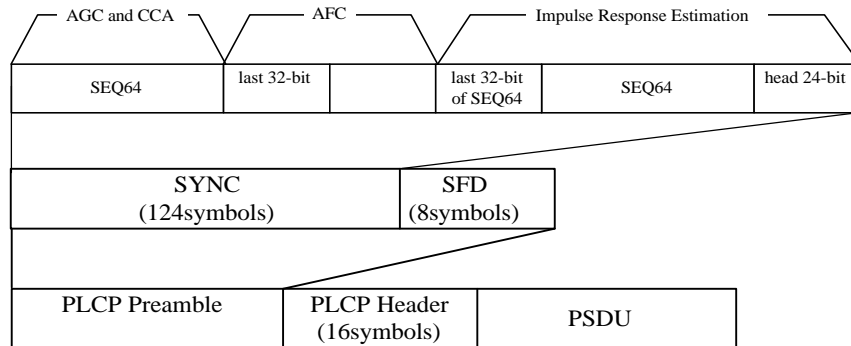
Receiver Structure(5) - AFC -



Receiver Structure(6) - PLL -



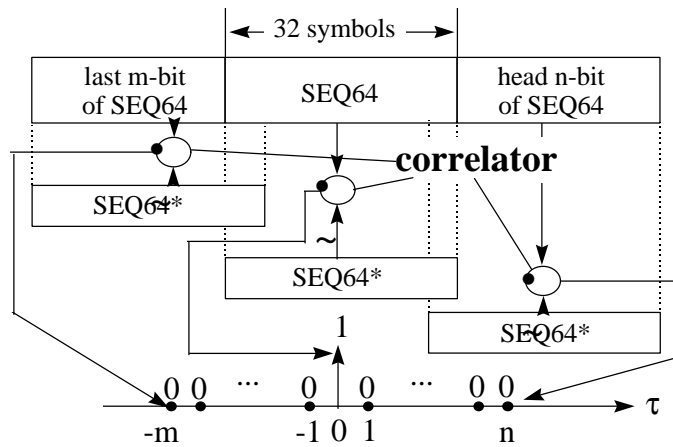
Preamble Structure



$$\begin{aligned}
 \text{SEQ64} &= (X_0 Y_0 X_1 Y_1 \dots X_{30} Y_{30} X_{31} Y_{31}) \\
 &= (0xFF \ 0xF0 \ 0xF3 \ 0x30 \ 0xC0 \ 0x0F \ 0x0C \ 0xC0)
 \end{aligned}$$

Channel Impulse response Estimation

$$\begin{aligned}
 \text{SEQ64} &= (X_0 Y_0 X_1 Y_1 \dots X_{30} Y_{30} X_{31} Y_{31}) \\
 &= (0xFF \ 0xF0 \ 0xF3 \ 0x30 \ 0xC0 \ 0x0F \ 0x0C \ 0xC0)
 \end{aligned}$$

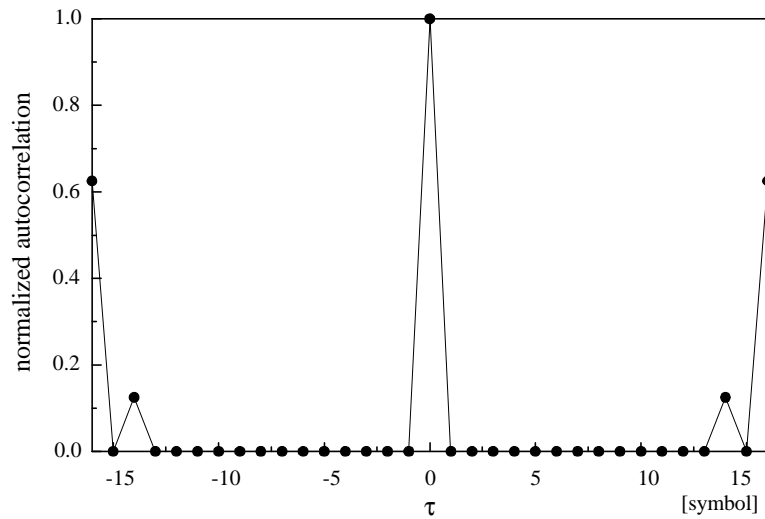


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Correlation function of SEQ64



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SlotTime and SIFSTime

$$\text{SlotTime} = \text{CCATime} + \text{RxTxTurnaroundTime} + \text{AirPropagationTime} + \text{MACProcessingTime}$$

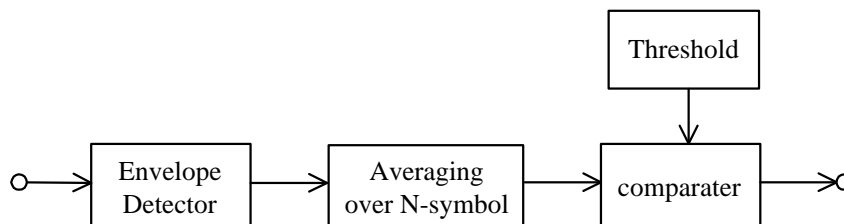
$$\text{SIFSTime} = \text{RxRFDelay} + \text{RxPLCPDelay} + \text{MACProcessingDelay} + \text{RxTxTurnaroundTime}$$

CCATime	RxRFDelay	4usec	10usec
	RxPLCPDelay	2.5usec	
RxTxTurnaround Time	TxPLCPDelay	1usec	14usec
	RxTxSwitchTime	10usec	
	TxRampOnTime	2sec	
	TxRFDelay	1usec	
AirPropagationTime			0.5usec
MACProcessingTime			2usec

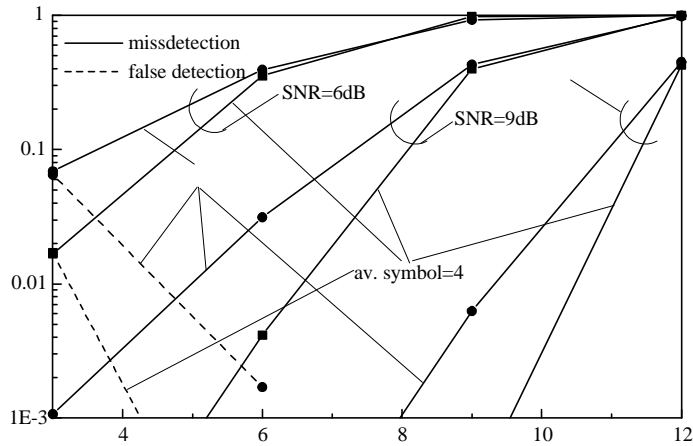
$$\text{SlotTime} = 26.5\text{usec}$$

$$\text{SIFSTime} = 22.5\text{usec}$$

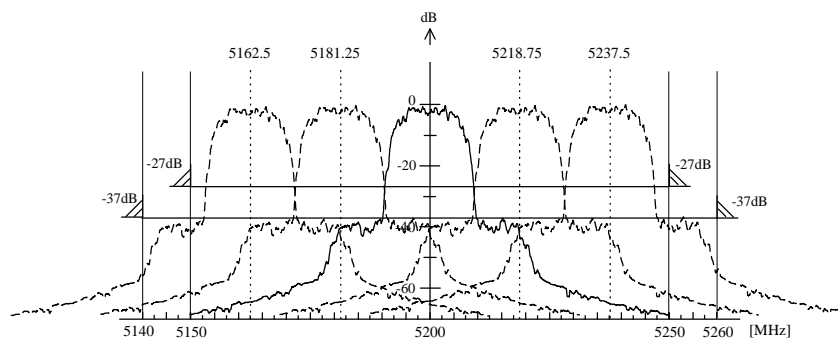
CCA mechanism



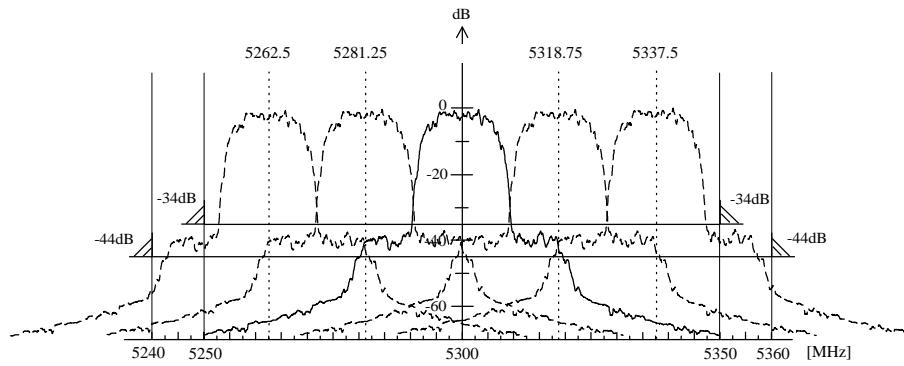
CCA Detection Performance



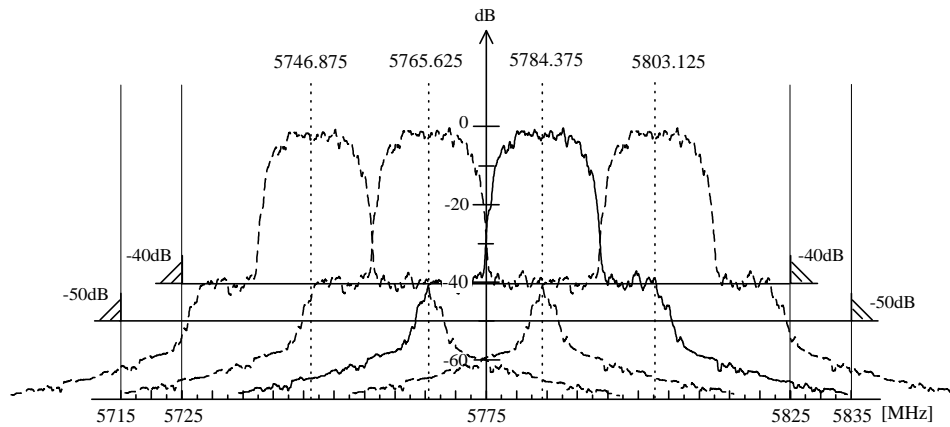
Channel Allocation in 5.15-5.25GHz band



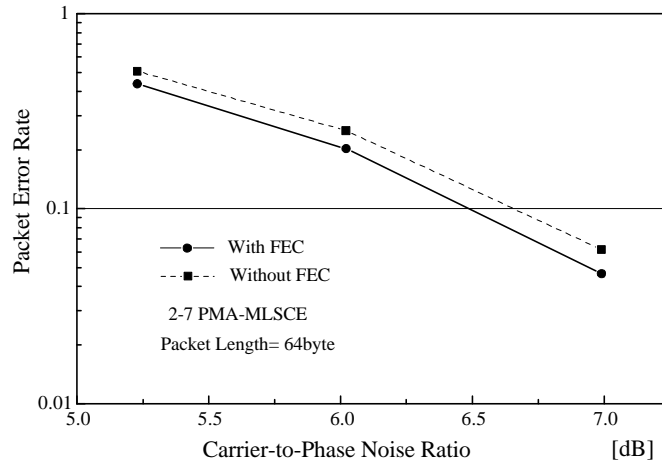
Channel Allocation in 5.25-5.35GHz band



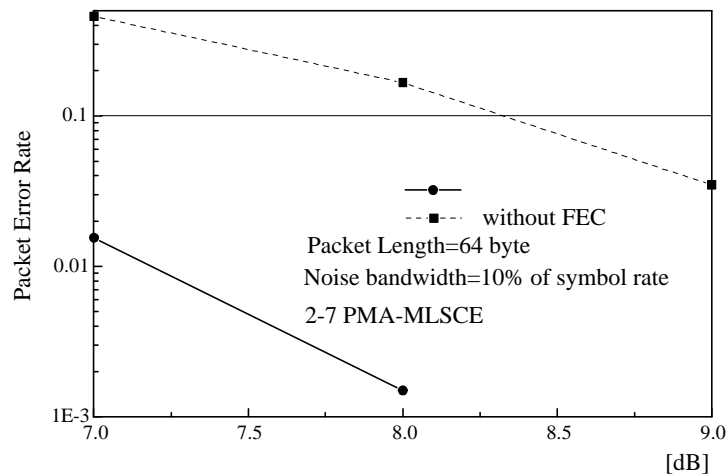
Channel Allocation in 5.725-5.825GHz band



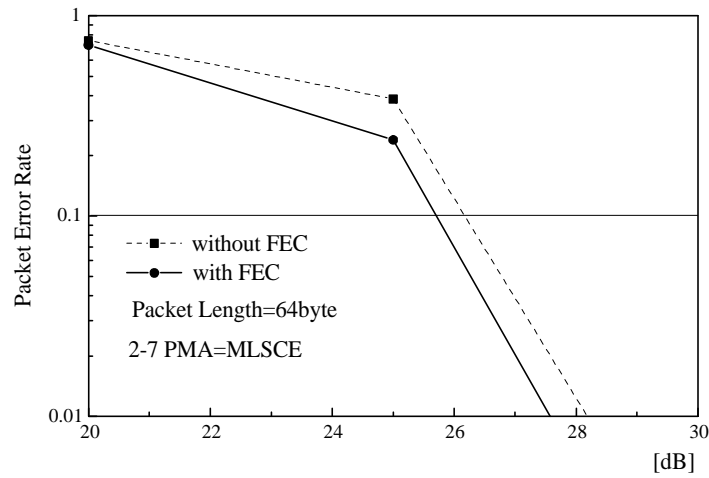
Appendix: PER versus Phase noise



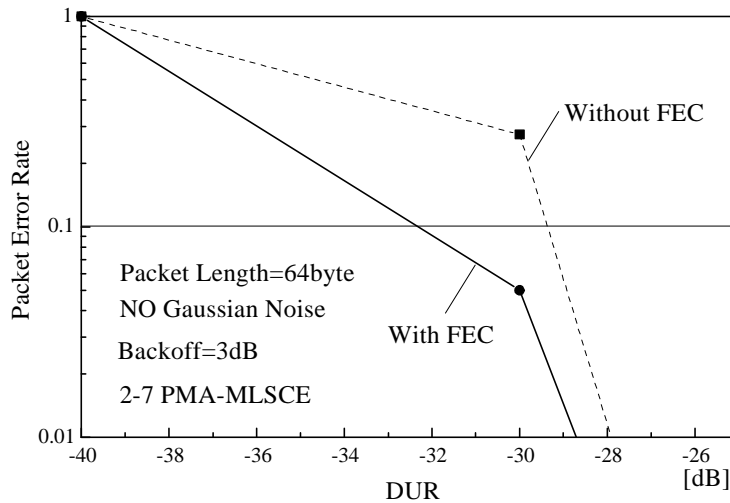
Appendix: PER v.s. Narrow-band Noise



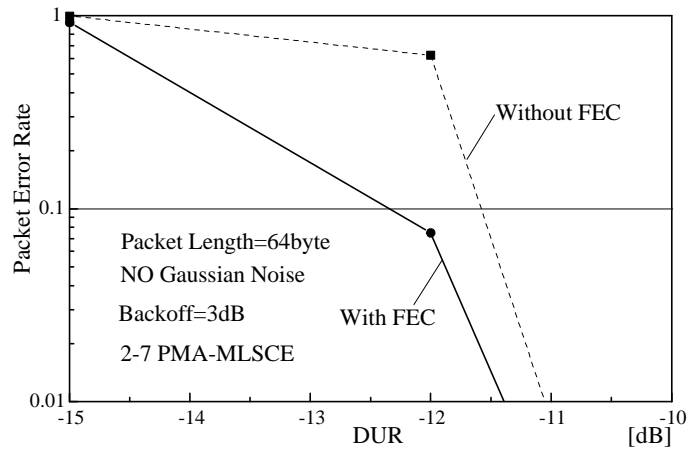
Appendix: PER v.s. CW-jamming



Appendix: PER v.s. Alternate Channel



Appendix: PER v.s. Adjacent Channel



Appendix: PER v.s. Co-Channel

