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| Re: | Call for comments, maintenance task group |
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| Abstract | |
| Purpose | |
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Corrections to definitions of Downlink MIMO in OFDMA PHY

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1 Problem Statement

Several ambiguities exist in the definitions of downlink MIMO in 802.16REVd/D5, specifically:

- 1. MIMO_DL_Basic_IE() and MIMO_DL_Enhanced_IE() both describe DL allocations. This is similar in concept to the regular UL-MAP_IE. The first paragraph in the section is therefore not correct as it refers to a subsequent allocation and mentions ongoing relevance until the end of the frame.
- 2. The value of 'Matrix Indicator' in MIMO_DL_Basic_IE() and MIMO_DL_Enhanced_IE() is not defined if transmit diversity mode is set to 'no diversity'. This configuration mode is a valid one since multiple MIMO transmission layers may be transmitted without STC encoding in each layer.
- 3. Definition of downlink MIMO capability negotiation is missing.

2 <u>Detailed Text Changes</u>

| 1. Section 8.4.5.3.8: | | | | | | |
|--|--|--|--|--|--|--|
| [Modify text from page 528 line 49 to page 529 line 3 as follows] | | | | | | |
| BEGIN | | | | | | |
| In the DL-MAP, a MIMO-enabled BS may transmit DIUC=15 with the MIMO_DL_Basic_IE() to indicate the MIMO configuration of the subsequent downlink allocation to a specific MIMO-enabled SS CID describe downlink allocations assigned to MIMO-enabled SSs. The MIMO mode indicated in the MIMO_DL_Basic_IE() shall only apply to the subsequent downlink allocations described in the IE until the end of frame. | | | | | | |
| END | | | | | | |
| [Modify 'Matrix_indicator' entry in tab | ble 281 a | s follows] | | | | |
| BEGIN | | | | | | |
| Matrix_indicator | 2 bits | STC matrix (see 8.4.8.1.4.) Transmit_diversity = transmit diversity mode indicated in the latest TD_Zone_IE(). if (Transmit_Diversity == 0b01) { 00 = Matrix A 01 = Matrix B 10 - 11 = Reserved } elseif (Transmit_Diversity == 0b10) { 00 = Matrix A 01 = Matrix B 10 = Matrix B 10 = Matrix C 11 = Reserved } else 1 00 - 11 = Reserved] | | | | |
| END | | | | | | |
| 2. Section 8.4.5.3.9: | | | | | | |
| [Modify text on page 530 lines 15-20 as follows] | | | | | | |
| BEGIN | | | | | | |
| the MIMO mode of the subsequent downlink a allocations assigned to MIMO-enabled SSs, ear The MIMO mode indicated in the MIMO_DL_allocations described in the IE until the end of | llocation to ch identific Enhanced | C=15 with the MIMO_DL_Enhanced_IE() to indicate to a specific MIMO-enabled SS describe downlink ed by the CQICH_ID previously assigned to it the SSIE() shall only apply to the subsequent downlink | | | | |
| END | | | | | | |

[Modify 'Matrix_indicator' entry in table 282]

----- BEGIN -----

| M-4-2 2 324 | 2 hita | STC matrix (see 8.4.8.1.4.) |
|------------------|--------|--|
| Matrix_indicator | 2 bits | · · · · · · · · · · · · · · · · · · · |
| | | Transmit_diversity = transmit diversity mode |
| | | indicated in the latest TD_Zone_IE(). |
| | | if (Transmit_Diversity == 0b01) |
| | | { |
| | | 00 = Matrix A |
| | | 01 = Matrix B |
| | | 10 - 11 = Reserved |
| | | } |
| | | elseif (Transmit_Diversity == 0b10) |
| | | { |
| | | 00 = Matrix A |
| | | 01 = Matrix B |
| | | 10 = Matrix C |
| | | 11 = Reserved |
| | | } |
| | | else |
| | | 1 |
| | | 00 - 11 = Reserved |
| | | 1 |

----- END -----

3. Add section 11.8.3.7.6: define downlink MIMO capability negotiation.

[Add new section 11.8.3.7.6]

----- BEGIN -----

11.8.3.7.6 OFDMA SS MIMO downlink support

This field indicates the different MIMO options supported by a WirelessMAN-OFDMA PHY SS in the downlink. This field is not used for other PHY specifications. A bit value of 0 indicates "not supported" while 1 indicates "supported."

| <u>Type</u> | Length | <u>Value</u> | Scope |
|-------------|--------|---|--------------------------|
| <u>155</u> | 1 | Bit #0: 2-antenna STC matrix A | SBC-REQ (see 6.3.2.3.23) |
| | | Bit #1: 2-antenna STC matrix B | SBC-RSP (see 6.3.2.3.24) |
| | | Bit #2: 4-antenna STC matrix A | |
| | | Bit #3: 4-antenna STC matrix B | |
| | | Bit #4: 4-antenna STC matrix C | |
| | | Bit #5: support multiple-layer DL-MAP IEs | |
| | | Bit #6-7: reserved | |

----- END -----