

Project	IEEE 802.16 Broadband Wireless Access Working Group < http://ieee802.org/16 >
Title	Power control clarification
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Re:	IEEE 802.16-2004 and Corrigenda
Abstract	Application of the power control command and step size
Purpose	To clarify the time when the UL power change should apply and the step range
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Suggested changes

Section 8.3..10.1 apply the following changes

For an SS not supporting subchannelization, the transmitter shall support a monotonic power level control of 30 dB minimum. For an SS supporting subchannelization, the transmitter shall support a monotonic power level control of 50 dB minimum. The minimum step size shall be no more than 1 dB. The relative accuracy of the power control mechanism is +/-1.5dB for step sizes not exceeding ~~30dB~~ 15dB, ~~and~~ +/-3dB for step sizes ~~from 15dB to 30dB~~ and 5dB for step size greater than 30dB. For a BS, the transmitter shall support a monotonic power level control of 10 dB minimum.

Apply the following changes to table 342:

System	Name	Time Reference	Minimum Value	Default Value	Maximum Value
SS	FPC processing time	Max.time between reception of Fast Power Control management messages and compliance to its instructions by SS			2.5 msec <u>from the start of the frame (n+1) were frame n is the frame containing the FPC. If there is an UL allocation to the SS before the 2.5 msec in frame n+1 then the power change shall be applied before the end of the frame n+1</u>
SS	RNG-RSP processing	Time allowed for an SS following receipt of a			25 msec 2.5 msec <u>from the start of the frame</u>

		RNG-RSP before it is expected to apply the corrections instructed by the BS			<u>(n+1) were frame n is the frame containing the RNG_RSP. If there is an UL allocation to the SS before the 2.5 msec in frame n+1 then the power change shall be applied before the end of the frame n+1</u>
SS	power control IE	Time allowed for an SS following receipt of a UL-MAP including a power control IE before it is expected to apply the corrections instructed by the BS			25 msec <u>2.5 msec from the start of the frame (n+1) were frame n is the frame containing the UL map containing the power control IE. If there is an UL allocation to the SS before the 2.5 msec in frame n+1 then the power change shall be applied before the end of the frame n+1</u>