

RPRWG Plenary Meeting Agenda Sunday Jan 20, 2002

Just fooling, there are no Sunday meetings although I will be building the network
so if you want to help you can show up and help tape down wires

RPRWG Plenary Meeting Agenda Monday Jan 21, 2002

Time	Len	Title	Ref	Name
8:00		Registration Desk Open		Everyone
8:45	0:15	Seating		Everyone
9:00	0:30	Welcome Slides		Mike Takefman
9:30	0:10	Introductions		Everyone
9:40	0:00	Deferred until quorum - Approval of Minutes from November		Mike Takefman
9:40	1:15	Proposal for Moving Forward		Bob Love
10:55	0:20	Break		
11:15	0:10	Agenda Scrub		Mike Takefman
11:25	0:25	Darwin Overview		John Hawkins
11:50	0:15	Darwin MAC Architecture		David Meyer
12:05	1:00	Lunch		
13:05	0:05	Seating		
13:10	0:10	Darwin Frame Format		Steve Wood
13:20	0:45	Darwin Fairness Algorithm		Harry Peng
14:05	0:30	Break		
14:35	0:30	Transit Path and Fairness Behavior		Italo Busi
15:05	0:30	Bandwidth Reservations - Negotiating for Provisions		David James
15:35	0:30	Fairness Benchmarking of MACs		Harmen van As
16:05	0:00			
16:05	0:00			
16:05	0:00	Recess		
16:05				
16:05	1:00	Access Algorithm		Kanaiya Vasani
Evening Sessions				
18:30		None Planned		
18:30				

RPRWG Plenary Meeting Agenda Tuesday Jan 22, 2002

Time	Len	Title	Ref	Name
8:45	0:15	Seating		Everyone
9:00	0:05	Agenda Scrub		Mike Takefman
9:05	0:20	PHY Update		Rhett Brikovskis
9:25	0:20	Darwin PHY		Steve Wood
9:45	0:30	Topology and Protection Issues		Italo Busi
10:15	0:20	Darwin Topology Discovery		Jim Kao
10:35	0:15	Break		
10:50	0:30	Unified topology discovery/heartbeat/flow-control signaling		David James
11:20	0:30	Customer ID		Anoop Ghanwani
11:50	1:00	Lunch		
12:50	0:10	Seating		
13:00	0:40	Bridging in 802.17 Networks		Bob Castellano
13:40	0:20	TTL Updates and Implications		David James
14:00	0:30	Darwin Protection		Gal Mor
14:30	0:30	RPR Path Restoration in the Case of Node Failure		Jeanne de Jaegher
15:00	0:20	RPR MIB Structure		Gal Mor
15:20	0:30	Break		
15:50	0:30	Simulation Scenarios		Khaled Amer
16:20	0:30	HOL Delay Bound for Darwin with 1 TB		Changcheng Huang
16:50	0:45	Delay and Jitter for Darwin with 2 TB		Vasan Karighattam
17:35	0:00	Recess		
20:00		Editing Staff Meeting		

RPRWG Plenary Meeting Agenda Wednesday Jan 23, 2002

Time	Len	Title	Ref	Name
8:45	0:15	Seating		Everyone
9:00	0:05	Agenda Scrub		Mike Takefman
9:05	1:00	RPR Performance		Khaled Amer
10:05	0:30	Cyclic Reservation Beats All MACs		Harmen van As
10:35	0:15	Break		
10:50	0:20	Lessons Learned from Java Simulations		Stein Gjessing
11:10	0:45	Congestion Management and Congestion Avoidance Interworking		Adisak Mekkittikul
11:55	1:05	Lunch		
13:00	1:00	Access Algorithm		Kanaiya Vasani
14:00	0:30	MAC Performance Comparison		Harmen van As
14:30	0:30	Darwin OAM		Leon Bruckman
15:00	0:20	Break		
15:20	1:00	Procedure Madness		
16:20	1:20	Motion Madness		
17:40	0:00	Recess		
17:40				
17:40				
18:00		Social Event		

RPRWG Interim Meeting Agenda Friday Jan 25, 2002

Go HOME THE MEETING ENDED YESTERDAY