Fairness
ad-hoc considerations

DVJ request for comments

Agenda topics

- Normalized fairRate (fullScale=2^{16}).
- Upper class interactions
  ClassA0 – Shaper classD on transit
  ClassA1 – Upstream STQ depth management
  ClassB-EIR – Adds stop as STQ fills
- Well defined upstream behavior
  ClassC – Shaper to before congestion point
  ClassCC – Shaper after congestion point
  ClassB-EIR – Adds stop as STQ fills
- Well defined interface
  Conservative, aggressive, or differentiated variants

Procedure

- Deliverables

Comments	editors and readers show sufficient (desired) or provide corrections as necessary
Equations	engineers
Simulations	academics & skeptics
Filters &loops	DSP and control theory

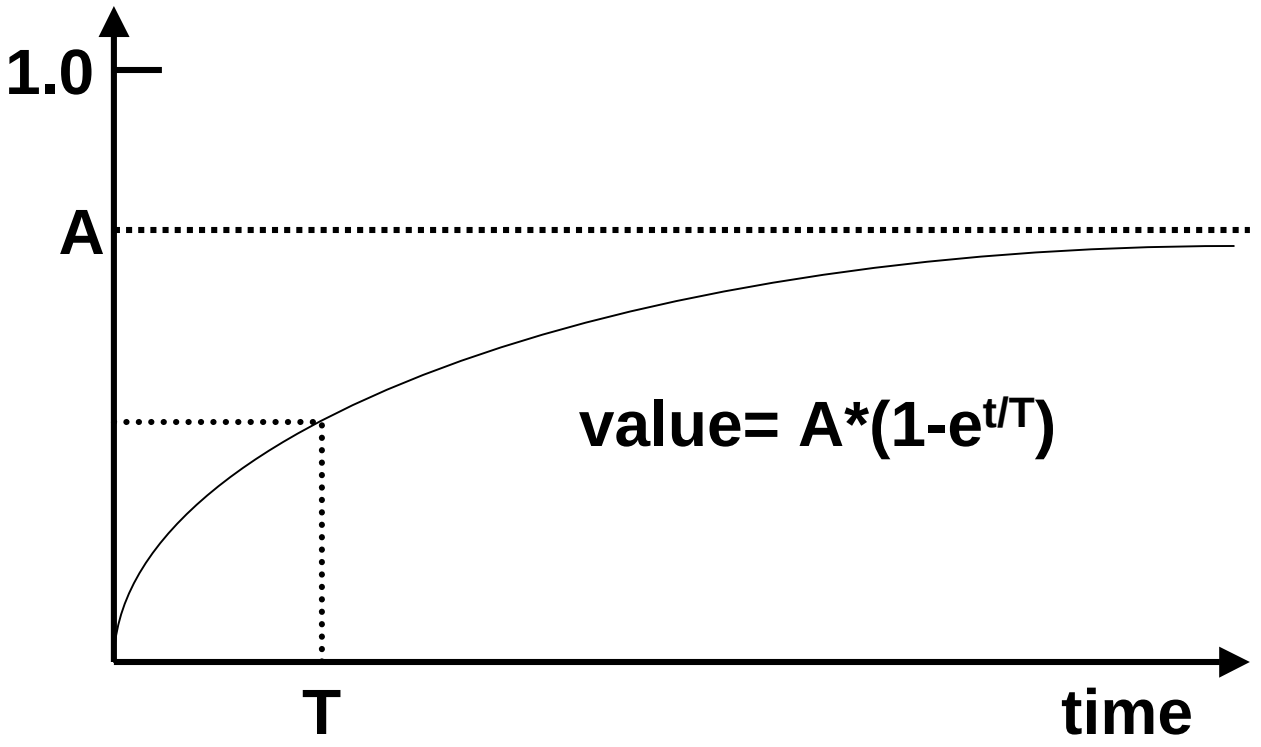
- Constraints

Formats: reserved fairness frame fields only
Comparable/reduced computational complexity
Wide dynamic range (Mb/s to Tb/s)
Limited travel $$, free teleconference calls

Mechanisms

- Freeconference.com facilities
- Weekly meetings
- Timing to allow European participants

DSP perspectives



Creative computing

// 3-bit accurate constant divide

c= a/B

= a*(1/B)

= (a<<shA)+N*(a<<shB)

where N=1,-1