

Time	Author(s)	Affiliation(s)	Title	Duration		
8:30:00 AM	Kent Lusted	Intel	Opening remarks	0:05		
8:35:00 AM	Phil Sun	Credo	Baseline Proposal for 100GAUI-1/200GAUI-2/400GAUI-4 C2M Refe	0:20:00	20	
8:55:00 AM	Ali Ghiasi	Ghiasi Quantum/In	Completing C2M Baseline Proposal TBDs	0:30:00	30	
9:25:00 AM	Discussions on items needed to complete baselines			0:20:00		
9:45:00 AM	Break			0:30:00		
10:15:00 AM	Mike Li	Intel	Comparison of CTLE+DFE vs TDECQ/FFE Reference Receivers for 80	0:15:00	15	
10:30:00 AM	Phil Sun	Credo	C2M simulation with proposed reference receivers	0:30:00	30	
11:00:00 AM	Tom Palkert	MACOM	RX Reference Receiver Power requirements	0:20:00	20	
11:20:00 AM	Tom Palkert	MACOM	RX Reference Receiver	0:20:00	20	
11:40:00 AM	Lunch			1:00:00		
12:40:00 PM	Ali Ghiasi	Ghiasi Quantum/In	C2M Link Analysis at Host and Module Output	0:25:00	25	
1:05:00 PM	Mike Dudek	Marvell	Comparison of C2M performance at TP1a with whole channel perf	0:25:00	25	
1:30:00 PM	Nathan Tracy	TE	**LATE** New chip to module channel simulation and analysis	0:15:00	15	Late
1:45:00 PM	Mark Kimber	Semtech	**LATE** Further analysis of synthesized 100G C2M short channel	0:20:00	20	Late
2:05:00 PM	Discussions and straw polls			1:15:00		
3:20:00 PM	Break			0:30:00		
3:50:00 PM	Mau-Lin Wu	MediaTek	C2M TP1a VEC and ERL Test Specs	0:30:00	30	
4:20:00 PM	Mike Li	Intel	802.3ck Chip-to-Module TP1a/TP4 Compliance Test Measurement	0:25:00	25	
4:45:00 PM	Rich Mellitz	Samtec	C2M ERLmin Proposal	0:20:00	20	
5:05:00 PM	Discussions and straw polls			1:00:00		
6:05:00 PM	Break for the Day					