



Doing the work...

TP2

Management

- *Create/drive detailed schedule to get comments into 1/7 ballot to resolve TBDs*
 - *Who can do what by when?*

TP2 work to be done

- Waveform capture method
 - Test patterns
 - Pattern length?
 - Is 348 bit pattern okay?
 - If not, how to get equivalent to PRBS9?
 - Pattern-end effects?
 - Timing of trigger key?
 - Trigger recovery - describe this and document it
 - Block should trigger once per repetition
 - EDC Rx code
 - Trade waveforms and corroborate results
 - How/when to share/support it? Other sources?
 - Document it
 - EDC Rx should recover actual data rate clock, follow 4 MHz CDR
 - Noise and jitter control
 - See comment proposed resolution to comment 109, Part B
 - Trigger recovery - how to describe this and document it
 - Block should trigger once per repetition of square wave
 - Block should follow 4 MHz CDR
 - Values/limits – initial values already given; review after all else comes together
- Statistical approach?
 - Methods and correlation for simulated and experimental results
- Is all HW approach really so implausible?
 - TP3 ISI generators after O/E
 - EDC Rx...