

Code Summary

Input Data	Sync			Bit Fields									
	[0]	[1]	[2]	D ₀	D ₁	D ₂	D ₃	D ₄	D ₅	D ₆	D ₇		
D ₀ D ₁ D ₂ D ₃ /D ₄ D ₅ D ₆ D ₇	0	1		[0] D ₀	[0] D ₁	[0] D ₂	[0] D ₃	[0] D ₄	[0] D ₅	[0] D ₆	[0] D ₇		
Z ₀ Z ₁ Z ₂ Z ₃ /Z ₄ Z ₅ Z ₆ Z ₇	1	0	0X1e 10000111	[0] Z ₀	[0] Z ₁	[0] Z ₂	[0] Z ₃	[0] Z ₄	[0] Z ₅	[0] Z ₆	[0] Z ₇		