

Code Summary

Input Data	Sync		Bit Fields								
	[0]	[1]	[2]							[65]	
D ₀ D ₁ D ₂ D ₃ /D ₄ D ₅ D ₆ D ₇	0	1	D ₀	D ₁	D ₂	D ₃	D ₄	D ₅	D ₆	D ₇	
Z ₀ Z ₁ Z ₂ Z ₃ /Z ₄ Z ₅ Z ₆ Z ₇	1	0	0X1e 10000111	Z ₀	Z ₁	Z ₂	Z ₃	Z ₄	Z ₅	Z ₆	Z ₇