

Ordered-Set Code Summary

XGMII Pattern	Sync		Bit fields 0-63								
	ZZZZ/ODDD	1	0	0x2D	Z0	Z1	Z2	Z3	y	D5	D6
ZZZZ/SDDD	1	0	0x33	Z0	Z1	Z2	Z3		D5	D6	D7
ODDD/ZZZZ	1	0	0x4B	D1	D2	D3	x	Z4	Z5	Z6	Z7
ODDD/ODDD	1	0	0x55	D1	D2	D3	x	y	D5	D6	D7
ODDD/SDDD	1	0	0x66	D1	D2	D3	x		D5	D6	D7
SDDD/DDDD	1	0	0x78	D1	D2	D3	D4	D5	D6	D7	

Control Codes (Table 49-1)

XGMII Code	Control Code Name	Notation	C Code	x/y Code	8b/10b
0x07,1	Idle	/I/	0x00	-	-
0xfb,1	Start	/S/	TYPE	-	K27.7
0xfd,1	Terminate	/T/	TYPE	-	K29.7
0xfe,1	Error	/E/	0x1e	-	K30.7
0x9c,1	Pulse Ordered Set	/P/	TYPE	0b0000	K28.4
0x5c,1	reserved for 10GFC	/Fsig/	TYPE	0b1111	K28.2
0xdc,1	reserved for 10GFC	/Fseq/	TYPE	0b0101	K28.6
0x1c,1	reserved 2	/R/	0x2d	-	K28.0
0x3c,1	reserved 3	-	0x33	-	K28.1
0x7c,1	reserved 4	/A/	0x4b	-	K28.3
0xbc,1	reserved 5	/K/	0x55	-	K28.5
0xfc,1	reserved 6	-	0x66	-	K28.7
0xf7,1	reserved 7	-	0x78	-	K23.7