CU4HDD SG Architecture Ad Hoc Report

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2015-11-09

Goals of Architecture ad hoc meetings

Charter:

• The charter of the 2.5G/5G Ethernet Backplane and Copper Study Group Architecture Ad Hoc is to discuss the different areas of work (RS/PCS/FEC/PMDs/AN/EEE) that will fall under the work of the study group and to prepare content, and contributions towards the study group's goal of developing the required documentation of objectives, PAR and CSD.

Architecture Ad Hoc Calls

- 3 meeting before September interim, and 1 in October.
- Materials posted @ usual path (under public folder below SG) http://www.ieee802.org/3/CU4HDDSG/public/archadhoc/
  - Good balance of 802.3 voters and new storage-centric colleagues.

- Intent to re-start the call between this Plenary and January 2016 interim.
  - IF PAR is approved at this plenary, THEN ad hoc will entertain project scope topics as well.

"Encouraged" Ad Hoc Topics

Topic	Status	Helps	Notes
CSD (Criteria for Standards Development)	Contrib		SG output
Objectives	Contrib		SG output
PAR (Project Authorization Request) Form	Started		SG output
Objectives: Bit Error Rate – diff from 10E-12?		Objectives	From CFI
Auto-Negotiation (CL73)		CSD	From CFI
Backplane Channel & Multi-rate Coexistence?		CSD	From CFI
Energy Efficiency and EEE		CSD	From CFI
Market needs for the short reach copper ("TwinAx")		PAR, CSD	From CFI
XGMII/GMII/PCS compatibility with/to 802.3bz		CSD	From CFI
CSD: Optimal balanced system cost from the proposed rates (e.g. why 10G does not serve the market).		CSD	From reflector (SK)

Note: Not all topics needs contributions. Keeping a laundry list…

Thank You!