

Unit Load Based Return Loss

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Objective

Nodes using power coupling inductance that is smaller than ~80uH will violate the existing return loss limit

Higher unit loads should be allowed smaller value power coupling inductance

Specify modifications to Clause 188 return loss lines so that unit load inductance is included



Low Frequency Return Loss of Node

Measure Input Impedance at TC1

Below 10MHz, model input impedance with single pole model HPF Corner Frequency:







Convert Low Frequency Corner To Return Loss

Single – pole model works well below ~10MHz Convert TC1 impedance to Return Loss...

Low frequency return loss based on unit load:

 $RL(f) \le min(0, -26.06-20*log10(f/NUNIT)) dB 0.3 \le f \le TBD$

Where

f is the frequency in MHz

NUNIT is the unit load value of the node; 1<= NUNIT <=16





Blending Low Frequency Return Loss

Low frequency RL is easy to calculate because it is based on power coupling inductance and termination resistance

At higher frequencies, RL is dependent on

Parasitic Capacitance - Cnode

Compensation Inductance – Lcomp

Higher frequency portion of the curve needs to be fit instead of calculated....

For the following simulations:

Set nominal parasitic capacitance to 10pF, 20pF, 30pF Calculate compensation inductance for Cnode Vary parasitic capacitance +/-10% Vary Lcomp +/-20%

Simulate the corner cases for Cnode, Lcomp

Compare to proposed RL



Equation

























All U1 – U16





Conclusion

