

New Ethernet Applications ad hoc Next-Gen MMF PMDs

10/27/2017

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Agenda

- IEEE patent policy reminder:
<https://development.standards.ieee.org/myproject/Public/mytools/mob/preparslides.pdf>
- IEEE 802 participation policy: <https://mentor.ieee.org/802-ec/dcn/16/ec-16-0180-05-00EC-ieee-802-participation-slide.pdf>
- Highlight changes to the draft CFI document lingle_nea_01_171027.pdf on NEA web site
 - Compare slide 3 vs. slide 4; graphic modified to look more like modern scale-out architecture; switch-to-switch application is highlighted as the primary volume driver
 - Material has been removed from Slide 26 to simplify the message
 - Slides 32 & 33 motion language modified as below
- **Modified** draft motion to create Study Group: “Move that the IEEE 802.3 Ethernet Working Group authorizes the formation of a study group to develop a Project Authorization Request (PAR) and Criteria for Standards Development (CSD) responses for "Next-generation 200 Gb/s and 400 Gb/s PHYs over Fewer MMF Pairs than in Existing Ethernet Projects"