IEEE P802.3at Task Force Power Via MDI Enhancements

Midspan/Channel Requirements below 1MHz – with 120uH

Addressing Draft D3.2 comment #78

Yair Darshan / Microsemi Corporation, November, 2008

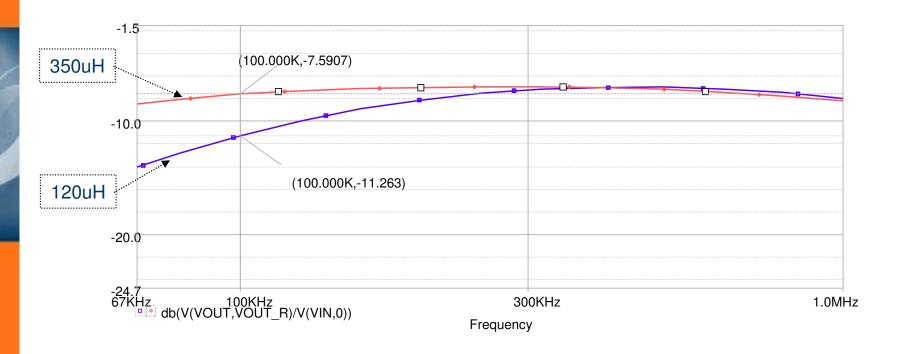


Objectives

- Checking the effect of reducing OCL to 120uH in Type 2 System Channel Model.
- How it affect on 100BT ALT A Midspan Transfer Function of Eq-33-18 in Draft D3.2



System Channel Model with 120uH vs 350uH (without Midspan)



1. Now, the 120uH curve represent the System Channel Model TF that Type 2 system need to meet. The 350uH curve represent the previous System TF with 350uH.



OCL value vs Midspan Presence

- Case 1: No Midspan. lunb=3%lpeak. OCL=120uH in Switch and PD.
- Case 2: Midspan is inserted and is not regulating lunb to Type 1 levels:
 - Switch side is without PoE current hence its OCL is 350uH min which is >120uH hence will not affect Eq-33-18.
 - PD side works with 3%lpeak hence PD OCL may be 120uH minimum.
 - It will affect Equation 33-18
- Case 3: Midspan is inserted and it is regulating lunb to Type 1 levels
 - Switch side is without PoE current hence its OCL is 350uH min which is >120uH hence will not affect Eq-33-18.
 - PD side works with Type 1 lunb hence PD OCL is 350uH minimum.
 - No effect on Equation 33-18
- Conclusions:
 - EQ-33-18 is not requiring adjustments if Midspan is regulating lunb to Type 1 levels



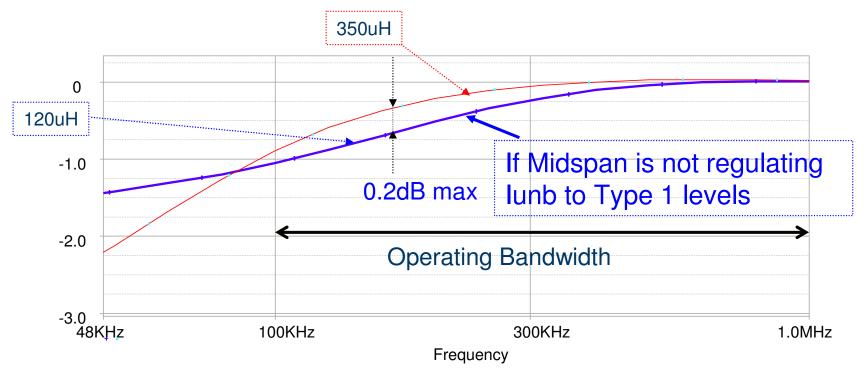
OCL value vs Midspan Presence summary

Case	Switch lunb	Switch OCL	Midspan lunb	PD lunb	PD OCL	Notes
1	3%lpeak	>=120uH	Without Midspan	3%lpeak	>=120uH	The New System Channel Model for Type 2 systems
2	0	>=350uH	3%lpeak	3%lpeak	>=120uH	Midspan <u>is</u> not regulating lunb to Type 1 levels
3	0	>=350uH	10.5mA	10.5mA	>=350uH	Midspan is regulating lunb to Type 1 levels



Midspan TF comparison: Simulation Verification

(without worst case analysis and without design margin function)



 $-db(V(VOUT,VOUT_R)/V(VIN,0)) + db(V(VOUT_2,VOUT2_R)/V(VIN,0)) \\$

The worst case difference is 0.2dB and varies with frequency



Summary

- Eq 33-18 is not affected due to the requirement that Midspan need to regulate lunb to Type 1 levels hence OCL in PD and Switch stays as in Type 1 systems i.e. 350uH minimum.
- At the worst case that Midspan is not regulating lunb, it will change Eq 33-18 by 0.2dB max due to the 120uH minimum at the PD as function frequency over the operating frequency range.
- Recommendations for Draft D3.2 comment #78
 - When ALT A Type 2 Midspan is regulating lunb to Type 1 levels, Eq 33-18 is not changed



Discussion



References

■ For complete analysis, model building, model validation techniques and lab test results see:

http://www.ieee802.org/3/at/public/2008/05/darshan 2 0508.pdf



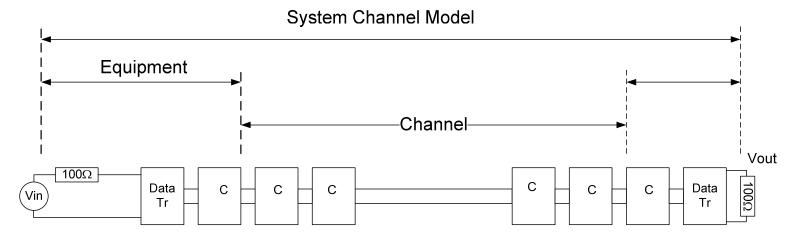
Short Summary of Previous Midspan ad hoc work when OCL=350uH in the System Channel Model. See complete work at:

http://www.ieee802.org/3/at/public/2008/05/darshan_2_0508.pdf



Terms and abbreviations

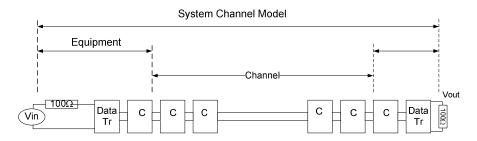
- Channel Model = CM : Cable + 4 Connectors forming 25 Ohms at 100m round loop on data pairs
- System Channel Model = SCM: Channel + 2xData 100BT Transformer connected to signal source with 100 ohm series impedance and loaded with 100 ohm termination
- Transfer Function =TF: The ratio between the voltage at the load termination to the signal source as function of frequency. The TF includes the effect of the source and load impedance for simulating the droop effect as function of the inductance of the data transformers
- Low Frequency Model=LFM: The System Channel Model used for derivation of the Transfer Function is limited to frequencies <1MHz
- LM, Magnetizing Inductance: Data transformer inductance
- Idc=The total dc bias current that the transformer is exposed too as a results of the data and the channel imbalance during PSE operation.





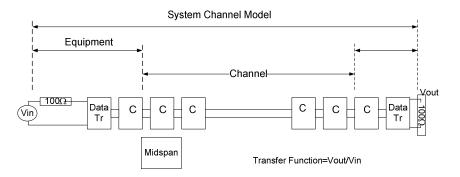
Objectives

 (1) To define the requirements for a System Channel Model at the signal path for 100BT operation at frequencies below 1MHz



Transfer Function=Vout/Vin

 (2) To define the requirements for a Midspan at the signal path for 100BT operation at frequencies below 1MHz as a result of (1)



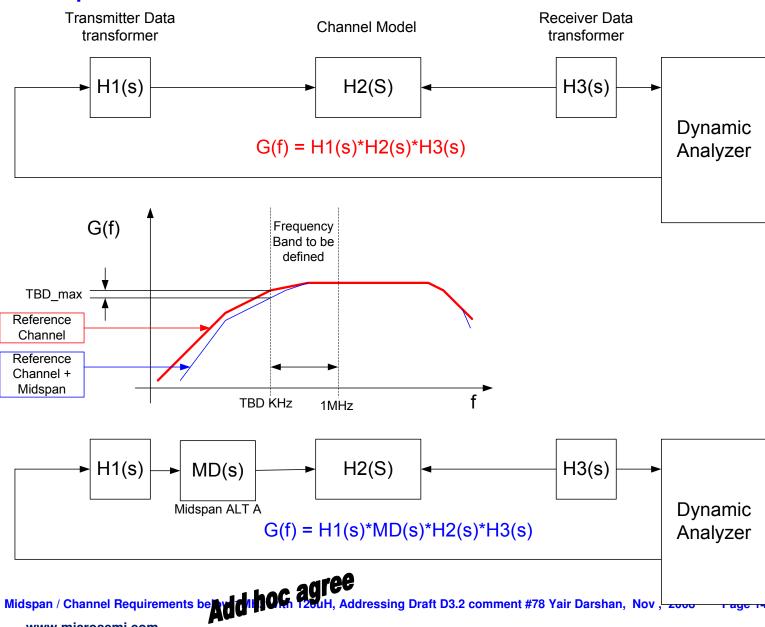


Background

- The IEEE802.3at task force approve using ALT A Midspan.
 - Powering the PD through the signal path
- The IEEE802.3 requires that when a Midspan is inserted in the channel it shall not alter the channel performance.
 - The channel performance is defined from 1MHz and up by 33.4.8
 - The 802.3 doesn't not define requirements for the channel below 1MHz.
- In addition, there is the inductance requirements as specified in ANSI X3.263-1995 (TP-PMD) subclause 9.1.7 which may be affected when a ALT A Midspan is sued in the channel for 100BT
- As a result, the droop of the signal may increased which may affect the BER
 - In addition, the effect of BLW on the BER may increase as well
- All of the above may further affected by the presence of DC bias due to the cabling imbalance



Proposed Solution

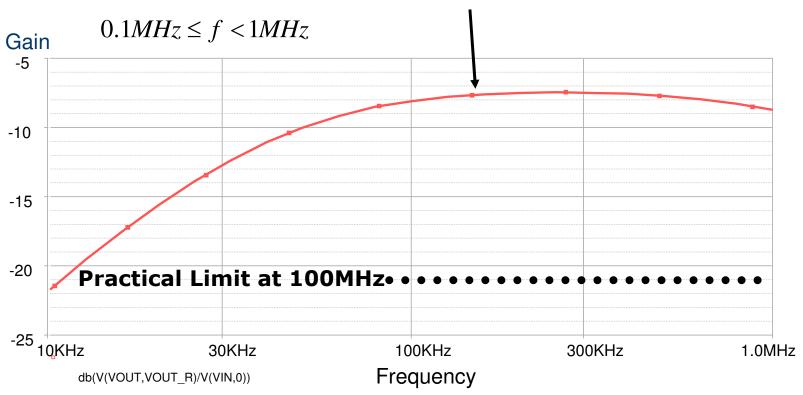




SCM: without Midspan at 350uH,100m. W.C analysis

■ A compliant System Channel Model Gain w/o Midspan, must be higher than this curve per current standards and standard components specifications

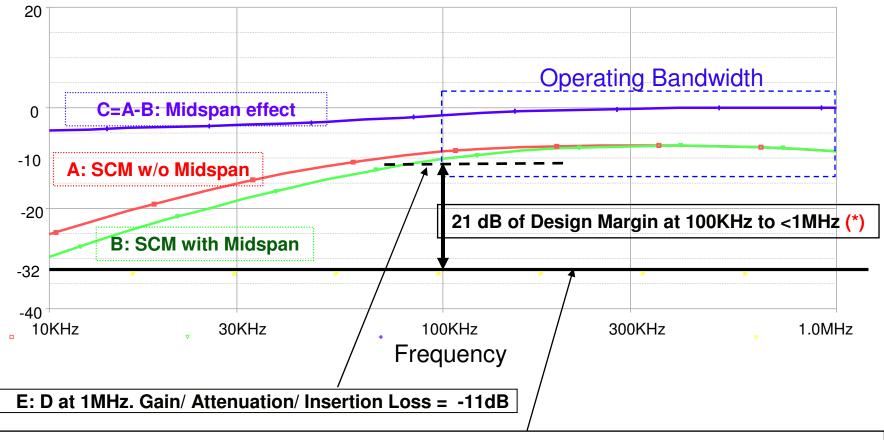
Gain = $-9.075 + 14.419 \cdot f - 41.781 \cdot f^2 + 45.759 \cdot f^3 - 18.075 \cdot f^4$





SCM: with and without Midspan at 350uH,100m. W.C analysis

Gain[dB]



D: SCM PHY to PHY minimum Gain/ Attenuation/ Insertion Loss requirements at 100MHz= -32dB



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(*) Actually margin is higher by additional 3dB due to the fact that Channel IL is ~1dB at f<1MHz and not 4dB

How to distribute the design margins that we have in the system at frequencies below 1MHz?

- At 100KHz, the system gain is:
 - ~ -8 dB w/o Midspan (-8.7dB at 1MHz, -7.5dB at 300KHz)
 - ~ -9 dB with Midspan (-8.7dB at 1MHz, -7.5dB at 300KHz)
 - Which is practically negligible difference.
- The SCM is required by various system components specifications to work with gain as low as
 - 32dB at 100MHz
 - 11dB at 1MHz
 - Hence the PHY is capable to work with System Channel Gain as low as -32dB
 - However the inductance issue is relevant at the low frequency range which is 21dB higher then the worst case conditions.
- Since the Midspan has negligible effect on the SCM it is recommended to assign most of the Margin to the Switch according to the following ratio:
 - Midspan: 1dB max. as function of frequency from 100KHz to 1MHz
 - Switch: 20dB min. as function of frequency from 100KHz to 1MHz



Generating Midspan TF

- Steps:
- 1. Generating System Model w/o Midspan, SCM.
- 2. Generating System Model with Midspan ALT A, SCMM.
- 3. Finding w.c analysis results and update the model.
- 4. Finding Midspan TF=SCM -SCMM (Gain[db] vs Frequency plot). Note that TF is not SCM/SCMM (as the conventional definition) due to the fact that we are looking for worst case template that allows at least 20dB margin to the Switch i.e. we are defining the worst case effect of the Midspan on the SCM gain.
- 5. Finding the best regression function structure to build the TF. (3, 4 and 5 order polynomial regression vs. Logarithmic regression were evaluated.)
- 6. Logarithmic structure showed best accuracy for the operating bandwidth under discussion (100KHz to 1MHz) for Midspan.
- 7. Adding margin function to cover Test Equipment errors and design.
- 8. Getting Final Equation.



Midspan TF and test setup for compliance.

Including w.c analysis, Test Equipment Error and design Margin

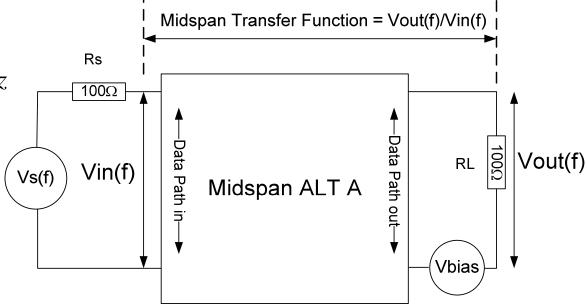
$$\left\{-c + 37.5 \cdot LOG_{10}\left(\frac{a \cdot f}{\sqrt{1 + b \cdot f^2}}\right)\right\}$$

$$a = 22.40$$

$$b = 520.5$$

$$c = 0.1000$$

 $0.1MHz \le f < 1MHz$



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- Vin(f) is the Sine wave signal to be used to measure the Midspan TF.
- Vbias is the DC offset voltage to be applied in series to RL in order to generate Ibias.
- Vout (f) is the Midspan response to Vin(f)

