Comment Type TR Comment Status X

LPI Client will need additional interfaces to control the Layer 2 LLDP negotiation of Transmit Tw and Receive Tw. There are cases within 802.1 AVB standards where LPI is desired but only if the negotiated transmit wait time is held to some maximum that may or may not be less than what the Ethernet implementation could otherwise support (when AVB streams are active on the link). Other upper layer technologies may have similar constraints that will be known to the LPI Client.

# SuggestedRemedy

Add following primitives:

LP\_MAX\_TX\_WAIT.request(time) time in usec, 0 means no restriction imposed by LPI Client

LP\_MAX\_RX\_Wait.request(time)
time in usec, 0 means no restriction imposed by LPI Client

LP\_TX\_WAIT.indication(time) time is negotiated transmit wait time in usec

LP\_RX\_WAIT.indication(time) time is negotiated receive wait time in usec

Proposed Response Status O

 CI 48
 SC 48.2.4.2
 P
 L
 # [2

 McCulloch, Ewan
 Cadence Design Syste

Comment Type T Comment Status D

The spec mentions that on receive, all ||I|| received during idle are translated to XGMII Idle control characters for transmission over the XGMII. All other !||I|| received during idle are mapped directly to XGMII data or control characters on a lane by lane basis, with the exception of /D20.5/ (Low Power Idle) being detected in any row and the rest of the rows in the same column being detected /K/ only or /R/ only, which will result in reporting LP\_IDLE in all lanes.

This implies that ||A|| is always translated to normal XGMII Idle characters, even if the previous column was a low power idle stripe (/D20.5/ in one row and /K/ or /R/ in all other rows). Is this the intention? This would make the received XGMII sequence quite different from the link partners transmitted XGMII, and complicate the detection of LPI in the MAC. I think the received ||A|| that is part of a stream of low power stripes of idles should be translated to LPI as well.

### SuggestedRemedy

Change the spec to

Whenever sync\_status=OK, all ||II|| received during idle are translated to XGMII Idle control characters for transmission over the XGMII. All other !||II|| received during idle are mapped directly to XGMII data or control characters on a lane by lane basis, with the following exceptions:

- 1. /D20.5/ (Low Power Idle) being detected in any row and the rest of the rows in the same column being detected /K/ only or /R/ only, which will result in reporting LP\_IDLE in all lanes.
- 2. ||A|| being detected AND /D20.5/ (Low Power Idle) being detected in any row of the previous column and the rest of the rows in the previous column being detected /K/ only or /R/ only, which will result in reporting LP\_IDLE in all lanes.

Proposed Response Status W

PROPOSED REJECT.

This change will require some discussion amongst interested parties and does not weigh on the "technical completeness" of the draft.

The commenter is urged to resubmit the comment during the Working Group ballot phase.

Comment Type T Comment Status D

Should idle insertion or deletion via clock tolerance compensation be allowed to proceed during LPI, if we choose not to implement the low power state machines (i.e. if the PCS is simply transporting LPI for compatibility, but not entering a low power state itself). 48.2.4.2.3 states that Idle insertion or deletion may be performed on ||R|| in the encoded data stream, which will never be the case when transporting LPI (one of the characters in the stripe of |R|'s will be |D20.5|)

Our assumption is that clock rate compensation should be allowed to continue during LPI, as this is consistent with allowing the deskew and comma sync processes within the PCS RX to continue (using ||A|| and individual /K/ symbols respectively).

### SuggestedRemedy

modify the spec to allow for clock rate compensation on a strpe that contained three /R/'s and one /D20.5/ in the encoded data stream

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

Because Low Power Idle is defined as a case of IDLE, the same rules described in 48.2.4.2.3 still apply. This can be made clearer to the reader.

Add the following sentence at the end of the paragraph on line 38 of page 128:

Clock compensation may be performed during Low Power Idle according to the rules described in 48.2.4.2.3.

Cl 35 SC 35.2.2.4 P 66 L 6 # 4 Dietz. Bryan Alcatel-Lucent

Comment Type E Comment Status D

Minor editorial change: replace semicolon with comma in list of "during the assersion of low power idle; carrier extend or carrier extend error code-groups." Semicolon is not appropriate in this context.

# SuggestedRemedy

Replace semicolon with comma. It should read "during the assersion of low power idle, Carrier Extend or Carrier Extend Error code-groups."

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Also change spelling to "assertion"

Cl 78 SC 78.4.2.5 P 238 L 21 # 5

Dietz, Bryan Alcatel-Lucent

Comment Type E Comment Status D

Suggestion to simplify language and eliminate "set of link partners".

#### SuggestedRemedy

The transmitting side controls the data placed on the medium connecting the transmit and receive link partners and enforces Tw\_sys. The transmitting link partner shall wait for the time indicated by the Transmit Tw\_sys after deasserting Low Power Idle at the xxMII before sending data frames.

The receiving link partner shall be ready to accept data based on (its echoed value of the) Transmit link partner's Tw\_sys. This ensures that the link partners transition out of LPI mode and receive frames without loss or corruption.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Text could be simplified without loss of content:

- Delete the words "Thus, ",, "a set of" from the second sentence
- Delete the words "Similarly," from the third sentence

 CI 36
 SC Fig36-9b
 P 81
 L
 #
 6

 Pillai, Velu
 Broadcom

Filial, Velu Broaucom

Arc from RX\_WTF to RX\_SLEEP has !rx\_tw\_timer\_done it should be rx\_wf\_timer\_done

Comment Status D

SuggestedRemedy

Comment Type

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

ER

Both this arc and the arc from RX WTF to RX ACTIVE need to be changed.

in state RX\_ACTIVE (fig 36-9b)

Cl 36 P 81 # CI 36 P 81 L 10 # 10 SC Fig36-9b SC Fia36-9b Pillai, Velu Pillai, Velu Broadcom Broadcom Comment Type TR Comment Status D Comment Type TR Comment Status D Arc from RX QUITE to RX WTF needs to be moved to RX QUIET to Transition out of RX ACTIVE back to itself has a condition sync status!= RX LINK FAIL. Presently signal detect=FAIL make it loop around from RX WTF back to code sync status. But sync status latches code sync status inside RX ACTIVE. Hence RX QUIET. Once the rx to timer done is a link fail. this transition condition is meaning less. SuggestedRemedy SuggestedRemedy Instead of the above, please use code sync status = FAIL Proposed Response Proposed Response Response Status W Response Status W PROPOSED ACCEPT. PROPOSED REJECT. Sync status only latches code sync status on entry to RX ACTIVE. Using "sync status!= C/ 36 SC Fig36-9b P 81 1 # 8 code sync status" ensures that any change will cause it to cycle around (allowing all Pillai, Velu Broadcom changes in code sync status to be reflected in sync status. Comment Type TR Comment Status D CI 36 SC Fig36-7a P 76 # 11 Arc from RX\_WTF to RX\_ACTIVE should be !detect\_lpidle instead of detect\_idle. Any recovery from RX WTF is not guaranteed to be receiving idle codewords. Pillai, Velu Broadcom SuggestedRemedy Comment Type TR Comment Status D Transition from LPI K to IDLE D is not checking EVEN boundary Proposed Response Response Status W SuggestedRemedy PROPOSED ACCEPT. Change the transition condition to detect idle \* rx lpi active =FALSE \* !EVEN Proposed Response Response Status W C/ 36 SC Fig 36-7a P 76 L 3 # 9 PROPOSED ACCEPT IN PRINCIPLE. Pillai. Velu Broadcom Use "ODD" instead of "!EVEN" Comment Type TR Comment Status D The variable rx\_lpi\_fail is not used any more. C/ 36 SC Table36-3b P82 L SuggestedRemedy Pillai. Velu **Broadcom** Hence remove rx lpi fail = TRUE condition to enter LINK FAILED Comment Type Comment Status D ER Proposed Response Response Status W There is a row for Tda. But there is no debounce state, hence no need for this timer value PROPOSED ACCEPT IN PRINCIPLE. SuggestedRemedy Remove the entire row Implement the suggested remedy and also delete definition for rx lpi fail and assignment

Proposed Response

PROPOSED ACCEPT.

6/9/2009 11:27:27 AM

Response Status W

Cl 36 P 73 # 13 Cl 48 P 135 L # 16 SC 36.2.5.1.5 L SC Fia48-9b Pillai, Velu Pillai, Velu Broadcom Broadcom Comment Type TR Comment Status D Comment Type E Comment Status D During the adhoc/meetings, the decision was to have the wake timer to be for 1ms. But in Please flip [A] and [B] to be consistent with Fig 36-9b the draft is point to TWR, which is only 10-11uSec. The purpose of this timer is to give the SuggestedRemedy receiver a chance to gracefully recover from a wake time fault. SuggestedRemedy Proposed Response Response Status W Add a row to Table 36-3b for Twtf and assign 1ms. In fact replace the TDA row for this. PROPOSED ACCEPT. Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE. C/ 48 P 135 SC Fia 48-9b L 43 Pillai, Velu Broadcom Change definition of rx wf timer: Comment Type ER Comment Status D "The timer terminal count is set to Twr" to "The timer terminal count is set to Twtf" Arc from RX WTF to RX LINK FAIL should have !rx wf timer done instead of rx tw timer done. Replace last row of Table 36-3b with: SuggestedRemedy Twtf Wake time fault recovery time 1mS C/ 36 Proposed Response Response Status W SC 36.2.5.1.5 P 73 L 27 # 14 PROPOSED ACCEPT IN PRINCIPLE. Pillai. Velu Broadcom Comment Type ER Comment Status D Arc from RX WTF to RX LINK FAIL is OK, however: Wake error counter needs to be added to the counter section Arc from RX\_WTF to RX\_ACTIVE should have !rx\_wf\_timer\_done instead of SuggestedRemedy !rx tw timer done. Add the description and link to the Register Cl 48 SC Fig48-9b P 135 / 45 # 18 Proposed Response Response Status W Pillai. Velu Broadcom PROPOSED ACCEPT IN PRINCIPLE. Comment Type TR Comment Status D Add wake error counter (identical to 49.2.13.2.2). Arc from RX\_WTF to RX\_ACTIVE should be !||LPIDLE|| instead of ||IDLE||. Any recovery from RX\_WTF is not guaranteed to be receiving idle codewords. Cl 48 SC 48-9b P 135 L 96 # 15 SuggestedRemedy Pillai, Velu Broadcom Comment Type ER Comment Status D Proposed Response Response Status W IIIDLE needs to be ||IDLE|| PROPOSED ACCEPT. SuggestedRemedy

This correction is needed at two places in this state diagram.

Response Status W

Proposed Response

Rx\_deact\_timer is no longer used

Response Status W

SuggestedRemedy Remove the timer Proposed Response

PROPOSED ACCEPT.

# 23

C/ 48 SC Fig 48-9b P 135 L 5 # 19 Cl 48 SC Table 48-10 P 136 L 18 Pillai, Velu Pillai, Velu Broadcom Broadcom Comment Type TR Comment Status D Comment Type ER Comment Status D RX ACTIVE state should set rx quiet <= FALSE There is a row for Tda. But there is no debounce state, hence no need for this timer value SuggestedRemedy SuggestedRemedy Remove the entire row Proposed Response Proposed Response Response Status W Response Status W PROPOSED ACCEPT. PROPOSED ACCEPT. C/ 48 SC Fig 48-9 P 132 L 23 Cl 48 P 129 # 20 SC 48.2.6.1.5 L 39 Pillai, Velu Broadcom Pillai, Velu Broadcom Comment Type ER Comment Status D Comment Type TR Comment Status X rx LPI active = FALSE During the adhoc/meetings, the decision was to have the wake timer to be for 1ms. But in the draft is point to TWR, which is only 8-9uSec. The purpose of this timer is to give the SuggestedRemedy receiver a chance to gracefully recover from a wake time fault. rx\_lpi\_active = FALSE SuggestedRemedy Proposed Response Response Status W Add a row to Table 48-10 for Twtf and assign 1ms. In fact replace the TDA row for this. PROPOSED ACCEPT. Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE. C/ 48 SC 48.2.6.1.5 P 129 L 25 # 21 Pillai. Velu Broadcom Change definition of rx\_wf\_timer: Comment Status D Comment Type ER "The timer terminal count is set to Twr" to "The timer terminal count is set to Twtf" LPI\_fail\_timer is not needed anymore Replace last row of Table 48-10 with: SuggestedRemedy Remove the timer. Twtf Wake time fault recovery time 1mS Proposed Response Response Status W PROPOSED ACCEPT. C/ 48 SC 48.2.6.1.5 P 129 L 29 # 22 Pillai. Velu Broadcom Comment Type ER Comment Status D

Cl 48 P 135 # 25 SC Fig48-9b L 10 Pillai, Velu Broadcom

Comment Type TR Comment Status D

Transition out of RX ACTIVE back to itself has a condition align status!= deskew align status. But align status latches deskew align status inside RX ACTIVE. Hence this transition condition is meaning less.

SuggestedRemedy

Instead of the above, please use deskew align status = FAIL

Proposed Response Response Status W

PROPOSED REJECT.

Align status only latches deskew align status on entry to RX ACTIVE. Using "align status!= deskew align status" ensures that any change will cause it to cycle around (allowing all changes in deskew\_align\_status to be reflected in align\_status.

C/ 49 SC 49.2.13.2.5 P 145 L 7 # 26 Broadcom

Comment Type ER Comment Status D

Rx deact timer is no longer used

SuggestedRemedy

Pillai, Velu

Remove it

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 49 SC Table 49-3 P 150 / 28 # 27

Pillai, Velu Broadcom

Comment Type ER Comment Status D

There is a row for Tda. But there is no debounce state, hence no need for this timer value

SuggestedRemedy

Remove the entire row

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 49 P 145 L 22 # 28 SC 49.2.13.2.5

Pillai, Velu Broadcom

Comment Type TR Comment Status D

During the adhoc/meetings, the decision was to have the wake timer to be for 1ms. But in the draft is point to TWR, which is only 11-12uSec (13-14uSec if FEC is ON). The purpose of this timer is to give the receiver a chance to gracefully recover from a wake time fault.

SuggestedRemedy

Add a row to Table 49-3 for Twtf and assign 1ms. In fact replace the TDA row for this.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change definition of rx wf timer:

"The timer terminal count is set to Twr" to "The timer terminal count is set to Twtf"

Replace last row of Table 49-3 with:

Twtf Wake time fault recovery time 1mS

C/ 49 SC Fig49-16 P 148 L 12 # 29

Broadcom Pillai. Velu

Comment Type ER Comment Status D

The arrow that goes out of TX ACTIVE for the condition T TYPE(tx row) != LI needs to touch the Arc that goes back to TX\_ACTIVE

SuggestedRemedy

Proposed Response Response Status W

C/ 49 SC Fig 49-16 P148 L19 # 30
Pillai, Velu Broadcom

Comment Type T Comment Status D

SCR\_RESET\_2 is a redundant state as the transition out of that state is a UCT to TX\_ACTIVE and scrambler\_reset variable is set to false in TX\_ACTIVE state. The original proposal had this state to assert 1uSec of IDLE codeword after the SCR\_RESET\_1 state. But that extra time is added to the T\_wake Sys time budget. This serves the same purpose. Hence remove this state and rename the previous state from SCR\_RESET\_1 to SCR\_RESET.

SuggestedRemedy

Proposed Response Status W
PROPOSED ACCEPT.

C/ 49 SC Fig49-17 P149 L7 # 31

Pillai, Velu Broadcom

Comment Type TR Comment Status D

RX ACTIVE state should set rx quiet <= FALSE

SuggestedRemedy

Proposed Response Response Status W
PROPOSED ACCEPT.

Comment Type TR Comment Status D

LPI TX state diagram designed only to go through scrambler reset only during WAKE. Hence during refresh the PCS will not detect codewords, if FEC is ON. Which means the receiver will not take the arc from RX\_WAKE to RX\_QUIET shown in LPI receive state diagram. The refresh time for KR PHY is 17usec and rx\_tw\_timer timeout is 13-14usec, hence it is guaranteed that rx\_tw\_timer\_done will be asserted during every refresh cycle.

## SuggestedRemedy

A state is needed between RX\_WAKE and RX\_WTF when rx\_tw\_timer\_done is asserted. This new state (RX\_REFRESH\_WITH\_FEC), should set Start rx\_wf\_timer and the transition out of it needs to be

- 1. An arc to RX QUITE for energy detect = false.
- 2. And arc to RX\_WTF for rx\_rwt\_timer\_done + (R\_TYPE(rx\_coded != LI \* rx\_block\_lock).

Remove the arc going from RX\_WTF to RX\_SLEEP and also to RX\_QUIET. Remove setting Start rx wf timer.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

This problem is fixed by reducing the refresh time (see comment #44)

Cl 49 SC Fig 49-17 P149 L 17 # 33

Pillai, Velu Broadcom

Comment Type TR Comment Status D

Transition from RX\_SLEEP to RX\_ACTIVE needs be R\_TYPE(rx\_coded) = IDLE and not R\_TYPE(rx\_coded != LI. When Transmitter deactivates, received codewords may not be LI.

SuggestedRemedy

Proposed Response Status W

Cl 49 SC Figure-49-15 P147 L # 34

Pillai, Velu Broadcom

Comment Type TR Comment Status D

Rx PCS state machine resets to INIT state when rx\_block\_lock is lost. This can happen during Rx LPI state machine transitions into RX QUIET state.

SuggestedRemedy

RX PCS should reset to INIT state only when (reset + r\_test\_mode + hi\_ber + !block\_lock This solution also handles the rx link fail state, where block lock is set to false.

Proposed Response Status W

Cl 49 SC Table 49-2 P150 L12 # 35

Pillai, Velu Broadcom

Comment Type TR Comment Status D

Value of Twl is 17 us. This was the orignal value, before the proposel to use scrambler reset to handle FEC. And this value is also more than the total T wake sys.

SuggestedRemedy

Reduce this value to 12usec.

Proposed Response Status W

PROPOSED ACCEPT.

PROPOSED ACCEPT.

C/ 49 SC Fig 49-17 P 149 L 10 # 36

Pillai, Velu Broadcom

Comment Type TR Comment Status D

Transition out of RX\_ACTIVE back to itself has a condition block\_lock!= rx\_block\_lock. But block\_lock latches rx\_block\_lock inside RX\_ACTIVE. Hence this transition condition is meaning less.

SuggestedRemedy

Instead of the above, please use rx\_block\_lock = FAIL

Proposed Response Status W

PROPOSED REJECT.

Block\_lock only latches rx\_block\_lock on entry to RX\_ACTIVE. Using "block\_lock!= rx\_block\_lock" ensures that any change will cause it to cycle around (allowing all changes in rx\_block\_lock to be reflected in block\_lock).

CI 73A SC P 250 L 32 # 37

Pillai, Velu Broadcom

Comment Type TR Comment Status D

The wording is not representative of the number of pages needed nor does it provide enough information for implementation. Suggested fix is similar to existing wording for other next pages defined in the existing annex.

SuggestedRemedy

Change wording from

"Multiple clauses use next page message code 10 to indicate that EEE technology will follow the transmission of this page [the initial, Message (formatted) next page] with at least one unformatted next pages that contain information defined in 45.2.7.13a." to

"Multiple clauses use next page message code 10 as an identifier for EEE technology. The EEE technology code message shall consist of only a Message next page. The message code field, 000 0000 1010 shall be contained in bits 10:0 and 45.2.7.13.6:0 shall be contained in bits 22:16. The remaining field bits, 47:23 shall be sent as zero and ignored on receipt."

Proposed Response Status W

PROPOSED ACCEPT.

C/ 36 SC 36.2.5.2.1 P75 L5 # 38

Barnette, James Vitesse Semiconducto

Comment Type TR Comment Status D

In Figure 36-6 PCS transmit code-group state diagram, there is no implementation of code-group generation for ordered-set tx\_o\_set=/LI/.

SuggestedRemedy

- Add 5 new states, LPI\_DISPARITY\_TEST, LPI\_DISPARITY\_WRONG, LPI\_I1B, LPI\_DISPARITY\_OK, and LPI\_I2B that have a similar flow as the 5 existing states, IDLE\_DISPARITY\_TEST, IDLE\_DISPARITY\_WRONG, IDLE\_I1B, IDLE\_DISPARITY\_OK, and IDLE\_I2B.
- Add a new arc from GENERATE\_CODE\_GROUPS to LPI\_DISPARITY\_TEST when tx\_o\_set=/Ll/.
- Replicate the existing arcs that are in the IDLE\_\* states into the new LPI\_\* states includeing the exit to the common GENERATE\_CODE\_GROUPS state.
- Change the tx\_code-group output in the new LPI\_I1B and LPI\_I2B states from /D5.6/ and /D16.2/ to /D6.5/ and /D26.4/, respectively

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See response to comment #40

Comment Type TR Comment Status D

When detect\_lpidle is asserted and the state transitions from RX\_ACTIVE to RX\_SLEEP, the next ordered set to be received is an LPI, which is /K28.5/D6.5/ or /K28.5/D26.4/. Then after /K28.5/ is received, detect\_idle would be asserted using the definition from section 36.2.5.1.3 and the state would transition to RX\_ACTIVE. When /D6.5/ or /D26.4/ is received then detect\_lpidle is asserted, thus transitioning back to RX\_SLEEP from RX\_ACTIVE. This means, as long as the LPI ordered set is received then the state transitions back and forth between RX\_ACTIVE and RX\_SLEEP and that is clearly not the intended behavior.

### SuggestedRemedy

To avoid toggling back and forth, while in RX\_SLEEP active, detect\_idle should be sampled only for every other code word. This way when an ordered set /K28.5//<some\_code\_word>/ is received, then detect\_idle or detect\_lpidle will go high appropriately after decoding <some\_code\_word>. One possible way to do this is to split RX\_SLEEP into two states RX\_SLEEP\_1 and RX\_SLEEP\_2, both having the same functionality of the existing RX\_SLEEP state.

When detect\_lpidle is asserted, RX\_ACTIVE/RX\_WAKE/RX\_WTF would transition into RX\_SLEEP\_1 state and as long as detect\_lpidle is asserted state would always be RX\_SLEEP\_1. While in RX\_SLEEP\_1, detect\_idle would transition to RX\_SLEEP\_2 state. If current state is RX\_SLEEP\_2 and detect\_idle is asserted, then state transitions to RX\_ACTIVE else if detect\_lpidle is asserted then state transitions to RX\_SLEEP\_1. If signal\_detect fails while either in state RX\_SLEEP\_1 or RX\_SLEEP\_2 then state transitions to RX\_QUIET.

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

The commenter has correctly identified the behavior problem.

The same can be achieved by including the term "\* ODD" (qualifying detect\_idle) in the exit conditions for RX\_SLEEP; RX\_WAKE and RX\_WTF.

Cl 36 SC 36.2.5.2.1 P75 L11 # 40

Barrass, Hugh Cisco

Comment Type **T** Comment Status **D**There needs to be a transition for tx o set = /LI/

SuggestedRemedy

Change " $tx_o_set = /I/$ " to " $tx_o_set = /I/ + /LI/$ "

Change state IDLE\_I1B: "tx\_code-group <= /D5.6/" to "if tx\_o\_set = /I/ then tx\_code-group <= /D5.6/ else tx\_code-group <= /D6.5/"

Change state IDLE\_I2B: "tx\_code-group <= /D16.2/" to "if tx\_o\_set = /I/ then tx\_code-group <= /D16.2/ else tx\_code-group <= /D26.4/"

Proposed Response Status W PROPOSED ACCEPT.

Cl 36 SC 36.2.5.2.1 P73 L 44 # 41

Barrass, Hugh Cisco

Comment Type E Comment Status D

Figure references wrong

SuggestedRemedy

Change "Figures 36-1 and 36-2" to "figures 36-5 and 36-6" (with active links).

Also, P.74, change figure title to "Figure 36-5"

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 36 SC 36.2.5.2.8 P80 L 23 # 42

Barrass, Hugh Cisco

Comment Type T Comment Status D

The "loop" transitions for states TX\_SLEEP, TX\_QUIET and TX\_REFRESH are all invalid because they would cause the timers to keep restarting (even if they didn't, they would be redundant since the state machine remains in the state unless an exit is valid.

SuggestedRemedy

Delete the "loop" transitions for states TX\_SLEEP, TX\_QUIET and TX\_REFRESH.

Proposed Response Status W

CI 48 SC 48.2.6.2.5 P134 L 21 # 43
Barrass, Hugh Cisco

Comment Type T Comment Status D

The "loop" transitions for states TX\_SLEEP, TX\_QUIET and TX\_REFRESH are all invalid because they would cause the timers to keep restarting (even if they didn't, they would be redundant since the state machine remains in the state unless an exit is valid.

SuggestedRemedy

Delete the "loop" transitions for states TX SLEEP, TX QUIET and TX REFRESH.

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 49 SC 49.2.13.3.1 P150 L10 # 44

Barrass, Hugh Cisco

Comment Type T Comment Status D

It doesn't make sense that the refresh time is longer than the time that the receiver is allowed to recover a wake signal. This also poses problems for the receive LPI state machine.

SuggestedRemedy

Change T(ul) to 11uS

Proposed Response Status W

PROPOSED ACCEPT.

C/ 49 SC 49.2.13.3.1 P148 L 20 # 45
Barrass, Hugh Cisco

Comment Type T Comment Status D

The "loop" transitions for states TX\_SLEEP, TX\_QUIET and TX\_REFRESH are all invalid because they would cause the timers to keep restarting (even if they didn't, they would be redundant since the state machine remains in the state unless an exit is valid.

SuggestedRemedy

Delete the "loop" transitions for states TX\_SLEEP, TX\_QUIET and TX\_REFRESH.

Proposed Response Response Status W

PROPOSED ACCEPT.

CI **00** SC **0** P L # 46

Brown, Matt AMCC

Comment Type ER Comment Status D

In many of the state machine figures, new transition criteria include comparison of boolean variable with boolean value (e.g., energy\_detect = FALSE). This comparison is redundant and is inconsistent in style.

SuggestedRemedy

Replace all instances in draft as follows:

"<boolean\_variable> = TRUE" with "<boolean\_variable>"

"<boolean\_variable> = FALSE" with "!<boolean\_variable>"

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Recommended change will be made where it does not, by itself, cause a change in the base text of the draft.

In places where this would create a change in the base text that is not required by the objectives of this task force, i.e., it is a service to humanity, the editors will use their discretion.

Cl 36 SC 36.2.5.1.3 P72 L 32 # 47

Brown, Matt AMCC

Comment Type T Comment Status D

What is an "enumerated variable"?

SuggestedRemedy

Change "enumerated" to "boolean".

Proposed Response Status W

PROPOSED ACCEPT.

Cl 46 SC 46.3.1.2 P121 L 10 # 48

Brown, Matt AMCC

Comment Type ER Comment Status D

The 06 character is often referred to in subsequent sections as the LP\_IDLE character so should have this label here.

SuggestedRemedy

Add "LP\_IDLE" (all capitals) label under description in row with TXD = 06.

Proposed Response Response Status W

Cl 48 SC 48.2.3 P 126 # 49 L 30 **AMCC** Brown, Matt Comment Type ER Comment Status D The diagram shows XGMII and PCS encoding spanning all LPI states but labels only the WAKE cycle. SuggestedRemedy Label columns 1-2 and 16-18 as active time. Label columns 3 to 15 as LPI time. Label columns 3 to 9 and LPI sleep/quiet/refresh time. Proposed Response Response Status W PROPOSED ACCEPT. C/ 48 SC 48.2.4 P 127 L 29 # 50 Brown, Matt **AMCC** Comment Type T Comment Status D Table 48-2 footnote (a) refers to "rules described below". Not clear to what it is referring. SuggestedRemedy Change "below" to "in 48.2.4.2". Proposed Response Response Status W PROPOSED ACCEPT. C/ 48 P 127 # 51 SC 48.2.4 L 53 Brown. Matt **AMCC** Comment Type T Comment Status D Table 48-3 footnote (a) refers to "rules described below". Not clear to what it is referring. SuggestedRemedy Change "below" to "in 48.2.4.2".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 48 SC 48.2.4.2 P128 L 26 # 52

Brown, Matt **AMCC** 

Comment Type ER Comment Status D Clarify that this means LP IDLE characters.

SuggestedRemedy

Change LP IDLE to LP IDLE characters.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 48 P128 SC 48.2.6.1.2 L 47

Brown, Matt **AMCC** 

Comment Type ER Comment Status D

This is not an "alias". ||LPIDLE|| is not the same as ||I||.

SuggestedRemedy

Change definition of ||LPIDLE|| to ...

"Low power idle ordered sets are a special case of Idle ordered sets (||I||) transmitted during low power idle mode as described in 48.2.4.2."

Alternately, make changes suggested for 48.2.4.2 and delete this defition altogether.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See resolution to comment #54

Cl 48 SC 48.2.4.2 P128 14 # 54 Brown, Matt AMCC

Comment Type ER Comment Status D

Define low power idle ordered sets here rather than as alias in comment section.

SuggestedRemedy

Change title to "48.2.4.2 Idle (||I||) and Low Power Idle (||LPIDLE||)

Add the following the paragraph on line 38 of page 128 as follows:

"The low power idle ordered set ||LPIDLE|| is a special of ||I|| where low power idle is ..."

Also, deleted the defintion of ||LPIDLE|| in section 48.2.6.1.2 on page 128 line 47.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Move the definition of ||LPIDLE|| from 48.2.6.1.2 to the end of paragraph starting line 38, page 128.

"Reporting of Low power Idle is indicated by ||LPIDLE||."

Cl 48 SC 48.2.6.1.3 P129 L6 # 55
Brown, Matt AMCC

Comment Type T Comment Status D

deskew\_align\_status is the same as align\_status used to be not as it is. Need to adopt old align\_status definition for deskew\_align\_status and re-define align\_status.

SuggestedRemedy

Delete current defintion of deskew\_align\_status.

Pull in definition from 802.3-2008 for align status and rename from "align\_status" to "deskew\_align\_status":

deskew\_align\_status

A parameter set by the PCS Deskew process to reflect the status of the ane-to-lane codegroup alignment.

Values:

FAIL; The deskew process is not complete. OK; All lanes are synchronized and aligned.

Re-define align status as follows ...

align\_status

Variable equivalent to deskew\_align\_status when not in LPI mode. During LPI mode align\_status is overridden by the LPI receive state machine as specified in Table 48-9.

Proposed Response Status W

PROPOSED REJECT.

The definitions, as written, are adequate.

Cl 48 SC 48.2.6.1.3 P129 L 10 # 56

Comment Status D

Brown, Matt AMCC

What is an "enumerated variable"?

SuggestedRemedy

Comment Type T

Change "enumerated" to "boolean".

Proposed Response Response Status W

PROPOSED ACCEPT.

C/ 48 SC 48.2.6.1.3 P129 L 10 # 57

Brown, Matt AMCC

Comment Type T Comment Status D

When rx\_lpi\_active is FALSE it may not be "capable of receiver data" as there may be an input fault.

SuggestedRemedy

Change "capable of receiving data" to "is not in the LPI mode".

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change "when it is in an active state and capable of receiving data"

to "when it is in an active state and is not restricted by the LPI receive state machine"

Cl 48 SC 48.2.6.1.3 P129 L14 # 58

Brown, Matt AMCC

Comment Type T Comment Status D

rx lpi fail also indicates that the link has failed during LPI.

SuggestedRemedy

Append the sentence with "or if the link has otherwise failed".

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Append the sentence with "or if the link has otherwise failed during LPI".

Cl 48 SC 48.2.6.1.3 P129 L 17 # 59

Brown, Matt AMCC

Comment Type T Comment Status D

Need text to indicate the significance of rx guiet.

SuggestedRemedy

Add the following sentence...

When this variable is TRUE it indicates that receive PCS and PMD may power-down non-

essential functions.

Proposed Response Status W

Delete rx\_deact\_timer and description.

Proposed Response

PROPOSED ACCEPT.

SC 48.2.6.1.3 Cl 48 P 129 # 60 L 20 **AMCC** Brown, Matt Comment Type Т Comment Status D Need text to indicate the significance of tx guiet. SuggestedRemedy Add the following sentence... When this variable is TRUE it indicates that transmit PCS and PMD may power-down nonessential functions. Proposed Response Response Status W PROPOSED ACCEPT. C/ 48 SC 48.2.6.1.5 P 129 / 26 # 61 Brown, Matt AMCC Comment Type T Comment Status D LPI fail timer is no longer used in this section. SuggestedRemedy Delete LPI fail timer and description. Proposed Response Response Status W PROPOSED ACCEPT. C/ 48 SC 48.2.6.1.5 P 129 L 31 # 62 Brown, Matt **AMCC** Comment Type T Comment Status D rx\_deact\_time is no longer used in this section. SuggestedRemedy

Response Status W

C/ 48 SC 48.2.6.1.5 P130 L3 # 63
Brown, Matt AMCC

Comment Type T Comment Status D

The tx tg timer is part of the PCS LPI transmit state machine not PMD receiver.

SuggestedRemedy

Change "PMD's receiver enters the TX\_QUIET state" to "LPI transmit state machine enters the TX\_QUIET state".

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

The same typo is in the definitions for tx\_ts\_timer, tx\_tq\_timer, and tx\_tr\_timer. Change the 3 instances of "receiver" to "transmitter."

CI 48 SC 48.2.6.1.5 P130 L7 # 64

Brown, Matt AMCC

Comment Type T Comment Status D

The tx\_tr\_timer is part of the PCS LPI transmit state machine not PMD receiver.

SuggestedRemedy

Change "PMD's receiver enters the TX\_REFRESH state" to "LPI transmit state machine enters the TX\_REFRESH state".

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See response to comment #63

Comment Type TR Comment Status D

PMD RXQUIET.request(rx\_quiet) description not correct.

SuggestedRemedy

Delete current description and replace with the following:

"A boolean signal sent by the PCS to the PMD to indicate, when the value is TRUE, that the PMD may power down non-essential functions. The value of

PMD\_RXQUIET.request(rx\_quiet) is equal to the rx\_quiet variable as set in the LPI receive state machine.

Proposed Response Response Status W

PROPOSED REJECT.

The current definition is adequate.

Comment Type

SuggestedRemedy

Proposed Response

ER

Change "reset=TRUE" to "reset"

PROPOSED ACCEPT.

Comment Status D

Response Status W

Redundant and out of style to equate variable to Boolean value.

# 69

# 70

# 71

L 8

L 10

L 16

SC 48.2.6.1.6 Cl 48 P 130 # 66 Cl 48 SC 48.2.6.2.5 P 135 L 22 **AMCC** Brown, Matt **AMCC** Brown, Matt Comment Type TR Comment Status D Comment Type т Comment Status D PMD TXQUIET.request(tx quiet) description not correct. In Figure 48-9b, need to initialize rx guiet variable. SuggestedRemedy SuggestedRemedy In RX ACTIVE state add line... Delete current description and replace with the following: "A boolean signal sent by the PCS to the PMD to indicate when the value is TRUE that the "rx\_quiet <= FALSE" PMD must disable the driver output and may power down non-essential functions. The Proposed Response Response Status W value of PMD\_TXQUIET.request(tx\_guiet) is equal to the rx\_guiet variable as set in the LPI PROPOSED ACCEPT. receive state machine." Proposed Response Response Status W Cl 48 SC 48.2.6.2.5 P 135 PROPOSED REJECT. **AMCC** Brown, Matt The current definition is adequate. Comment Type T Comment Status D In Figure 48-9b, in the transition from RX ACTIVE state to itself the condition ||IDLE|| is C/ 48 SC 48.2.6.2.1 P 131 L 52 # 67 unnecessary since the only purpose for this transition appears to be to keep align status Brown, Matt **AMCC** up to date. Comment Type Т Comment Status D SuggestedRemedy In the notes at the bottom of Figure 48-6... Change "||IDLE|| + align status != deskew align status" to "align status != /D20.5/ is replaced in one row not column. deskew align status". SuggestedRemedy Perhaps the intent was the following... Replace "one column is replaced" with "one row is replaced". "!||LPIDLE|| \* align status != deskew align status" Proposed Response Response Status W Proposed Response Response Status W PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE. C/ 48 SC 48.2.6.2.5 P 134 L 11 Change "IIIDLEII + align status != deskew align status" to "align status != # 68 deskew align status". **AMCC** Brown, Matt Cl 48 SC 48.2.6.2.5 P 135

> AMCC Brown, Matt

Comment Type E Comment Status D

In Figure 48-9b, there are two instances of ||IDLE|| where the right-hand bars appear to be "II" (two "I's") not "||" (two bars).

SuggestedRemedy

Replace IIIDLE|| with ||IDLE||.

Proposed Response Response Status W

PROPOSED ACCEPT.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Comment ID

Page 14 of 25

Cl 48 P 135 # 72 SC 48.2.6.2.5 L 26 **AMCC** Brown, Matt

Comment Type TR Comment Status D

In Figure 48-9b, the transition from RX WAKE to RX QUIET when signal detect=FAIL could be and endless loop in realitic failure conditions such as link partner driver soft failing where the signal level on the link is sporadic. The problem is caused by the timer being continually reset.

#### SuggestedRemedy

The suggested remedy is to create a new state that prevents the timer from being reset every time a false wake or refresh is detected.

Create a new state between RX SLEEP and RX QUIET.

Call the new state RX QUIET INIT (or other suitable name).

The transition criteria from RX SLEEP to RX QUIET INIT will be "signal detect=fail". Within RX QUIET INIT state include the following action:

"Start rx tw timer"

The transition criteria from "RX QUIET INIT to "RX QUIET" is UCT (unconditional transition).

In RX QUIET state delete Start rx tg timer. (This is the key to letting the timer run.)

As a result, regardless of how many transitions occur between RX\_QUIET and RX\_WAKE or RX WTF due to sporadic energy, the rx tq timer will time out and an fault will be detected.

Proposed Response

Response Status W

PROPOSED REJECT.

The proposed remedy will not support refresh cycles. The quiet / refresh / quiet / refresh sequence could indeed look like a sporadically failing transmitter but a remedy for such a failing would need to be carefully thought out.

The commenter is invited to submit comments against this during Working Group ballot.

Cl 48 P 135 L 13 SC 48.2.6.2.5 # 73

Brown, Matt **AMCC** 

Comment Type TR Comment Status D

In Figure 48-9b, it is possible to be stuck in RX SLEEP state if the link partner driver continues to send anything other than IIIDLEII and does not disable its output.

#### SuggestedRemedy

Create new timer rx ts timer with terminal time TSLRX slightly larger than TSL. Define new timer in 48.2.6.1.5 as follows: "This timer is started when the LPI receive state machine enters the RX SLEEP state. The timer terminal counter is set to TSLRX. When the timer reach the terminal count it will set rx ts timer done = TRUE." Add action to RX SLEEP state "Start rx ts timer".

Add transition to RX LINK FAIL state with criteria "rx ts timer done".

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

A new timer is unnecessary.

In state RX SLEEP, add action "start rx to timer"

Add a transition from RX SLEEP to RX LINK FAIL "rx tg timer done"

Cl 48 P 134 # 74 SC 48.2.6.2.5 L 37 Brown, Matt AMCC

Comment Type T Comment Status D

In the LPI receiver state diagram in Figure 48-3, the exit criteria from RX WTF and RX WAKE required detection of either ||LPIDLE|| or ||IDLE||. For the latter, the length of the wake sequence is not enforced by the PCS but rather depends upon the layer above to give the correct value. This layer may be on another device so compliance may not be easy to guarantee.

#### SuggestedRemedy

Make the following changes to the LPI transmit state machine.

Create new timer "tx wake timer" with terminal count equal to required wake time TWR. In TX REFRESH state add the action "Start tx wake timer".

Change the criteria for transition from TX\_REFRESH to TX\_ACTIVE to "TX != LPIDLE \* tx wake timer done".

Proposed Response Response Status W

PROPOSED REJECT.

This change will require some discussion amongst interested parties and does not weigh on the "technical completeness" of the draft.

The commenter is urged to resubmit the comment during the Working Group ballot phase.

Cl 48 SC 48.2.6.2.5 P 136 # 75 Cl 49 SC 48.2.13.2.2 P 144 L 28 # 78 L 8 **AMCC** Brown, Matt **AMCC** Brown, Matt Comment Type Т Comment Status D Comment Type т Comment Status D TUL definition in Table 48-9 is incorrect. TUL is used by TX state machine, but current What is an "enumerated variable"? definition sounds like a receiver specification. SuggestedRemedy SuggestedRemedy Change "enumerated" to "boolean". Replace TUL definition with "Local refresh time from signal enable to signal disable." Proposed Response Response Status W Proposed Response Response Status W PROPOSED ACCEPT. PROPOSED ACCEPT. Cl 49 SC 49.1.6 P 139 L 22 C/ 48 SC 48.2.6.2.5 P 136 L 18 # 76 Brown, Matt **AMCC AMCC** Brown, Matt Comment Type ER Comment Status D Comment Type ER Comment Status D Signal from PMA is signal detect not energy detect. TDA defined in Table 48-10 is no longer used. SuggestedRemedy SuggestedRemedy Change energy\_detect to signal\_detect. Delete row defining TDA. Proposed Response Response Status W Proposed Response Response Status W PROPOSED REJECT. PROPOSED ACCEPT. The signal is, indeed, called energy\_detect - see 51.8a.1 for definition. Cl 48 SC 48.2.6.2.5 P 135 L7 # 77 C/ 49 SC 49.2.4.7 P139 L **52** # 80 Brown, Matt **AMCC AMCC** Brown, Matt Comment Type T Comment Status D Comment Type ER Comment Status D rx lpi fail is not set to any value other than FALSE. Is this a necessary variable? Clarify sentence. SuggestedRemedy SuggestedRemedy In RX\_ACTIVE state delete "rx\_lpi\_fail". Replace "idle control code 0x00 is replaced with 0x07" with "low power idle control Also, delete rx\_lpi\_fail definition on page 129. character /LI/ (0x07) is sent continuously in place of /I/." Proposed Response Response Status W Proposed Response Response Status W PROPOSED ACCEPT. PROPOSED ACCEPT.

# 84

# 85

# 86

SC 49.2.13.2.3 Cl 49 P 141 L 43 # 81 Cl 49 SC 49.2.13.2.2 P 144 L 20 **AMCC** Brown, Matt **AMCC** Brown, Matt Comment Type Т Comment Status D Comment Type т Comment Status D LI is by definition here not a special case of C type, rather its a type on its own. The energy detect variable is derived from the message PMA SIGNAL.indication(signal detect). Define it as such. SuggestedRemedy SuggestedRemedy Replace "LI type is a special case of the C type where" with "LI type is supported where". Replace definition for energy\_detect with ... Proposed Response Response Status W "A boolean variable that indicates when energy is detected at the receiver. Set to TRUE if PROPOSED ACCEPT. PMA\_SIGNAL.indication(signal\_detect) = OK or FALSE if PMA SIGNAL.indication(signal detect) = FAIL." Cl 49 SC 49.2.13.2.3 P 143 L 46 # 82 Proposed Response Response Status W Brown, Matt **AMCC** PROPOSED REJECT. Comment Type ER Comment Status D See 51.8a.1 LI is by definition here not a special case of C type, rather its a type on its own. C/ 49 SC 49.2.13.2.2 P 144 L 20 SuggestedRemedy Brown, Matt **AMCC** Replace "LI type is a special case of the C type where" with "LI type is supported where". Comment Type TR Comment Status D Proposed Response Response Status W rx\_block\_lock is not accurate. rx\_block\_lock is equal to what was block-lock and PROPOSED ACCEPT. block lock depends on receive LPI state. C/ 49 SC 49.2.4.4 P 139 # 83 SugaestedRemedy L 22 Brown, Matt **AMCC** Replace rx block lock definition with the current block lock definition: "Boolean variable that is set true when receiver acquires block delineation." Comment Status D Comment Type T Re-define block lock as follows: Energy detect is indicated through PMA\_SIGNAL.indication(signal\_detect). "Boolean variable is set true when receiver acquires block delineation when receive LPI mode is not active and set based on the LPI receive state machine when receive LPI mode SuggestedRemedy is active." Remove energy detect line and lable from figure. Proposed Response Response Status W Proposed Response Response Status W PROPOSED REJECT. PROPOSED REJECT. The definition is adequate as it is. See 51.8a.1 Cl 49 SC 49.2.13.2.2 P144 L 32 **AMCC** Brown, Matt Comment Type ER Comment Status D Clarify rx quiet definition.

SuggestedRemedy

Proposed Response

PROPOSED ACCEPT.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Comment ID

Page 17 of 25

Change "while in the RX QUIET state" to "while the reciever is in the RX QUIET state".

Response Status W

SuggestedRemedy

Proposed Response

PROPOSED ACCEPT.

Delete rx\_deact\_timer and definition.

Response Status W

Proposed Response

PROPOSED ACCEPT.

Cl 49 SC 49.2.13.2.2 P 144 L 40 # 87 **AMCC** Brown, Matt Comment Type T Comment Status D Clarify scrambler\_reset definition. SuggestedRemedy Change "registers of the scrambler" to "bits of the scrambler delay line". Proposed Response Response Status W PROPOSED ACCEPT. Cl 49 SC 49.2.13.2.2 P 144 # 88 L 39 Brown, Matt **AMCC** Comment Type T Comment Status D Clarify scrambler\_reset definition. SuggestedRemedy Change "this variable is used" to "the boolean variable is used". Proposed Response Response Status W PROPOSED ACCEPT. C/ 49 SC 49.2.13.2.2 P 144 L 39 # 89 **AMCC** Brown. Matt Comment Type T Comment Status D Clarify scrambler\_reset\_enable definition. SuggestedRemedy Change "A variable used" to "A boolean variable used". Proposed Response Response Status W PROPOSED ACCEPT. C/ 49 # 90 SC 49.2.13.2.5 P 145 L 8 Brown, Matt **AMCC** Comment Type ER Comment Status D rx\_deact\_timer is no longer used

C/ 49 SC 49.2.13.3 P 147 L4 # 91 Brown, Matt AMCC Comment Type ER Comment Status D Incorrect use of /LI/. SuggestedRemedy In RX LI state replace /LI/ with LI. Proposed Response Response Status W PROPOSED ACCEPT. Cl 49 SC 49.2.13.3.1 P 148 L 5 Brown, Matt **AMCC** Comment Type ER Comment Status D Redundant and out of style to equate variable to Boolean value. SuggestedRemedy Change "reset=TRUE" to "reset"

Response Status W

Cl 49 P 149 # 93 SC 49.2.13.3.1 L 21 **AMCC** Brown, Matt

Comment Type TR Comment Status D

In Figure 49-17, the transition from RX WAKE and RX WTF to RX QUIET when !energy detect could be an endless loop in realitic failure conditions such as link partner driver soft failing where the signal level on the link is sporadic or taps at wrong value. The problem is caused by the timer being continually reset.

#### SuggestedRemedy

The suggested remedy is to create a new state that prevents the timer from being reset every time a false wake or refresh is detected.

Create a new state between RX SLEEP and RX QUIET.

Call the new state RX QUIET INIT (or other suitable name).

The transition criteria from RX SLEEP to RX QUIET INIT will be "signal detect=fail". Within RX QUIET INIT state include the following action:

"Start rx tw timer"

The transition criteria from "RX QUIET INIT to "RX QUIET" is UCT (unconditional transition).

In RX QUIET state delete Start rx tg timer. (This is the key to letting the timer run.)

As a result, regardless of how many transitions occur between RX\_QUIET and RX\_WAKE or RX WTF due to sporadic energy, the rx tg timer will time out and a fault will be detected.

Proposed Response Response Status W

PROPOSED REJECT.

The proposed remedy will not support refresh cycles. The quiet / refresh / quiet / refresh sequence could indeed look like a sporadically failing transmitter but a remedy for such a failing would need to be carefully thought out.

The commenter is invited to submit comments against this during Working Group ballot.

C/ 49 SC 49.2.13.3.1 P 149 / 21 # 94 AMCC

Brown, Matt

Comment Status D Comment Type ER

Redundant and out of style to equate variable to Boolean value.

SuggestedRemedy

Replace all instances of "energy\_detect=false" with "!energy\_detect".

Replace all instances of "energy detect=true" with "energy detect".

Replace "reset=TRUE" with "reset".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 49 P 149 L 21 SC 49.2.13.3.1 # 95

Brown, Matt **AMCC** 

Comment Type ER Comment Status D

Incorrect comparison in Fig 49-17. rx block lock is a boolean variable.

SuggestedRemedy

Replace all instances of "rx block lock=OK" with "rx block lock".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 49 SC 49.2.13.3.1 P149 L 21

Brown, Matt **AMCC** 

Comment Type T Comment Status D

Incorrect variable name in transition criteria from RX ACTIVE to RX SLEEP in Fig 49-17.

SuggestedRemedy

Change "R\_TYPE(rx\_raw)" to "R\_TYPE(rx\_coded)".

Proposed Response Response Status W

PROPOSED ACCEPT.

C/ 49 SC 49.2.13.3.1 P149 L 21

Brown, Matt **AMCC** 

Comment Type T Comment Status D

rx\_lpi\_fail is not set to any value other than FALSE and is not defined in this Clause. Is this a necessary variable?

SuggestedRemedy

In RX ACTIVE state delete "rx lpi fail"

Proposed Response Response Status W

SuggestedRemedy

Proposed Response

Delete row specifying TDA.

PROPOSED ACCEPT.

Response Status W

Cl 49 SC 49.2.13.3.1 P 149 # 98 Cl 49 P 149 L 8 # 101 L 11 SC 49.2.13.3.1 **AMCC** Brown, Matt **AMCC** Brown, Matt Comment Type Т Comment Status D Comment Type Т Comment Status D In Figure 49.17, in the transition from RX ACTIVE state to itself the the criteria logic In Figure 49-17, need to initialize rx guiet variable. doesn't seem correct. SuggestedRemedy SuggestedRemedy In RX ACTIVE state add line... Change criteria to the following (changing OR to AND) "rx\_quiet <= FALSE" "R TYPE(rx coded) != LI \* align status != deskew align status" Proposed Response Response Status W Proposed Response Response Status W PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE. CI 72 SC 72.6.5 P 209 L 9 # 102 Using the same reasoning as for comment #70: **AMCC** Brown, Matt Change criteria to "align status != deskew align status" Comment Type T Comment Status D Clarification of Tx target level. No need to specify "maximum" value. Also, the values are # 99 C/ 49 SC 49.2.13.3.1 P 150 *L* 11 trained not negotiated. Brown, Matt **AMCC** SuggestedRemedy Comment Type T Comment Status D Replace "greater than 90% of the negotiated maximum value" with "greater than 90% of In Table 49-2, redefine TUL as transmitter variable. the trained peak-to-peak value". Proposed Response SuggestedRemedy Response Status W Replace "from Signal\_Detect asserted to" to "from start of TX\_REFRESH state to start of". PROPOSED ACCEPT. Proposed Response Response Status W CI 72 SC 72.7.1 P 211 L 16 # 103 PROPOSED ACCEPT. Brown, Matt **AMCC** Cl 49 SC 49.2.13.3.1 P 150 L 28 # 100 Comment Status D Comment Type ER Brown, Matt **AMCC** In table 72-6, fix deact time description. Comment Type ER Comment Status D SuggestedRemedy In Table 49-3, TDA is no longer required. Change description to "Transmitter deactivation time (TTD) from active to LPI quiet.

Proposed Response

PROPOSED ACCEPT.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Comment ID

Response Status W

Cl 72 SC 72.7.1 P211 L18 # 104
Brown, Matt AMCC

Comment Type ER Comment Status D

In table 72-6, fix act. time description.

SuggestedRemedy

Change description to "Transmitter activation time (TTA) from LPI quiet to active.

Proposed Response Response Status W PROPOSED ACCEPT.

C/ 72 SC 72.7.1 P 212 L 15 # 105
Brown, Matt AMCC

Comment Type ER Comment Status D
In Table 72.9, fix deact, time description.

SuggestedRemedy

Change description to "Signal detect deactivation time (TSD) from active to LPI quiet.

Proposed Response Response Status W PROPOSED ACCEPT.

Cl 72 SC 72.7.1 P 212 L 18 # 106
Brown, Matt AMCC

Comment Type ER Comment Status D
In Table 72.9, fix act. time description.

SuggestedRemedy

Change description to "Signal detect activation time (TSA) from LPI guiet to active.

Proposed Response Status W PROPOSED ACCEPT.

Cl 22 SC 22.7a.1 P31 L34 # 107

Grimwood, Michael Broadcom

Comment Type T Comment Status D

To achieve consistency with related comments submitted against Clauses 35 and 46, change link\_status from READY to OK. Clauses 40 and 55 and the associated link monitors do not have a "READY" state in their link monitor functions nor do they specify READY as an allowable value for link status.

SuggestedRemedy

Change:

LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = READY, see 28.2.6.1.1). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link status changing state to READY.

To:

LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, see 24.3.3.2). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link\_status changing state to OK.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

The commenter is correct that "link\_status = OK" indicates that the link is operational not "link\_status = READY" (which indicates that the autoneg has resolved and the link may be enabled). However, the definition of link\_status from 28.2.6.1.1 must be used because it comes from the autonegotiation function and this clause is defining the RS behavior (not the PCS/PMA).

Therefore change "link\_status = READY" to "link\_status = OK" - 2 instances.

Cl 25 SC 25.4.5 P 53 L 28 # [108]
Grimwood, Michael Broadcom

Comment Type TR Comment Status D

For 100BASE-TX EEE, require that jitter specifications be met during low-power operation.

# SuggestedRemedy

In subclause 25.4.5, after the sentence, "The jitter measurement specified in 9.1.9 of TP-PMD may be performed using scrambled IDLEs.", add the following:

During Low Power operation, jitter shall be measured using scrambled SLEEP code groups transmitted during the TX\_SLEEP state. Total transmit jitter with respect to a continuous unjittered reference shall not exceed 1.4 ns peak-to-peak with the exception that the jitter contributions from the clock transitions occurring during TX\_QUIET and the first 5 usec of TX\_SLEEP are ignored. The jitter measurement time period shall be not less than 100 msec and not greater than 1 second.

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 35 SC 35.5a P69 L 54 # 109
Grimwood, Michael Broadcom

Comment Type T Comment Status D

A one second timer for LP\_IDLE.request assertion was applied to Clause 22 but not globally to all PHYs since only Clause 22 defines LP\_IDLE.request.

#### SuggestedRemedy

As has been done in 22.7a, add a section 35.5a entitled "LPI messages". Modify that section for GMII compatibility.

In this new section, add the following requirement to the definition of LP IDLE, request:

LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, see 40.3.3.1). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link\_status changing state to OK.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

This should be added in 35.2.1 (where the rest of the mapping changes are described).

Add after "This behavior and restrictions are the same as described in 22.7a, with the details of the signaling described in 35.2.2."

"LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, according to the underlying PCS/PMA). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link status changing state to OK."

Cl 40 SC 40.6.1.2.5 P106 L 44 # 110

Grimwood, Michael Broadcom

Comment Type T Comment Status D

For consistency with the text earlier in the subsection, eliminate the word "clock" from "uniittered reference clock".

SuggestedRemedy

As outlined in comment above.

Proposed Response Status W

PROPOSED ACCEPT.

Cl 46 SC 46.5a P124 L 34 # [111

Grimwood, Michael Broadcom

Comment Type T Comment Status D

A one second timer for LP\_IDLE.request assertion was applied to Clause 22 but not globally to all PHYs since only Clause 22 defines LP\_IDLE.request.

#### SuggestedRemedy

As has been done in 22.7a, add a section 46.5a entitled "LPI messages". Modify that section for XGMII compatibility.

In this new section, add the following requirement to the definition of LP\_IDLE.request:

LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, see 55.4.5.1). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link status changing state to OK.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

This should be added in 46.1.7 (where the rest of the mapping changes are described).

Add after "This behavior and restrictions are the same as described in 22.7a, with the details of the signaling described in 46.3."

"LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, according to the underlying PCS/PMA). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link\_status changing state to OK."

Cl 78 SC 78.1.2.1.2 P 229 L 17 # 112
Grimwood, Michael Broadcom

Comment Type T Comment Status D

A one second timer for LP\_IDLE.request assertion was applied in Clause 22 for MII but not globally to all PHYs.

SuggestedRemedy

LPI\_IDLE.request shall not be set to ASSERT unless the attached link is operational (i.e. link\_status = OK, see 28.2.6.1.1). LP\_IDLE.request shall remain to be set to DEASSERT for 1 second following link\_status changing state to OK.

Proposed Response Response Status W

PROPOSED ACCEPT.

Comment Type E Comment Status D

In the main 802.3 document, the cext\_errn definition is before the Sdn[1] definition. When the cext\_errn definition change was added back to this document in D1.3, it was inadvertantly placed after the Sdn[1] definition.

SuggestedRemedy

Swap cext\_errn and Sdn[1] definition changes.

Proposed Response Status W

PROPOSED ACCEPT.

C/ 40 SC 40.6.1.2.5 P106 L 42 # 114

McIntosh, James Vitesse

Comment Type TR Comment Status D

The states "WAIT\_SILENT, QUIET, WAKE, and WAKE\_SILENT" are listed with "WAIT\_SILENT" in the list twice. I believe the first instance was intended to be "WAIT\_QUIET".

SuggestedRemedy

Change list to "WAIT\_QUIET, QUIET, WAKE, and WAKE\_SILENT".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 55 SC 55.3.5.4 P174 L17 # [115

McClellan, Brett Solarflare

Comment Type TR Comment Status D

The creation of the T\_BLOCK\_TYPE I and separation of type I from type C when low power idle is supported has broken the transmit state diagram in Figure 55-15. Transitions that only call out C will not be taken when an I block is to be transmitted. For example from state TX C there is no transition for a type I.

SuggestedRemedy

Change state machine transitions that originally included only C to include both C and I.

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

On page 171 I and LI are currently defined as special types of the C field, therefore C includes I.

While the specific example in the comment does not seem to be a problem, there are issues caused by this definition.

For example at the transitions from TX\_WN to TX\_C and to TX\_E (Figure 55-15a), either transition could be taken since LI is a subtype of C in draft 1.4. The transitions from TX\_C to TX\_C and TX\_C to TX\_L on Figure 55-15 have a similar problem. Also on Figure 55-15a TX\_L to TX\_WN and TX\_L to TX\_WE are ambiguous (there are separate transitions on I and C, but I is a subtype of C).

In addition, it was noted that transitions from TX\_C to TX\_E caused by a single error followed by /LI/ will stall the 64B/65B Tx state machine in the error state. An extra transition from TX\_E to TX\_L when /LI/ is detected will be added to the diagram to fix this. A similar transition is required on the receive state diagram.

LI will be redefined as its own type, and not as a subtype of C.

Edited text (to be applied to R BLOCK TYPE and T BLOCK TYPE):

- C; The vector contains a data/ctrl header of 1 and one of the following:
- a) A block type field of 0x1E and eight valid control characters, none of which are /E/ and, if the low power idle function is supported, all of which are not /Ll/;

I; If the optional Low Power Idle function is supported then the I type is a special case of the C type where the vector contains a data/ctrl header of 1, a block type field of 0x1e, and eight control characters of 0x00 (/I/) [see comment #117]

LI: If the optional Low Power Idle function is supported then the LI type occurs when the vector contains a data/ctrl header of 1, a block type field of 0x1e, and eight control characters of 0x06 (/LI/).

The following changes will be made to the state diagrams:

1) remove LI from transition from TX E to TX E on Figure 55-15

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Comment ID

Page 23 of 25

- 2) add transition from TX E to TX L conditioned on /LI/ on Figure 55-15
- 3) change C to (C.!I) on transition from TX\_L to TX\_WE on Figure 55-15a
- 4) change C to (C.II) on transition from TX\_WN to TX\_WE on Figure 55-15a
- 5) change C to (C.II) on transition from TX\_WN to TX\_E on Figure 55-15a
- 6) remove LI on transition from RX\_E to RX\_E on Figure 55-16.
- 7) Add transition from RX\_E to RX\_L on Figure 55-16
- 8) Correct a typo on Figure 55-15a : tx\_lpi\_done=false should be tx\_lpi\_active=false (tidstrom\_02\_1108.pdf)

Also note that the E (circle) entrance to TX\_E has disappeared from the diagram and will be replaced.

Comment Type TR Comment Status D

Both Clause 55 and Clause 49 share a common block encoder (64B/65B and 64B/66B). However the changes made for /LI/ are different between Clause 49 and 55. The control code for Clause 49 is 0x07 while the control code for Clause 55 ix 0x06. These clauses should maintain commonality as much as possible

# SuggestedRemedy

Change the control code for /LI/ in Clause 55 to 0x07. Also make the associated changes to R BLOCK TYPE LI and T BLOCK TYPE LI.

Proposed Response Status W

PROPOSED ACCEPT.

C/ 55 SC 55.3.5.2.4 P171 L3 # 117

McClellan, Brett Solarflare

Comment Type TR Comment Status D

A new T\_BLOCK\_TYPE and R\_BLOCK\_TYPE of LI has been introduced for use in Figure 55-15a and Figure 55-16a. However the control code listed as 0x07 is incorrect. The control code for an idle control character in the 64B/65B encoder is 0x00.

#### SuggestedRemedy

Change the control code for LI from 0x07 to 0x00 on lines 3 and 32 on page 171.

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

After a brief discussion with the commentor it was noted that there is a typo in the comment. LI should be replaced with I in the comment and the suggested remedy.

Change the control code for /l/ from 0x07 to 0x00 on lines 3 and 32 on page 171.

Cl 55 SC 55.3.5.4 P176 L17 # [118

McClellan, Brett Solarflare

Comment Type TR Comment Status D

The creation of the R\_BLOCK\_TYPE I and separation of type I from type C when low power idle is supported has broken the receive state diagram in Figure 55-16. Transitions that only call out C will not be taken when an I block is to be transmitted. For example from state RX C there is no transition for a type I.

#### SuggestedRemedy

Change state machine transitions that originally included only C to include both C and I.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE

See response to comment #115

Cl 99 SC P1 L 30 # 119
Thompson, Geoff Nortel

Comment Type ER Comment Status D

The description on the front page is only a project description, not a draft description

#### SuggestedRemedy

Please expand the description to include where the draft was in the process and a result of what meeting. This sort of information has turned out to be tremendously helpful when it is necessary to go back and pull out old drafts. A macro textual description of what changes went into the particular draft is also very helpful.

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Description will be expanded to include where the draft was in the process and the result of what meeting.

A macro textual description of what changes went into the particular draft may be too long to put into the abstract in general though this will be done if there are a few very significant changes.

Cl 14 SC P16 L # 120
Thompson, Geoff Nortel

Comment Type ER Comment Status D

I find no text added anywhere to clause 14 that states or even gives a hint of the compatibility between 10BASE-T and 10BASE-Te. How is a customer to know how to mix the two on a network?

# SuggestedRemedy

Add a new subclause to clause 14 to address the topic of cross compatibility between 10BASE-T and 10BASE-Te, i. e. the two MDI can be freely mixed as long as the cabling meets the requirements for 10BASE-Te.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change 14.1.1.1 (i) from:

Provides for operation with reduced transmit amplitude for type 10BASE-Te (optional)

to:

Provides for operation with reduced transmit amplitude for type 10BASE-Te (optional). A 10BASE-Te PHY will interoperate with a 10BASE-T PHY if the minimum cabling requirements of a 10BASE-Te PHY are met.

Cl 24 SC 24.1.1 P34 L10 # 121

Thompson, Geoff Nortel

Comment Type ER Comment Status D

The text: "the PHY enters the low power idle mode during periods of low link utilization." is, shall we say, mysterious. There is no "low link utilization" signal available within the PCS/PMA.

### SuggestedRemedy

It would be more appropriate to say something like that the transmitter, and in turn the linked receiver transition into low power mode in response to a command sent across the MII that is expected when the transmitting station is expecting low link utilization.

Proposed Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change the second sentence of the paragraph starting on line 8 to read:

When a transmitting station does not need the full bandwidth of a link with this capability, the LPI agent can use a command across the MII to put the local PHY transmitter and the link partner's receiver into low power idle mode to conserve energy.

Cl 30 SC 30.5.1.1.21 P L 48 # 122

Thompson, Geoff Nortel

Comment Type TR Comment Status D

I don't understand what this attribute indicates. Is it the state of the standard at time of implementation? Or is it the PHYs for which the PCS and higher can support EEE operation?

SuggestedRemedy

Add text to clarify.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change the "BEHAVIOUR" definition to:

A read-only list of the possible PHY types for which the system could support Energy Efficient Ethernet as defined in Clause 78. If Clause 28 or Clause 73 Auto-Negotiation is present, then this attribute will map to the local technology ability or advertised ability of the local device.:

 Cl 22
 SC 22.2.2.6a
 P 28
 L 21
 # 123

 Traeber, Mario
 Infineon Technologies

Comment Type ER Comment Status D

Replace "MAC client" by "LPI agent" to be consistent with 35.2.2.6a

SuggestedRemedy

simply replace the text as suggested.

Proposed Response Status W

PROPOSED ACCEPT.

C/ 22 SC 22.2.2.9a P28 L 52 # 124

Traeber, Mario Infineon Technologies

Comment Type ER Comment Status D

Replace "MAC client" by "LPI agent" to be consistent with 35.2.2.9a

SuggestedRemedy

simply replace the text as suggested.

Proposed Response Response Status W