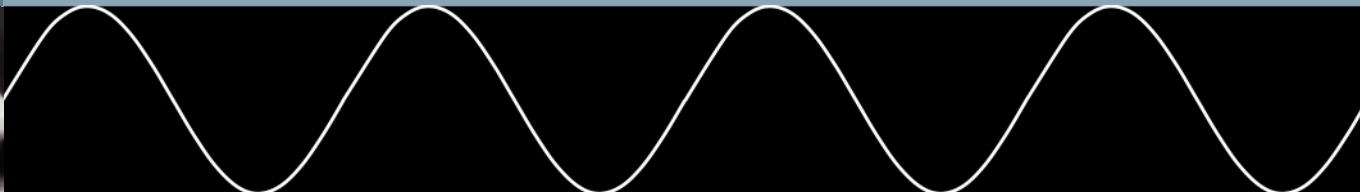


# Class Mapping to Accommodate LTPoE++

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# Current Class Mappings

- Proposed BT Classes at PSE PI (from Abramson\_01\_0914.pdf):
  - 45W: 4-4-1-1-1
  - 60W: 4-4-2-2-2
  - 90W(TBD): 4-4-3-3-3
- LTPoE++ Classes at PSE PI (from LTC4279 datasheet):
  - 45W: 4-4-0-0-0
  - 64W: 4-4-1-1-1
  - 91W: 4-4-2-2-2
  - (128W): 4-4-3-3-3
- As currently defined, LTPoE++ PDs will motorboat with BT PSEs

## Remapped BT Classes

- Map BT classes this way:
  - 45W: 4-4-0-0-0
  - 60W: 4-4-1-1-1 (bump to 65W?)
  - 90W(TBD): 4-4-2-2-2
  - Autoclass: 4-4-3-3-3
- Must define minimum current for T3/T4 “0” to make this work (but we should do this anyway)