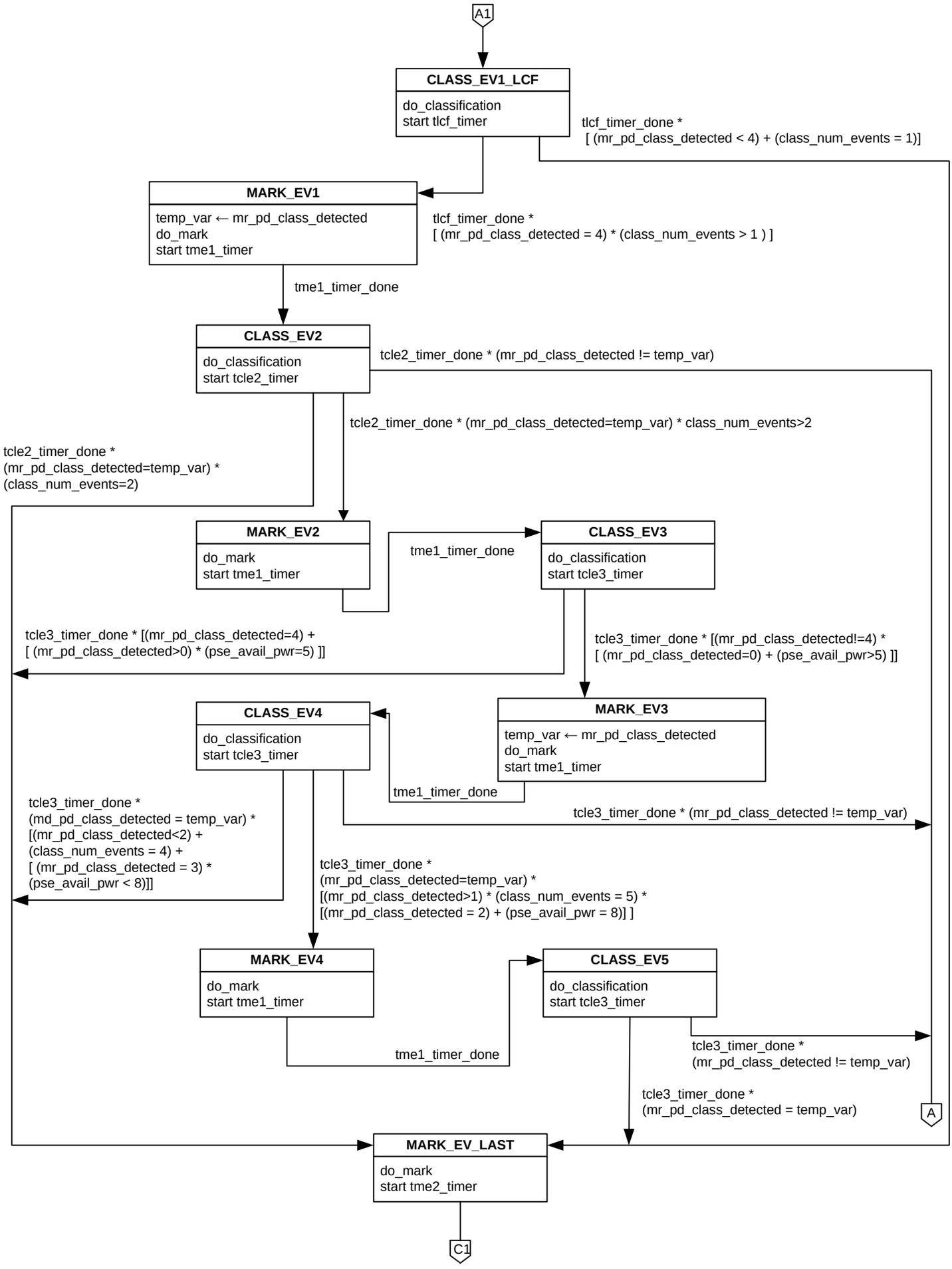
A1
CLASS_EV1_LCF
do_classification
start tlcf_timer
tlcf_timer_done *
[(mr_pd_class_detected < 4) + (class_num_events = 1)]
MARK_EV1
temp_var ← mr_pd_class_detected
do_mark
start tme1_timer
tlcf_timer_done *
[(mr_pd_class_detected = 4) * (class_num_events > 1)]
tme1_timer_done
CLASS_EV2
do_classification
start tcle2_timer
tcle2_timer_done * (mr_pd_class_detected != temp_var)
tcle2_timer_done * (mr_pd_class_detected=temp_var) * class_num_events>2
tcle2_timer_done *
(mr_pd_class_detected=temp_var) *
(class_num_events=2)
MARK_EV2
do_mark
start tme1_timer
tme1_timer_done
CLASS_EV3
do_classification
start tcle3_timer
tcle3_timer_done * [(mr_pd_class_detected=4) +
[(mr_pd_class_detected>0) * (pse_avail_pwr=5)]]
tcle3_timer_done * [(mr_pd_class_detected!=4) *
[(mr_pd_class_detected=0) + (pse_avail_pwr>5)]]
CLASS_EV4
do_classification
start tcle3_timer
MARK_EV3
temp_var ← mr_pd_class_detected
do_mark
start tme1_timer
tme1_timer_done
tcle3_timer_done * (mr_pd_class_detected != temp_var)
tcle3_timer_done *
(md_pd_class_detected = temp_var) *
[(mr_pd_class_detected<2) +
(class_num_events = 4) +
[(mr_pd_class_detected = 3) *
(pse_avail_pwr < 8)]]
tcle3_timer_done *
(mr_pd_class_detected=temp_var) *
[(mr_pd_class_detected>1) * (class_num_events = 5) *
[(mr_pd_class_detected = 2) + (pse_avail_pwr = 8)]]
MARK_EV4
do_mark
start tme1_timer
CLASS_EV5
do_classification
start tcle3_timer
tme1_timer_done
tcle3_timer_done *
(mr_pd_class_detected != temp_var)
tcle3_timer_done *
(mr_pd_class_detected = temp_var)
A
MARK_EV_LAST
do_mark
start tme2_timer
C1