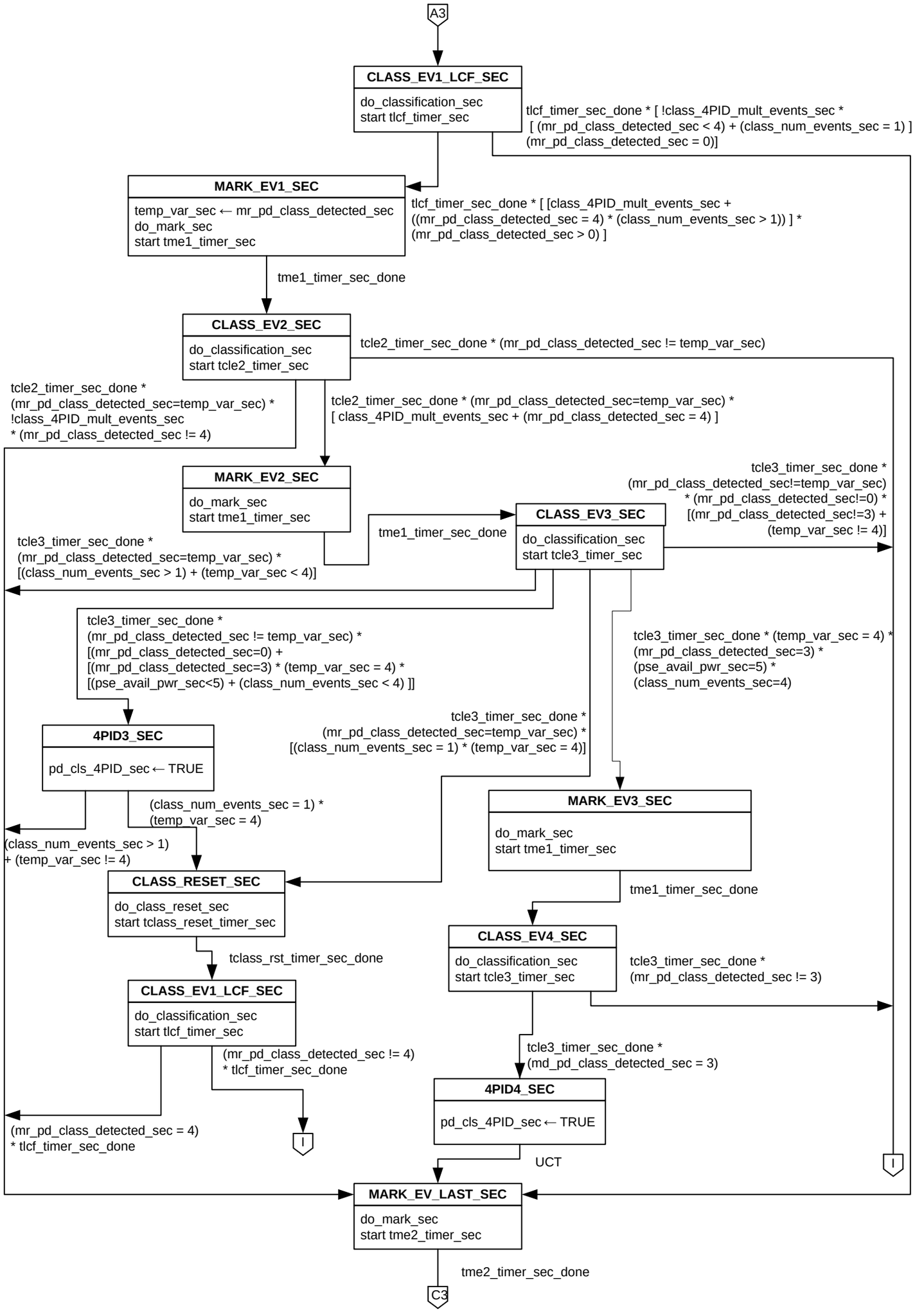
A3
CLASS_EV1_LCF_SEC
do_classification_sec
start tlcf_timer_sec
tlcf_timer_sec_done * [!class_4PID_mult_events_sec *
[(mr_pd_class_detected_sec < 4) + (class_num_events_sec = 1)] +
(mr_pd_class_detected_sec = 0)]
MARK_EV1_SEC
temp_var_sec ← mr_pd_class_detected_sec
do_mark_sec
start tme1_timer_sec
tlcf_timer_sec_done * [[class_4PID_mult_events_sec +
((mr_pd_class_detected_sec = 4) * (class_num_events_sec > 1))] *
(mr_pd_class_detected_sec > 0)]
tme1_timer_sec_done
CLASS_EV2_SEC
do_classification_sec
start tcle2_timer_sec
tcle2_timer_sec_done * (mr_pd_class_detected_sec != temp_var_sec)
tcle2_timer_sec_done *
(mr_pd_class_detected_sec=temp_var_sec) *
!class_4PID_mult_events_sec
* (mr_pd_class_detected_sec != 4)
tcle2_timer_sec_done * (mr_pd_class_detected_sec=temp_var_sec) *
[class_4PID_mult_events_sec + (mr_pd_class_detected_sec = 4)]
MARK_EV2_SEC
do_mark_sec
start tme1_timer_sec
tme1_timer_sec_done
CLASS_EV3_SEC
do_classification_sec
start tcle3_timer_sec
tcle3_timer_sec_done *
(mr_pd_class_detected_sec!=temp_var_sec)
* (mr_pd_class_detected_sec!=0) *
[(mr_pd_class_detected_sec!=3) +
(temp_var_sec != 4)]
tcle3_timer_sec_done *
(mr_pd_class_detected_sec=temp_var_sec) *
[(class_num_events_sec > 1) + (temp_var_sec < 4)]
tcle3_timer_sec_done *
(mr_pd_class_detected_sec != temp_var_sec) *
[(mr_pd_class_detected_sec=0) +
[(mr_pd_class_detected_sec=3) * (temp_var_sec = 4) *
[(pse_avail_pwr_sec<5) + (class_num_events_sec < 4)]]
tcle3_timer_sec_done * (temp_var_sec = 4) *
(mr_pd_class_detected_sec=3) *
(pse_avail_pwr_sec=5) *
(class_num_events_sec=4)
4PID3_SEC
pd_cls_4PID_sec ← TRUE
tcle3_timer_sec_done *
(mr_pd_class_detected_sec=temp_var_sec) *
[(class_num_events_sec = 1) * (temp_var_sec = 4)]
(class_num_events_sec = 1) *
(temp_var_sec = 4)
(class_num_events_sec > 1)
+ (temp_var_sec != 4)
MARK_EV3_SEC
do_mark_sec
start tme1_timer_sec
CLASS_RESET_SEC
do_class_reset_sec
start tclass_reset_timer_sec
tme1_timer_sec_done
CLASS_EV4_SEC
do_classification_sec
start tcle3_timer_sec
tclass_rst_timer_sec_done
tcle3_timer_sec_done *
(mr_pd_class_detected_sec != 3)
CLASS_EV1_LCF_SEC
do_classification_sec
start tlcf_timer_sec
tcle3_timer_sec_done *
(md_pd_class_detected_sec = 3)
(mr_pd_class_detected_sec != 4)
* tlcf_timer_sec_done
4PID4_SEC
pd_cls_4PID_sec ← TRUE
(mr_pd_class_detected_sec = 4)
* tlcf_timer_sec_done
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MARK_EV_LAST_SEC
do_mark_sec
start tme2_timer_sec
tme2_timer_sec_done
C3