

# 143.5 Protocol implementation conformance statement (PICS) proforma for Clause 143, Multi-Channel Reconciliation Sublayer<sup>3</sup>

## 143.5.1 Introduction

The supplier of a protocol implementation that is claimed to conform to Clause 143, Multi-Channel Reconciliation Sublayer, shall complete the following protocol implementation conformance statement (PICS) proforma.

A detailed description of the symbols used in the PICS proforma, along with instructions for completing the PICS proforma, can be found in Clause 21.

## 143.5.2 Identification

### 143.5.2.1 Implementation identification

Supplier <sup>1</sup>	
Contact point for enquiries about the PICS <sup>1</sup>	
Implementation Name(s) and Version(s) <sup>1,3</sup>	
Other information necessary for full identification—e.g., name(s) and version(s) for machines and/or operating systems; System Name(s) <sup>2</sup>	
NOTE 1—Required for all implementations. NOTE 2—May be completed as appropriate in meeting the requirements for the identification. NOTE 3—The terms Name and Version should be interpreted appropriately to correspond with a supplier's terminology (e.g., Type, Series, Model).	

### 143.5.2.2 Protocol summary

Identification of protocol standard	IEEE Std 802.3ca-201x, Clause 143, Multi-Channel Reconciliation Sublayer
Identification of amendments and corrigenda to this PICS proforma that have been completed as part of this PICS	
Have any Exception items been required? No [ ] Yes [ ] (See Clause 21; the answer Yes means that the implementation does not conform to IEEE Std 802.3ca-201x.)	

Date of Statement	
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<sup>3</sup>Copyright release for PICS proformas: Users of this standard may freely reproduce the PICS proforma in this subclause so that it can be used for its intended purpose and may further publish the completed PICS.

### 143.5.3 Generic MCRS

Item	Feature	Subclause	Value/Comment	Status	Support
*MC1	Envelope Header structure	143.3.2	Uses the format shown in Table 143-3	M	Yes [ ]
*MC2	Input Process	143.3.3.6.1	Implements the state diagram as depicted in Figure 143-12	M	Yes [ ]
*MC3	Transmit Process	143.3.3.6.2	Implements the state diagram as depicted in Figure 143-13	M	Yes [ ]
*MC4	Receive Process	143.3.4.5.1	Implements the state diagram as depicted in Figure 143-15	M	Yes [ ]
*MC5	Output Process	143.3.4.5.2	Implements the state diagram as depicted in Figure 143-16	M	Yes [ ]

### 143.5.4 MCRS in Nx25G-EPON

Item	Feature	Subclause	Value/Comment	Status	Support
25M1a	Implement a single-channel MCRS	143.4.1.1	For 25/10G-EPON and 25G/25G-EPON architectures	MC1:M * MC2:M * MC3:M * MC4:M * MC5:M	Yes [ ] N/A [ ]
*25M1b	Implement two-channel MCRS in downstream and a single channel MCRS in upstream	143.4.1.1	50/10G-EPON and 50/25G-EPON architectures	MC1:M * MC2:M * MC3:M * MC4:M * MC5:M	Yes [ ] N/A [ ]
*25M1c	Implement a two-channel MCRS	143.4.1.1	50G-EPON architectures	MC1:M * MC2:M * MC3:M * MC4:M * MC5:M	Yes [ ] N/A [ ]
25M2	Channel bonding for two-channel MCRS	143.4.1.1	Supported in direction with two-channel MCRS	25M1b:M + 25M1c:M	Yes [ ] N/A [ ]

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