

	A	B
1	Area:	Description of Need
2		
3	CLAUSE 1.3 - NORMATIVE REFERENCES	Check rest of document
4		
5	CLAUSE 45.5 - NEEDS CONTENT	Clause 45 PICS need to be done.
6		
7	CLAUSE 78 - NEEDS CONTENT	All content adds are TBD - see 802.3bp for required timing fields
8		
9	CLAUSE 98 TBDs	
10	CL 98.5.2 (P60 L6)	2 TBD slots (for microsecond units) in break_link_timer description.
11	CL 98.5.2 (P60 L39)	TBD inserted for link_fail_inhibit_timer description. (see editor's note at L 31)
12	CL 98.5.2 (P61 L1, L2)	2 TBD slots (for microsecond units) in rx_wait_timer description.
13	CL 98.5.6.3 (P63 L11, L17)	2 TBD slots inserted: one for detection_timer and one for failure_timer.
14	CL 98.6.8 (P64 L10, L36, L48, L49)	6 TBD slots inserted in table for timing variables on break_link_timer, link_fail_inhibit_timer, and rx_wait_timer *PICS CORRESPONDING TO THE ABOVE)
15	CL 98 - GENERAL	Need to check how Figure 98-11 state diagrams relate to existing Clause 98 text. The concept is there, implementation looks like it needs work. An overview presentation would be helpful necessary.
16		
17	CLAUSE 104 TBDs	
18	CL 104.6.2 (P69 L43)	146.8.xxx is listed as fault tolerance reference. Needs reference and expansion to include 10BASE-T1S.
19	Table 104-8 (P73 L12)	TBD next to value of 72 in table for max bus capacitance. - check and remove TBD.
20		
21	CLAUSE 146 - TBDs	
22	CL 146.1.2.x (P79 L13)	Editor's note says content of 146.1.2.1 - 146.2 was added to D1.1 without review, needs to be checked & editors note deleted.
23	CL 146.2 - P81 L10	CL 146.2 needs content on descriptions of service primitives
24	CL 146.5.4.4 (P107 L4)	Editor's note says PSD mask and power level values are TBD - need review and removal of note.
25	CL 146.5.5.3 - P109 L3	Editor's note to be removed prior to D 2.0 appears to have been resolved.
26	CL 146.5.5.3 - P109 L34	Editor's note issues need to be closed
27	CL 146.5.6 - P109 L50	Delete editor's note with a suggestion
28	CL 146.6.1 - NEEDS CONTENT P110 L47	Editor's note - need to define autoneg parameters and force mode options
29	CL 146.7.1.5 (P114 L31, L37, L38, L47)	TBD inserted after IEC document reference. Then all coupling attenuation values are TBD. Also editors note acknowledging TBD's for coupling attenuation; followed by table 146-6 in which the TBD's are listed
30	Table 146-7 (P115 L6)	TBD's in "radiated RF- AM" slots of the table.
31	CL 146.8 - 146.10 (P116 L23)	Editor's note has served its purpose - text is now under review and was commented on in D1.2 - delete or identify specific needs of attention
32	CL 146.8.1 (P116 L43, L44)	3 TBD's entered related to connector selection for 'applications with lower environmental requirements', including pin specifications of connections.
33	146.8.3 (P117 L7 through P117 L22)	MDI return loss proposals needed per editor's note, and note on line 19 saying values are 'preliminary'...
34	CL146.9.1	Editor's note specified entire section as TBD and subject to work in the isolation and ad hoc. - Need to check with Jon Lewis and update
35	CL 146 (P77 L23, P78 L41, P79 L5, P85 L36, general) - NEEDS CONTENT	EEE specification (LPI mode description, wake signal?, refresh signal?, timers)
36		
37	CLAUSE 147 - TBDs	
38	CL 147.1 (P129 L25)	Editor's note instructing task force to define EEE operation per objectives. - More likely - EEE is inherent in the objectives as half-duplex operation. We need proposals to include text regarding how the PHY responds to LPI from the MAC.
39	CL 147.1.2 (P129 L45, L46, L53)	3 TBD's inserted for description of 10BASE-T1S PHY operations.
40	CL 147.3.5 (P.142 L21)	Timeout for detecting collision needs to be added according to Editor's note.
41	Cl 147.4 (P143 L33)	The PMA Figure (like 146-12) needs to be developed according to Editor's note.
42	CL 147.4.2 (P143 L3, L7)	The 10BASE-T1S PMA Transmit function signal flow Figure needs to be developed according to Editor's note, and Figure is not yet in the draft (TBD on line 29 would be the xref). Figure should be like 146-13 - need proposalsThe PMA transmit Figure needs to be developed according to Editor's note. Also instructions to improve clarity (i.e its relationship to the PCS state diagram, and whether it belongs in the PMA or the PCS).
43	Table 147-2 (P144 L44, L45)	4 TBD's inserted in table for DME Timings.
44	Cl 147.4.3 (P145 L27-29)	The 10BASE-T1S PMA Receive function signal flow Figure needs to be developed according to Editor's note, and Figure is not yet in the draft (TBD on line 29 would be the xref). Figure should be like 146-14 - need proposals

	A	B
45	CL 147.5 (P145 L54)	Copy or reference clauses 146.5.1.1 and 146.5.1.2 here as they apply as well for 10BASE-T1S according to Editor's note.
46	CL 147.5.1 (P146 L22)	Editor's note - Need to determine how to generate sequence below this clause. - This appears to be completed in the text. Is it? If so, propose deleting editor's note, if not, propose what else is needed.
47	CL 147.5.2.1 (P147 L4)	Editor's note: Are additional requirements and test fixtures needed to test the transmitter tolerance to expected line impedance variations?
48	CL 147.5.3.1 (P147 L18)	Transmitter output voltage to be aligned with PSD mask specifications. Nominal voltage should be 1V, to be discussed in the group; according to Editor's note.
49	Figure 147-10 (P146 L52)	TBD inserted for PSD mask in Transmitter test fixture (where figure is not included).
50	CL 147.5.3.1 (P147 L21)	3 TBD's inserted; 1 for unfinished reference in figure-147-TBD, and 2 for peak to peak differential on transmitter output voltage.
51	CL 147.5.3.2 (P147 L36)	Unfinished reference to figure 147-TBD inserted again
52	Figure 147-11 (P147 L46)	The 10BASE-T1S transmitter only has a single transmitter level. The control bit text either needs to be deleted or a description of select-able transmit levels needs to be added to clause 147 (according to Editor's note).
53	Figure 147-11 (P148 L5)	Copy or reference clause 146.5.4.2 here as it applies as well for 10BASE-T1S (according to Editor's note).
54	CL 147.5.3.4 (P149 L25) - NEEDS CONTENT	Need content for Receiver electrical specs, including receiver alien crosstalk tolerance. Also need to determine if there is a PMA local loopback function. See Editor's note instructing task force discussion of: 1. Copy or reference clause 146.5.x.x Alien Crosstalk Noise Injection here as it applies as well for 10BASE-T1S. 2. Copy or reference clause 146.5.7 PMA local loopback here as it applies as well for 10BASE-T1S. 3. Copy or reference clause 146.5.5 Receiver electrical specifications here as it applies as well for 10BASE-T1S.
55	CL 147.5.3.4 (P148 L44)	TBD inserted for ppm in range of symbol transmission rate.
56	CL 147.8 (P151 L47)	Review initial mixing segment specifications (based on the point to point link segment requirements) as well as terminations and PHY requirements, especially MDI return loss when transmitting and other MDIs in high impedance state. (according to Editor's note)
57	CL 147.9.2.1 (P151 L30)	TBD inserted for frequency range in MHz of MDI Return Loss
58	CL 147.10 (P153 L3) - NEEDS CONTENT	General, environmental and network safety is all blank...
59	CL 147.11 (P153 L16) - NEEDS CONTENT	Need to determine delay constraints. This matters for full duplex and for multidrop timing delay as it will determine the maximum segment length
60	CL 147.12.3 (P155 L1) - PICS	Need PICS for 10BASE-T1S
61		
62	Clause 148 TBD's	
63	CL 148.2 (157 L21) - NEEDS CONTENT	According to Editor's note: high level description of the operation and specification of PLCA is needed here (description only, no Requirements).
64	CL 148.3 (P155 L28) - NEEDS CONTENT	Content for "Relationship with Other IEEE Standards" specified as TBD in Editor's note.
65	CL 148.4.4.2.4 (P163 L4)	Editor's note requesting commentators review description in text and state diagram of early receive indication and its behavior to valid correctness and conciseness of both the diagram and the text.
66	CL 148.4.5.1 (P161 L20)	TBD inserted for unfinished reference to PLCA Control State Diagram: "Table 148-TBD".
67	CL 148.4.5.2 (P165 L4, L9, L45)	3 TBD's inserted: 1 for register on plca_reset, 1 for register on plca_en, and 1 for register on MAX_ID. (PLCA parameters marked for negotiation in editor's note).
68	CL 148.4.5.2 (P167 L39) - NEEDS CONTENT	Specify whether and how PLCA parameters may be negotiated (e.g., Clause 98). Specified in two Editor's notes.
69	CL 148.4.5.4 (P166 L30)	TBD inserted for register on TO_TIMER
70	CL 148.4.6.1 (P166 L51)	TBD inserted for PLCA Data State Diagram as figure 148-TBD.
71	Bibliography (P177 L10)	Any new informative references to be added here. (currently blank)
72	CL 200A.1.1.2.1 (P185 L50)	Task Force has not yet agreed on powered trunk DCR characteristics (According to Editor's note).
73	CL 200A.1.1.2.2 (P186 L1)	Task Force has not yet agreed on powered trunk class power requirements (According to Editor's note)
74	CL 200A.1.10 (188 L1) - NEEDS CONTENT	All tables on this page are blank; needs content.