TUESDAY

As announced, the main discussion on Shawn Nicholl's extended sub-layer proposal is happening around 9am in .3cn.

Those particularly interested in this topic are encouraged to attend the .3cn Tuesday morning

Time	Presenter	Affiliation	Title	File Name	Duration
1:00 PM	Beth Kochuparambil	Cisco	Agenda	agenda_3ck_01_0119	0:35
1:35 PM	Beth/Mike L/Mike D		OIF Liason Review		0:20
1:55 PM	Liav BenArtsi	Marvell	Package Discussions Update & Suggested PKG Model Base-Line for 802.3ck	benartsi_3ck_01_0119	0:20
2:15 PM	Rob Stone	Broadcom	Switch Package Physical Design Considerations	stone_3ck_01_0119	0:25
2:40 PM	Howard Heck	Intel	Assessment of Proposed Reference Package Model	heck_3ck_01_0119	0:25
3:05 PM	BREAK				
3:25 PM	Mau-Lin Wu	MediaTek	COM Sensitivity Analysis of PKG Model	wu_3ck_01_0119	0:30
	Discussion				
tentative	Kent Lusted	Intel	Nomenclature	lusted_3ck_01_0119	0:25
	Rich Mellitz	Samtec	COM Updates to Support C2M Investigations	mellitz_3ck_01_0119	0:20
6:00 PM	BREAK FOR THE DAY (NEA is in our room at 7pm)				

Howard Heck

Nathan Tracy

Phil Sun

Phil Sun

Ali Ghiasi

Yan Zhuang

Mike Peng Li

Beth Kochuparambil Cisco

Intel

Credo

Intel

Credo

Huawei

ΤE

1:15 PM

1:30 PM

1:50 PM

2:10 PM

2:50 PM

3:30 PM

4:10 PM

tentative

6:00 PM

WEDNESDAY					
Time	Presenter	Affiliation	Title	File Name	Duration
8:00 AM	Beth Kochuparambil	Cisco	Plan for the day		0:05
8:05 AM	Kent Lusted	Intel	C2M Update - January 2019	lusted_3ck_02_0119	0:15
8:20 AM	Phil Sun	Credo	100G C2M Study Results	sun_3ck_01a_0119	0:45
9:05 AM	Pete Anslow	Ciena	RS(544,514) FEC performance for C2M 400G and 100G without interleaving	anslow_3ck_01_0119	0:30
9:35 AM	Toshiyasu Ito	Yamaichi	100GEL C2M Channel Model Study Update	ito_3ck_01_0119	0:20
9:55 AM			BREAK		0:15
10:10 AM	Ali Ghiasi	Ghiasi Quantum	C2M Simulations and Methodology	ghiasi_3ck_01_0119	0:30
10:40 AM	Steve Sekel	Keysight	Suggested change in Module Input and Host Input tests stressed eye c	sekel_3ck_01_0119	0:30
	Discussion				
12:00 PM	BREAK				

Channel Simulations for 112G Backplane Analysis

KR/CR Simulation Results with COM Tool 2.57

What is Important for a Reference Receiver

Summary of System Discussion of Backplane Channels

Discussion

BREAK FOR THE DAY - (802.3cg) PLCA Tutorial in our room at 7pm

Summary of IEEE 802.3ck Baseline Reference Receivers and Clarification of [zhuang_3ck_01_0119]

heck_3ck_02_0119

tracy 3ck 01 0119

sun 3ck 02 0119

li 3ck 01 0119

sun_3ck_03_0119

ghiasi_3ck_02_0119

kochuparambil_3ck_01

0:15

0:20

0:20

0:40

0:40

0:40

0:40

0:15

1:15

Host Backplane Channel Update

112Gbps LR COM Investigation (III)

Ghiasi Quantum How to Proceed on C2C-S and C2C-L

THURSDAY

Time	Presenter	Affiliation	Title	File Name	Duration	
8:30 AM	Beth Kochuparambil	Cisco	Plan for the day		0:05	
8:35 AM	Mark Gustlin	Cisco	PCS, FEC and PMA Sublayer Baseline Proposal	gustlin_3ck_01_0119	0:25	
9:00 AM	Xiang He	Huawei	Further study on RS(544,514) FEC - symbol interleaving and bit muxing	he_3ck_01_0119	0:30	
9:30 AM	BREAK					
9:45 AM	Ilya Lyubomirsky	Inphi	(late) Latency Concerns on Interleaved FEC for KR/CR	lyubomirsky_3ck_01_0	0:30	
10:15 AM	Shawn Nicholl	Xillinx	Baseline for CGMII Extender, CGMII Extender Sublayer (100GXS)	nicholl_3ck_01_0119	0:25	
	Remaining Presentations, Discussion, & Straw Polls					
12:00 PM	BREAK					
	Discussion & Straw Polls					
3:00 PM	BREAK					
	Discussion & Straw Polls					
6:00 PM	BREAK FOR THE DAY - 802.3 Working Group meeting starts in our room at 6:30pm					