## Presentations – Wednesday

Topic	Time	Author(s)	Affiliation(s)	Title	Duration
	9:00:00 AM	Kent Lusted	Intel	Agenda	0:45
	9:45:00 AM	Matt Brown	Independent	Editors report	0:10:00
KR	9:55:00 AM	Howard Heck	Intel	Backplane Reference Rx Tap Weight Data	0:30:00
	10:25:00 AM	Break			0:20:00
KR	10:45:00 AM	Mike Peng Li	Intel	ERL Investigations for Ethernet 106G Backplane (II)	0:20:00
KR	11:05:00 AM	Rich Mellitz	Samtec	ERL KR Baseline Proposal	0:20:00
	11:25:00 AM	Discussions & Straw P	olls		0:40
	12:05:00 PM	Lunch			1:15:00
C2C	1:20:00 PM	Rick Rabinovich	Keysight	Examples of C2C Channels with Impairments 10dB 16dB 18dB 20dB Test Cases	0:30:00
C2C	1:50:00 PM	Mike Peng Li	Intel	106Gbps C2C COM Investigation (II)	0:25:00
C2C	2:15:00 PM	Ali Ghiasi	Ghiasi Quantum	C2C COM Simulation	0:20:00
C2C	2:35:00 PM	Mike Peng Li	Intel	Baseline Proposal for "100 Gb/s, 200 Gb/s, and 400 Gb/s Chip-to-Chip Attachment Ur	0:30
	3:05:00 PM	Break			0:20:00
	3:25:00 PM	Discussions & Straw Polls			0:30
FEC	3:55:00 PM	mark Gustlin	Cisco	100GBASE-KR1/CR1 FEC Thoughts	0:30:00
FEC	4:25:00 PM	Ilya Lyubomirsky	Inphi	FEC Latency and Power/Area Tradeoffs for 100G KR/CR	0:20:00
FEC	4:45:00 PM	Yan Zhuang	Huawei	Summary of Error Propagation for 100GBASE-KR1/CR1	0:15:00
FEC	5:00:00 PM	Yan Zhuang	Huawei	Auto-Negotiation for Dual mode FEC	0:25:00
	5:25:00 PM	Discussions & Straw Polls			0:35
	6:00:00 PM	Break for the Day			0:0

Note –Times listed are subject to change.

## Presentations – Thursday

Topic	Time	Author(s)	Affiliation(s)	Title	Duration
	9:00:00 AM	Kent Lusted	Intel	Agenda	0:05
C2M	9:05:00 AM	Mark Kimber	Semtech	Module on-die termination model	0:25:00
C2M	9:30:00 AM	Phil Sun	Credo	C2M TP1a Criteria Considering Both Long and Short Host Traces	0:30:00
	10:00:00 AM	Break			0:20:00
C2M	10:20:00 AM	Ali Ghiasi	Ghiasi Quantum	C2M COM Analysis on short and long channels	0:20:00
C2M	10:40:00 AM	Mike Dudek	Marvell	Comparison of C2M performance at TP1a with whole channel performance	0:30:00
C2M	11:10:00 AM	Mau-Lin Wu	Media Tek	Host to Module Short Channel Issue and Possible Solutions	0:30:00
	11:40:00 AM	Lunch			1:15:00
	12:55:00 PM	Discussions & Straw Polls			0:30
CR	1:25:00 PM	Liav Ben-Artsi	Marvell	Representing imperfections for CR Host Board	0:25:00
CR	1:50:00 PM	Rich Mellitz	Rich Mellitz Samtec CR Vmin and Vf Decisions		0:25:00
CR	2:15:00 PM	Rich Mellitz	Samtec	CA COM parameters SNR_TX and ETA_0 Baseline Proposal	0:25:00
CR	2:40:00 PM	Jane Lim	Cisco	100G CR End-to-End Channel Analysis Update (II)	0:25:00
	3:05:00 PM	Break			0:30:00
	3:35:00 PM	Discussions & Straw Polls			1:00:00
	4:35:00 PM	Break for the Day			

Note –Times listed are subject to change.

IEEE P802.3ck 100 Gb/s, 200 Gb/s, and 400 Gb/s Electrical Interfaces Task Force – September 2019 Interim meeting

## **Presentations - Friday**

Торіс	Time	Author(s)	Affiliatio	on(: Title	Duration
	9:00:00 AM	Kent Lusted	Intel	Agenda	0:05
	9:05:00 AM	Discussions & Stra	aw Polls		1:00:00
	10:05:00 AM	Break			0:20:00
	10:25:00 AM	Discussions & Stra	aw Polls, Future	1:30:00	
	11:55:00 AM	Close of Meeting			1:15:00

Note –Times listed are subject to change.

Version 3.6

IEEE P802.3ck 100 Gb/s, 200 Gb/s, and 400 Gb/s Electrical Interfaces Task Force – September 2019 Interim meeting