## Approved Responses

IEEE P802.3cy D2.0 10G+ Auto Task Force Initial Working Group ballot comments

					5				
C/ 1 SC 105.3	P 37	L <b>25</b>	# 369	C/ 165	SC 165.3.2.2	P 56	L <b>41</b>	# 341	
Grow, Robert RMG Consluting				Gorshe, Steve Microchip Technology					
Comment Type <b>TR</b> If 25GBASE-T1 deserves those sublayers in the reli- no on advancement to W seen these titles with no a SuggestedRemedy The technical experts in the text for the 25GBASE-T1 relevant to the 25GBASE- remaining subclauses.	Comment Status A its own protocol stack in evant 105.3.x subclauses. G ballot as the draft is not associated changes as ar he TF are much better qua protocol stack relevant se	Figure 105-1, the . I missed this ar t technically comp n indication of inc alified than I am tections. Delete th	nd should have voted olete. I should have completeness. to provide the missing ne subclause titles not	Comment Here a termin is actu Suggestee In all i be cha Response REJE The p	Type ER and other places, ology is incorrect ially an FEC code <i>IRemedy</i> instances where t anged to FEC "cc CT.	Comment Status R the term "9360-bit (936, 846 t or at least inconsistent with eword. An FEC frame consi	6) RS-FEC frame o typical terminolo sts of multiple FE I to as an FEC "fr	ogy. The 9360-bit entity EC codewords. rame" the term should	
Remove 105.3.1 through	105.3.5 P <b>42</b>	L 35	# 340						
Gorshe, Steve	<b>۲ 42</b> Microchip Te		# 340						
Comment Type ER The phrase "RS-FEC (930 that the FEC symbol size 846, 10)" using the 10-bit	is 2^10 = 1024 bits. It ap	pears that it shou	uld be "RS-FEC (936,						
SuggestedRemedy If the comment is correct,	, this should be changed to	o RS-FEC (936,	846, 10)						
Response ACCEPT IN PRINCIPLE.	Response Status W								
===== replace "(936,846,2^10)" v	with "(936,846) over GF(2	2^10)"							

C/ 165 SC 165.3.2.2