

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 00 SC 0 P3 L6 # 38

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A Editorial

Some additional keywords might be helpful.

SuggestedRemedy

Insert the following into the Keywords list in alphabetical order: grounded systems, grounded MPoE, isolated MPoE, isolated systems, Isolation, MPI, Power Interface, Power Sourcing Equipment, Powered Device, TCI, Trunk Connection Interface, TPS, Transmit Power Signature

Response Response Status C

ACCEPT IN PRINCIPLE.

(Editor's note: TCI is already there. Suggest that MPoE stands in for both grounded and isolated MPoE. We can drop TPS and we already have MPSE and MPD, which is what we use, not Powered Device or Power Sourcing Equipment by themselves. We don't use "Power Interface" by itself.

Keywords are the recommendation of the Working Group chair, so, suggest - add MPI – which is new – to the keywords.)

CI 00 SC 0 P8 L18 # 116

Law, David Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A Late

The list of members of the IEEE 802.3 Working Group is ordered incorrectly.

SuggestedRemedy

Update the list of names in the IEEE 802.3 Working Group member list so they are ordered by <given name> <family name> sorted by <family name>.

Response Response Status C

ACCEPT.

CI 00 SC 0 P10 L1 # 31

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A EZ

IEEE-SA Standards Board member names to be supplied at publication

SuggestedRemedy

Replace, "Alpesh Shah, Secretary"

with, "FirstName SecondName, Secretary"

Response Response Status C

ACCEPT.

CI 00 SC 0 P13 L51 # 30

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A EZ

Reference to 802.3dn./Cor 1 missing from Front Matter

SuggestedRemedy

Insert after the Amendment 9 reference:

"IEEE Std 802.3<TM>-2022/Cor 1-2024

Corrigendum 1—This corrigendum includes changes to IEEE Std 802.3-2022 to correct the MDI return loss specifications in Clause 149 and Clause 165."

Note: <TM> = TM in superscript

Response Response Status C

ACCEPT.

CI 1 SC 1.4 P24 L6 # 32

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A Editorial

Mixed use of "as follows" and "as shown" In Editing Instructions. Some editing instructions (e.g., P54, L19) are unnecessarily detailed.

SuggestedRemedy

Grant license for Editor to globally replace "as follows" with "as shown" for change instructions; replace "as shown" with "as follows" for insert instructions; remove detail from overly detailed instructions, and harmonize Editing Instructions across the entire document.

Response Response Status C

ACCEPT.

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 30 SC 30.2.2.1 P26 L14 # 106

Brandt, David Rockwell Automation
Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy
Change "instance" to "instances".

Response Response Status C
ACCEPT IN PRINCIPLE.

Accomodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

CI 30 SC 30.2.2.1 P26 L17 # 107

Brandt, David Rockwell Automation
Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy
Change "instance" to "instances".

Response Response Status C
ACCEPT IN PRINCIPLE.

Accomodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 30	SC 30.2.3	P27	L37	# 103
Brandt, David		Rockwell Automation		
Comment Type	T	Comment Status	A	Power-ODVA
Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.				
SuggestedRemedy				
Change arrows from oPHYEntity to oMPSE and oMPD to double headed end arrows denoting one-to-many relationships.				
Response		Response Status	C	
ACCEPT IN PRINCIPLE.				
Accomodated by comment 100: ACCEPT IN PRINCIPLE. Implement changes in https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.				

CI 30	SC 30.2.5	P28	L6	# 34
Maguire, Valerie		Copperopolis; aff'l w/ CME Consulting and Cisco		
Comment Type	E	Comment Status	R	Power-ODVA
Clunky and excessively wordy language. There's also no PICS for this item, so removing the "shall" is probably in order. PSE should be MPSE at the end of line 6.				
SuggestedRemedy				
Replace, " For managed MPSEs, the MPSE Basic Package is mandatory and the MPSE Recommended Package is optional. For a managed MPSEs to be conformant to this standard, it shall fully implement the PSE Basic Package." with, "Full implementation of the MPSE Basic Package is required for managed MPSEs. Implementation of the MPSE Recommended Package is optional.				
Replace< "For managed MPDs, the MPDs Basic Package is mandatory and the MPD Recommended Package is optional. For a managed MPD to be conformant to this standard, it shall fully implement the MPD Basic Package." with "Full implementation of the MPD Basic Package is required for managed MPDs. Implementation of the MPD Recommended Package is optional."				
Response		Response Status	Z	
REJECT.				
This comment was WITHDRAWN by the commenter.				

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 30	SC 30.2.5	P29	L20	# 108
Brandt, David		Rockwell Automation		
Comment Type	T	Comment Status	A	Power-ODVA
<p>Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.</p>				
<i>SuggestedRemedy</i>				
Insert at top of table "aMPSEID", ATTRIBUTE", "GET", "X" under optional.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accomodated by comment 100: ACCEPT IN PRINCIPLE. Implement changes in https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.				

CI 30	SC 30.2.5	P30	L20	# 109
Brandt, David		Rockwell Automation		
Comment Type	T	Comment Status	A	Power-ODVA
<p>Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.</p>				
<i>SuggestedRemedy</i>				
Insert at top of table "aMPDID", ATTRIBUTE", "GET", "X" under optional.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accomodated by comment 100: ACCEPT IN PRINCIPLE. Implement changes in https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.				

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 30 SC 30.17.1.1 P33 L49 # 104

Brandt, David Rockwell Automation

Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs.

Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy

Insert subclause and renumber:

30.17.1.1.1 aMPSEID

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

A read-only value that identifies a specific MPSE interface that is associated with an oPHYEntity.

0 = co-resident data and power interface

1-3 = separate power interfaces;

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 100:

ACCEPT IN PRINCIPLE.

Implement changes in

https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

CI 30 SC 30.17.1.1.8 P35 L33 # 35

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco

Comment Type E Comment Status A EZ

MDI should be MPI

SuggestedRemedy

Replace, "MPSE as measured at the MDI in"

with, "MPSE as measured at the MPI in"

Response Response Status C

ACCEPT.

CI 30 SC 30.17.1.1.9 P35 L35 # 6

Zimmerman, George ADI, APLgp, Cisco, Marvell, OnSemi, Sony, SenTekse

Comment Type E Comment Status A Management

aMPSECapabilities seems insufficiently named. It is not all the capabilities of the MPSE, but rather the Measurement capabilities.

SuggestedRemedy

Change "aMPSECapabilities" globally to "aMPSEMeasurementCapabilities".

On P35 L50, Change "This indicates the capabilities of the MPSE" to "This indicates the measurement capabilities of the MPSE"

Response Response Status C

ACCEPT.

CI 30 SC 30.17.2.1 P38 L40 # 105

Brandt, David Rockwell Automation

Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs.

Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy

Insert subclause and renumber:

30.17.2.1.1 aMPDID

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

A read-only value that identifies a specific MPD interface that is associated with an oPHYEntity.

0 = co-resident data and power interface

1-3 = separate power interfaces;

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 100:

ACCEPT IN PRINCIPLE.

Implement changes in

https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Clause, Subclause, page, line

CI 30

SC 30.17.2.1

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CI 30 SC 30.17.2.1.3 P39 L23 # 49
 Potterf, Jason Cisco
 Comment Type E Comment Status A EZ
 The idle state has been renamed in 189.5.3.5, rename here for consistency.
 SuggestedRemedy
 change "idle" to "out-of-range" with editorial license to adjust the word separation character for consistency.
 Response Response Status C
 ACCEPT.

CI 39 SC 39.17.1.1.8 P35 L33 # 3
 Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
 Comment Type T Comment Status A EZ
 MPoE measurements are at the MPI, not at the MDI.
 SuggestedRemedy
 Change "measured at the MDI" to "measured at the MPI"
 Response Response Status C
 ACCEPT IN PRINCIPLE.
 Accommodated by comment #35.
 Replace, "MPSE as measured at the MDI in"
 with, "MPSE as measured at the MPI in"

CI 79 SC 79.1 P53 L5 # 111
 Regev, Alon Keysight
 Comment Type T Comment Status R TLV
 The data in the 802da_D2p2.pdf does not match the data shown in 802da_D2p2_CMP.pdf. Specifically, section 79.1 in 802da_D2p2.pdf is shown as both section 79.1 (showing deletions) and section 79.2 (showing insertions) in 802da_D2p2_CMP.pdf. After this, all the section numbers in clause 79 in 802da_D2p2_CMP.pdf. are off by 1 from 802da_D2p2.pdf (so 79.2 is shown as 79.3, 79.3 as 79.4...)
 SuggestedRemedy
 Consider treating all of section 79 as in-scope for the next ballot as the review materials prepared for section 79 may allow errors to be introduced.
 Response Response Status C
 REJECT.
 No change to the draft is requested. There are expected to be changes in clause 79.

CI 79 SC 79.3 P54 L12 # 99
 Brandt, David Rockwell Automation
 Comment Type E Comment Status A EZ
 Wrong reference.
 SuggestedRemedy
 Change MPSE Subclause reference to "79.3.10".
 Response Response Status C
 ACCEPT.

CI 79 SC 79.3.9 P54 L30 # 61
 Brandt, David Rockwell Automation
 Comment Type T Comment Status A TLV-figure
 TLV header string length is incorrect.
 SuggestedRemedy
 Change TLV information string length from "9" to "8".
 Response Response Status C
 ACCEPT IN PRINCIPLE.
 Accommodated by comment #60.
 (Editor's note: The definitive specification is in the text, the figure creates opportunity for error, and is unnecessary.)
 Delete Figure 79-10.

CI 79 SC 79.3.9 P54 L30 # 60
 Brandt, David Rockwell Automation
 Comment Type T Comment Status A TLV-figure
 TLV information string is missing last (new) octet.
 SuggestedRemedy
 Add "PLCA nodeCount" octet to end of TLV information string.
 Response Response Status C
 ACCEPT IN PRINCIPLE.
 (Editor's note: The definitive specification is in the text, the figure creates opportunity for error, and is unnecessary.)
 Delete Figure 79-10.

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CI 79 SC 79.3.9.1 P55 L31 # 62

Brandt, David Rockwell Automation

Comment Type E Comment Status R TLV-figure

Why do we not use aPLCANodeCount which maps to plca_node_count?

SuggestedRemedy

Change note to "30.16.1.1.3"

Response Response Status C

REJECT.

Since clause 30 management is optional, reference to state diagram variables, which are mandatory, is preferred.

CI 79 SC 79.3.10 P56 L2 # 63

Brandt, David Rockwell Automation

Comment Type E Comment Status A TLV-figure

TLV description does not contain a graphical TLV format as seen for PLCA Figure 79-10.

SuggestedRemedy

Add in the graphical TLV format.

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment #60.

(Editor's note: The definitive specification is in the text, the figure creates opportunity for error, and is unnecessary.)

Delete Figure 79-10.

CI 79 SC 79.3.10 P56 L5 # 90

Brandt, David Rockwell Automation

Comment Type E Comment Status A TLV

The "element tables" do not have text to state whether all the referenced tables are required. It seems necessary to then have a fixed length as there is no length indicator.

SuggestedRemedy

Insert paragraph: "The MPoE MPSE Status TLV is composed of a single instance of each of the multiple required elements as indicated in Table 79-22b references to Tables 79-22c through 79-22h. The Reserved Field is necessary to achieve 16-bit alignment."

Response Response Status C

ACCEPT.

CI 79 SC 79.3.10 P56 L6 # 66

Brandt, David Rockwell Automation

Comment Type E Comment Status A TLV-format

Table 79-22b format should match Table 79-22a

SuggestedRemedy

Replace "Field size (bit)" with "Length (Octets)". List values in octets.

Response Response Status C

ACCEPT IN PRINCIPLE.

(Editor's note: The standard format in 79.3 for these tables is "Bit | Function | Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)

Reformat Tables 79-22a and 79-22u to columns "Bit | Function | Value/Meaning | Reference" with editorial license.

Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).

Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).

In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.

In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."

In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."

In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."

In Table 79-22r, change "Value/Meaning" inserting "in mV" to read: "The instantaneous voltage in mV observed..."

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Cl 79	SC 79.3.10	P56	L24	# 67
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Table 79-22c format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and gang rows as "Bitmap".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.10	P56	L24	# 73
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Tables 79-22c to 22h lack management references.				
SuggestedRemedy				
Add "Note" column. Add references with entry "30.17.1.1.x" if they can be mapped.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 79 SC 79.3.10 P56 L29 # 110

Regev, Alon Keysight
Comment Type T Comment Status A TLV-format

Field width in table 79-22c is misleading as there is a width on one row that covers all the rows, and all the other rows have a dash through the field. Also, it is not clear why the table 79-22B calls a similar column "Field size (bits)" and table 79-22c calls the column "Field width" and how "width" differs from "size" and why the units (bits) are not specified. this also needs to be fixed in tables 79-22d, 79.22e, 79.22j, 79-22k, and 79-22l

SuggestedRemedy

Either get rid of the field width column (as it's not needed in this table) or correct it to indicate the units (bits) and indicate the width for the specific row (i.e. 1 for the "Active" function row, "1" for the "Withdrawing power notification" function row, and 14 for the "Reserverd" function row.

Also fix in a similar fashin in tables 79-22d, 79.22e, 79.22j, 79-22k, and 79-22l

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment #66.

(Editor's note: The standard format in 79.3 for these tables is "Bit | Function | Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)

Reformat Tables 79-22a and 79-22u to columns "Bit | Function | Value/Meaning | Reference" with editorial license.

Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).

Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).

In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.

In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."

In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."

In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."

In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."

CI 79 SC 79.3.10 P56 L33 # 100

Brandt, David Rockwell Automation
Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy

Change Function Reserved Bit value "15:2" to "13:2". Add a row below for Function "Interface" and Bit value "15:14" with Value/Meaning of "00 = co-resident data and power ID", "01-11 = separate power ID".

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

Cl 79	SC 79.3.10	P56	L39	# 68
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22d format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and gang rows as "Bitmap".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.10	P57	L3	# 69
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Table 79-22e format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and gang rows as "Bitmap".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read: "The instantaneous voltage in mV observed..."				

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Cl 79	SC 79.3.10	P57	L19	# 70
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22f format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.10	P57	L29	# 71
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Table 79-22g format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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Cl 79	SC 79.3.10	P57	L38	# 117
Jones, Peter		Cisco Systems		
Comment Type	E	Comment Status	A	Late
Lists of fields/notifications are formatted incorrectly.				
SuggestedRemedy				
Change list items to a lettered list in the following locations:				
P57/L49/79.3.10 MPoE MPSE Status TLV bullet points starting with "Withdrawing power"				
P60/L38/ 79.3.11 MPoE MPD Status TLV bullet points starting with "Temporary power"				
Response		Response Status	C	
ACCEPT.				

Cl 79	SC 79.3.10	P57	L39	# 72
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22h format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response		Response Status	C	
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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CI 79 SC 79.3.11 P58 L2 # 64
Brandt, David Rockwell Automation
Comment Type E Comment Status A TLV-figure
TLV description does not contain a graphical TLV format as seen for PLCA Figure 79-10.
SuggestedRemedy
Add in the graphical TLV format.
Response Response Status C
ACCEPT IN PRINCIPLE.
Accommodated by comment #60.
(Editor's note: The definitive specification is in the text, the figure creates opportunity for error, and is unnecessary.)
Delete Figure 79-10.

CI 79 SC 79.3.11 P58 L5 # 91
Brandt, David Rockwell Automation
Comment Type E Comment Status A TLV
The "element tables" do not have text to state whether all the referenced tables are required. It seems necessary to then have a fixed length as there is no length indicator.
SuggestedRemedy
Insert paragraph: "The MPoE MPD Status TLV is composed of a single instance of each of the multiple required elements as indicated in Table 79-22i references to Tables 79-22j through 79-22s. The Reserved Field is necessary to achieve 16-bit alignment."
Response Response Status C
ACCEPT.

CI 79 SC 79.3.11 P58 L6 # 74
Brandt, David Rockwell Automation
Comment Type E Comment Status A EZ
Table 79-22i heading wrong.
SuggestedRemedy
Replace "MPSE" with "MPD"
Response Response Status C
ACCEPT.

CI 79 SC 79.3.11 P58 L6 # 75
Brandt, David Rockwell Automation
Comment Type E Comment Status A TLV-format
Table 79-22i format should match Table 79-22a
SuggestedRemedy
Replace "Field size (bit)" with "Length (Octets)". List values in octets.
Response Response Status C
ACCEPT IN PRINCIPLE.
Accommodated by comment #66.
(Editor's note: The standard format in 79.3 for these tables is "Bit | Function | Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)
Reformat Tables 79-22a and 79-22u to columns "Bit | Function | Value/Meaning | Reference" with editorial license.
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read: "The instantaneous voltage in mV observed..."

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Cl 79	SC 79.3.11	P58	L30	# 76
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Table 79-22j format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and gang rows as "Bitmap".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.11	P58	L30	# 87
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Tables 79-22j to 22s lack management references.				
SuggestedRemedy				
Add "Note" column. Add references with entry "30.17.2.1.x" if they can be mapped.				
Response		Response Status C		
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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Cl 79 SC 79.3.11 P58 L44 # 101

Brandt, David Rockwell Automation
Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs. Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy

Change Function Reserved Bit value "15:6" to "13:6". Add a row below for Function "Interface" and Bit value "15:14" with Value/Meaning of "00 = co-resident data and power ID", "01-11 = separate power ID".

Response Response Status C
ACCEPT IN PRINCIPLE.

Accomodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

Cl 79 SC 79.3.11 P59 L4 # 77

Brandt, David Rockwell Automation
Comment Type E Comment Status A TLV-format

Table 79-22k format should match Table 79-22a

SuggestedRemedy

Replace header "Field width" with "Format" and gang rows as "Bitmap".

Response Response Status C
ACCEPT IN PRINCIPLE.

Accommodated by comment #66.

(Editor's note: The standard format in 79.3 for these tables is "Bit | Function | Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)

Reformat Tables 79-22a and 79-22u to columns "Bit | Function | Value/Meaning | Reference" with editorial license.

Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).

Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).

In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.

In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."

In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."

In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."

In Table 79-22r, change "Value/Meaning" inserting "in mV" to read: "The instantaneous voltage in mV observed..."

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CI 79	SC 79.3.11	P59	L17	# 78
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22l format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and gang rows as "Bitmap".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

CI 79	SC 79.3.11	P59	L33	# 79
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22m format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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Cl 79	SC 79.3.11	P59	L43	# 80
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22n format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.11	P60	L4	# 81
Brandt, David		Rockwell Automation		
Comment Type E	Comment Status A		TLV-format	
Table 79-22o format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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Cl 79	SC 79.3.11	P60	L9	# 7
Zimmerman, George		ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse		
Comment Type	T	Comment Status	A	TLV
I think there's a typo - the Value/Meaning says "Normal power" in definiing the range, when this is for Temporary power.				
SuggestedRemedy				
Change "Normal power" in Value/Meaning to "Temporary power"				
Response		Response Status	C	
ACCEPT.				

Cl 79	SC 79.3.11	P60	L16	# 82
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22p format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response		Response Status	C	
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

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Cl 79	SC 79.3.11	P60	L28	# 83
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22q format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

Cl 79	SC 79.3.11	P60	L47	# 84
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22r format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				

CI 79	SC 79.3.11	P61	L4	# 85
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV-format
Table 79-22s format should match Table 79-22a				
SuggestedRemedy				
Replace header "Field width" with "Format" and label row as "Unsigned Integer".				
Response		Response Status	C	
ACCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard CCEPT IN PRINCIPLE.				
Accommodated by comment #66.				
(Editor's note: The standard format in 79.3 for these tables is "Bit Function Value/Meaning". Occasionally there is also a final column labeled "Reference" but this isn't used in the power clauses. It is appropriate for the management in 79-22a.)				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" with editorial license.				
Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).				
Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).				
In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.				
In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."				
In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."				
In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."				
In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."				
Reformat Tables 79-22a and 79-22u to columns "Bit Function Value/Meaning Reference" per file zimmerman_3da_01_0525.pdf				

Delete "Field width" and "Units" columns from tables 79-22 c, d, e, f, g, h, j, k, l, m, n, (basically tables 79-22c through , excluding 79-22i).

Replace em-dash in "Bit" column with appropriate bits (e.g., 31:0 for 32 bit field width...).

In tables, 79-22f, 79-22g, 79-22m, and 79-22n, add "in mW" to the end of the "Value/Meaning" text.

In Table 79-22o, change "Value/Meaning", inserting "in mW", to read: "Power in mW the MPD needs..."

In Table 79-22p, change "Value/Meaning" inserting "in seconds" to read: "Duration in seconds of..."

In Table 79-22q, change "Value/Meaning" inserting "in seconds" to read: "Delay in seconds of..."

In Table 79-22r, change "Value/Meaning" inserting "in mV" to read:" The instantaneous voltage in mV observed..."

CI 79	SC 79.3.11	P61	L4	# 86
Brandt, David		Rockwell Automation		
Comment Type	E	Comment Status	A	TLV
Table 79-22s Field width "32" does not match Field size "16" in Table 79-22i.				
SuggestedRemedy				
Make both fields match as 2 octets and unsigned integer.				
Response		Response Status	C	
ACCEPT IN PRINCIPLE.				
Change Table 79-22s to 2 octets (bits 15:0, per comment #66 reformat)				

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CI 79	SC 79.3.12	P61	L15	# 65
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Brandt, David Rockwell Automation

Comment Type E Comment Status A TLV-figure

TLV description does not contain a graphical TLV format as seen for PLCA Figure 79-10.

SuggestedRemedy

Add in the graphical TLV format.

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment #60.

(Editor's note: The definitive specification is in the text, the figure creates opportunity for error, and is unnecessary.)

Delete Figure 79-10.

CI 79	SC 79.3.12	P61	L18	# 92
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Brandt, David Rockwell Automation

Comment Type E Comment Status R TLV

Payload is 18 octets. Header is 2 octets. LLDP header is 6 octets. Ethernet payload is 1500 octets. $1492/18 = 82$ PDs. Not sure this is ever achieved. ODVA reaches 40. PLCA limit to 254. I don't think fragmentation or jumbo frames are an option.

SuggestedRemedy

Add text: "Different MDPs can be reported in separate LLDPDUs."

Response Response Status C

REJECT.

802.1ABdh-2021 added support for LLDP Multiframe Protocol Data Units allowing an LLDP data unit to be spread over multiple ethernet frames.

CI 79	SC 79.3.12	P61	L24	# 88
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Brandt, David Rockwell Automation

Comment Type T Comment Status A TLV

There seems to be no reason to have "number of allocated power entries" in Table 79-22t unless there can be more than one instance of Table 79-22u. I assume values are per "MPD MAC address".

SuggestedRemedy

Add the following paragraph following line 18: "The MPoE Power Allocation TLV is composed of a Table 79.22t fixed element followed by one or more Table 79-22u power entry elements, where the number of power entry elements is indicated by the Entry count in Table 79-22u."

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

CI 79	SC 79.3.12	P61	L24	# 89
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Brandt, David Rockwell Automation

Comment Type E Comment Status A TLV

There is no "Power Allocated Status TLV elements" Table as with MPD and MPSE.

SuggestedRemedy

Add an "elements" table to refer to Table 79.22t and 79.22u.

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

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CI 79 SC 79.3.12 P61 L44 # 102

Brandt, David Rockwell Automation

Comment Type T Comment Status A Power-ODVA

Two sets of power exist in a 10BASE-T1S cable within the ODVA specification, one called Network Power for communication and sensing, and another called Switched Power for actuators. This is important to the normal machine powerup sequence of assuring network operation and sensor state before applying actuator power. It also separates the actuator transients to another pair. It is convenient to have this all in a single cable. Also, IEC 63171-7 describes up to 7 way SPE connectors with a standard 2 way SPE data core. Draft IEC TS 63444 Ed. 2 describes switched and unswitched power on 2 separate pairs.

Management should allow multiple sets of MPoE per DTE.

SuggestedRemedy

Add Function Reserved Bit value "5:0". Add a row below for Function "Interface" and bits "7:6" with Value/Meaning of "00 = co-resident data and power ID", "01-11 = separate power ID".

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 100:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/jones_3da_01d_may_2025.pdf (text changes are detailed on pages 5 through 15) with editorial license, including license to replace and refine figure 189-1a.

CI 79 SC 79.5.13 P63 L13 # 93

Brandt, David Rockwell Automation

Comment Type T Comment Status A TLV

No "PLCA nodeCount field"

SuggestedRemedy

Insert "PLC3", "PLCA nodeCount", "79.3.9.3", "Contains and integer value indicating the PLCA nodeCount", "PL:M", "Yes [] N/A []". Re-number existing PLC3 as PLC4.

Response Response Status C

ACCEPT.

CI 79 SC 79.5.14 P63 L37 # 96

Brandt, David Rockwell Automation

Comment Type T Comment Status A TLV

If we allow an optional element to be removed, then we need to remove the Reserved field.

SuggestedRemedy

Suggest leaving Withdrawing power delay as M, and having MPSE set the value to 0. If there is no value at all, you can't really assume a "last gasp" period to store to Flash or some similar activity. You must assume 0.

Response Response Status C

ACCEPT IN PRINCIPLE.

Change MPSE6, MPD6, MPD7 from :O to :M

CI 79 SC 79.5.15 P64 L18 # 97

Brandt, David Rockwell Automation

Comment Type T Comment Status A TLV

If we allow an optional element to be removed, then we need to remove the Reserved field. MPD6 and MPD7 removal add an additional problem that we don't have type identifiers on the fields and MPD8 may be recognized as MPD6.

SuggestedRemedy

Suggest leaving fields as M, and having MPD never issue and Temporary power notification. Then the fields are don't care.

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment #96.

Change MPSE6, MPD6, MPD7 from :O to :M

CI 79 SC 79.5.16 P64 L36 # 94

Brandt, David Rockwell Automation

Comment Type E Comment Status A EZ

Missing reference.

SuggestedRemedy

Add to end of Value/Comment: "as defined in Table 79-22t"

Response Response Status C

ACCEPT.

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CI 79 SC 79.5.16 P64 L39 # 95
Brandt, David Rockwell Automation
Comment Type E Comment Status A EZ
Missing reference.
SuggestedRemedy
Add to end of Value/Comment: "as defined in Table 79-22u"
Response Response Status C
ACCEPT.

CI 148 SC 148.4.4.6 P71 L52 # 113
Law, David HPE
Comment Type TR Comment Status A EZ
There is a bracket imbalance in the condition for the transition from the NEXT_TX_OPPORTUNITY state to the RESYNC state in Figure 148-4 'PLCA Control state diagram, part b'. There are 8 opening brackets but only 7 closing brackets.
SuggestedRemedy
Suggest that:

... * ((dplca_txop_table_upd + (dplca_aging = OFF) + (!dplca_en))

should read:

... * ((dplca_txop_table_upd + (dplca_aging = OFF)) + (!dplca_en))

based on the same terms in the condition for the transition from the NEXT_TX_OPPORTUNITY state to the WAIT_TO state.
Response Response Status W
ACCEPT.

CI 148 SC 148.4.5.2 P72 L3 # 112
Law, David HPE
Comment Type E Comment Status A D-PLCA
The variable local_nodeID is now used in transition condition from the NORMAL to IDLE states in Figure 148-5 'PLCA Data state diagram, part a'. As a result, it should be added to the subclause 148.4.5.2 'Variables' list.
SuggestedRemedy
[1] Add the variable local_nodeID to the note under the heading '148.4.5.2 Variables'.

[2] Add the following additional entry to subclause 148.4.5.2:

local_nodeID
See 148.4.6.2.
Response Response Status C
ACCEPT.

CI 148 SC 148.4.5.2 P72 L5 # 39
Jones, Chad Cisco Systems, Inc.
Comment Type E Comment Status A EZ
"Change entries for variables COL, CRS, TX TX_EN, and TX_ER as shown:" - edit note missing two characters: "D,"
SuggestedRemedy
Change to: "Change entries for variables COL, CRS, TXD, TX_EN, and TX_ER as shown:"
Response Response Status C
ACCEPT.

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CI 148 SC 148.4.7.1 P75 L22 # 8
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A D-PLCA

The new text stating that soft or hard claims are removed from the claim table if they are older than soft aging cycles doesn't quite align with what the D-PLCA Aging state diagram appears to do in state TXOP_END. Rather than check the age of individual soft claims, as the text states, it appears that TXOP_END clears all soft claims every soft_aging_cycles transmit opportunities, and likewise for hard claims every hard_aging_cycles.

SuggestedRemedy

Replace "Soft claims are removed from the claim table, txop_claim_table, if they are older than soft_aging_cycles. Similarly, stale hard claims are removed every hard_aging_cycles." with ""All soft claims are cleared from the txop_claim_table every soft_aging_cycles. Similarly, only those hard claims detected within the previous hard_aging_cycles transmit opportunities are loaded into the txop_claim_table used by the D-PLCA Control state diagram."

Response Response Status C
ACCEPT IN PRINCIPLE.

Accommodated by Response to Comment 48.
ACCEPT IN PRINCIPLE.

Implement changes summarized in
Baggett_3da_Cmt48_DPLCA_Algorithm_Optimization_v01.pdf
as shown in
Baggett_3da_Cmt48_EditingInstructions_v03.pdf

CI 148 SC 148.4.7.2 P76 L48 # 50
Baggett, Tim Microchip
Comment Type T Comment Status R D-PLCA

The D-PLCA hard_aging_cycles and soft_aging_cycles need reasonable defaults that will allow the algorithm to converge quickly during start up. The current defaults were not selected appropriately. The default for hard_aging_cycles is already known to be too large as it can cause MAC excessive deferral errors if other nodes are transmitting large packets.

SuggestedRemedy

Simulate D-PLCA and determine better default values.

Response Response Status C
REJECT.

Commenter provides insufficient information for a remedy. No change to the draft proposed.

(Note - simulation of D-PLCA parameters and operation in the presence of non-PLCA DTE is needed prior to closure of initial SA ballot to guide descriptive text)

CI 148 SC 148.4.7.3 P78 L25 # 9
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type E Comment Status A Editorial

Duplicate 'shalls' - the state diagram is already called with a shall, which includes a requirement on the PICK_FREE_TXOP function, rendering the 'shall's' in the description of PICK_FREE_TXOP's outputs as duplicates.

SuggestedRemedy

change "a. it shall not return zero..." to
"a. It never returns the value zero..."
change "c. it shall return 255..." to "c. It returns 255..."

Response Response Status C
ACCEPT IN PRINCIPLE.

Change "a. it shall not return zero..."
to "a. it does not return zero..."

Change "c. it shall return 255..."
to "c. it returns 255..."

CI 148 SC 148.4.7.5 P79 L # 114
Law, David HPE
Comment Type E Comment Status A EZ

Suggest expanding the FOLLOWER state box to remove the '!' from the action.

SuggestedRemedy

See comment.

Response Response Status C
ACCEPT.

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CI 148	SC 148.4.7.5	P79	L11	# 46	CI 148	SC 148.4.7.5	P79	L24	# 47
Baggett, Tim					Baggett, Tim				
Microchip					Microchip				
Comment Type	TR	Comment Status	A	D-PLCA	Comment Type	TR	Comment Status	A	D-PLCA
<p>DPLCA is intended to work with nodes statically assigned node IDs. If a node is statically assigned to a node ID greater than 7 then it is possible that the DPLCA coordinator will never expand the node count and therefore the number of transmit opportunities enough to allow for the statically assigned node to gain an transmit opportunity. This occurs because the plca_node_count is initialized to 8, allowing for TOs 0-7. If no node ever claims TO 7, then the DPLCA coordinator will never increase the plca_node_count upwards.</p> <p><i>SuggestedRemedy</i></p> <p>Add new parameter specifying the minimum plca_node_count. Initialize the plca_node_count to this parameter in WAIT_BEACON. When reducing the node count, make sure that the REDUCE_NODE_COUNT state is only entered if plca_node_count is greater than the minimum node count parameter. Add text that the minimum node count parameter is configured high enough to cover the largest statically allocated PLCA node.</p> <p><i>Response</i></p> <p><i>Response Status</i> C</p> <p>ACCEPT IN PRINCIPLE.</p> <p>Implement changes shown on slides 13-16 of Baggett_3da_Cmt46_DPLCA_Inclusion_Of_Static_Nodes_v02.pdf</p>					<p>The 10BASE-T1S and 10BASE-T1M PHYs are defined to loop back BEACONS from the transmit MII path to the receive MII path during collision-free transmission. As discussed in previous meetings, the detection of a self-transmitted BEACON will cause the D-PLCA Control state diagram to incorrectly transition from the COORDINATOR state to LEARNING. To resolve this, a new LOOPBACK state was added which would be entered when a BEACON was transmitted. The LOOPBACK would exit back to the COORDINATOR state once BEACON was no longer received.</p> <p>The current solution is not sufficient to meet timing due to PHY delays. Specifically, the 4000 ns maximum delay of MDI input to RX_ER asserted means that the detection of a BEACON will not occur until after the BEACON has been transmitted.</p> <p><i>SuggestedRemedy</i></p> <p>Add a new variable plca_beacon_tx that is set to TRUE in the PLCA Control state diagram SEND_BEACON state when the BEACON is transmitted. The plca_beacon_tx is will be set set to false once tx_cmd and rx_cmd both transition to FALSE.</p> <p>Update the D-PLCA Control state diagram so that it remains in the LOOPBACK state when a BEACON is transmitted and not returning to COORDINATOR until plca_beacon_tx becomes FALSE.</p> <p>See presentation for editing details.</p> <p><i>Response</i></p> <p><i>Response Status</i> C</p> <p>ACCEPT IN PRINCIPLE.</p> <p>Implement changes shown in Baggett_3da_Cmt47_DPLCA_Block_Own_BEACONS_V01.pdf slides 8 & 9.</p>				

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CI 148	SC 148.4.7.6	P80	L17	# 48	Cl 188	SC 188.1	P83	L14	# 51
Baggett, Tim					Zimmerman, George				
Microchip					ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse				
Comment Type	TR	Comment Status	A	D-PLCA	Comment Type	TR	Comment Status	A	Unsatisfied
Supporting the mixture of non-PLCA and D-PLCA nodes greatly complicates the algorithm adding risk and can significantly impact the time and ability for D-PLCA nodes to converge on their unique transmit opportunity. Additionally, it requires the need to balance the need for adjusting the HARD and SOFT aging parameters which is dependent on traffic patterns. These issues can be eliminated by disallowing non-PLCA nodes on a D-PLCA segment.					Need better explanation of the relationship of the clause 188 phy to the clause 147 phy. Pile on to comment 188 from draft 2.0. Attempt to resolve unsatisfied comment with an alternate resolution.				
<i>SuggestedRemedy</i>					<i>SuggestedRemedy</i>				
Disallow non-PLCA nodes on segments with D-PLCA enabled nodes. A mixture of D-PLCA and legacy statically assigned PLCA nodes will still be supported. Remove all references to SOFT claims.					Change Title of clause 188 to Physical Coding Sublayer (PCS), Physical Medium Attachment (PMA) sublayer, Physical Medium Dependent (PMD) sublayer, and baseband medium, type 10BASE-T1M				
See associated presentation.					Change first paragraph of 188.1 to read: "This clause defines the type 10BASE-T1M Physical Coding Sublayer (PCS), type 10BASE-T1M				
					Physical Medium Attachment (PMA) sublayer, and the 10BASE-T1M Physical Medium Dependent (PMD) sublayer. Together, the PCS, PMA, and PMD sublayers comprise a 10BASE-T1M Physical Layer device (PHY).				
<i>Response</i>					Insert new second paragraph:				
<i>Response Status</i> C					Functional and electrical specifications for the type 10BASE-T1M PCS, PMA, PMD, and the interface to the medium, referred to as the Trunk Connection Interface (TCI) are provided in this clause. The Clause 188 10BASE-T1M PCS and 10BASE-T1M PMA are functionally identical to the Clause 147 10BASE-T1S PCS and 10BASE-T1S PMA with the exception that Clause 188 only supports multidrop operation and does not support the functionality required for clause 147 in point-to-point half-duplex or point-to-point full-duplex modes. The specification includes many enhancements for clarity enabled by the focus functioning only in multidrop mode. The 10BASE-T1M PMD is a refinement of the electrical specifications of the electrical specifications of the 10BASE-T1S PMA found in 147.5, to improve plug-and-play performance, improve multidrop interoperability, and support specification of the enhanced mixing segment in 188.8.				
ACCEPT IN PRINCIPLE.					Change Figure 188-1 to add a PMA below the PMA.				
Implement changes summarized in					Change 188.6 title to PMD electrical specifications				
Baggett_3da_Cmt48_DPLCA_Algorithm_Optimization_v01.pdf					Replace first sentence with: "The 10BASE-T1M PMD converts between the logical DME signals specified in the PMA and the electrical signals on the mixing segment in both transmit and receive directions. It interfaces the PMA to the TCI, and this subclause defines the electrical characteristics of the PMD for a 10BASE-T1M PHY."				
as shown in					In 188.6.2.1 (P104 L7) Change "PMA's receiver" to "PHY's receiver"				
Baggett_3da_Cmt48_EditingInstructions_v03.pdf					In 188.6.2.2 (P104 L11) Change "PMA transmitter" to "transmitter"				
					In 188.6.5 (P105 L42), Change "PMA" to "PMD"				
					In Figure 188-17 (P110), Change "PMA" to "PHY" (2 instances)				
					In 188.9 (P115 L5) Change "DTE/PMA" to "DTE/PHY" and Change PMA to PHY at P115 L9, L11, L13, and L15				
					Change title of 188.12 to match new clause title for 188.				
					In title of 118.12.4.5 change "PMA" to "PMD", and in 118.12.4.5, change Item designations from PMAEn to PMDEn.				
					In PMAE16 Value/Comment (P124 L18) change PMA to PHY				

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Response *Response Status* **C**

ACCEPT IN PRINCIPLE.

Change Title of clause 188 to Physical Coding Sublayer (PCS), Physical Medium Attachment (PMA) sublayer, Physical Medium Dependent (PMD) sublayer, and baseband medium, type 10BASE-T1M

Change first paragraph of 188.1 to read: "This clause defines the type 10BASE-T1M Physical Coding Sublayer (PCS), type 10BASE-T1M Physical Medium Attachment (PMA) sublayer, and the 10BASE-T1M Physical Medium Dependent (PMD) sublayer. Together, the PCS, PMA, and PMD sublayers comprise a 10BASE-T1M Physical Layer device (PHY).

Insert new second paragraph:
Functional and electrical specifications for the type 10BASE-T1M PCS, PMA, PMD, and the interface to the medium, referred to as the Trunk Connection Interface (TCI) are provided in this clause. The Clause 188 10BASE-T1M PCS and 10BASE-T1M PMA are functionally identical to the Clause 147 10BASE-T1S PCS and 10BASE-T1S PMA with the exception that Clause 188 only supports multidrop operation and does not support the functionality required for clause 147 in point-to-point half-duplex or point-to-point full-duplex modes. The specification includes many enhancements for clarity related to multidrop mode operation. The 10BASE-T1M PMD refines the electrical specifications of the 10BASE-T1S PMA (see 147.5) to improve plug-and-play performance, improve multidrop interoperability, and support specification of the enhanced mixing segment in 188.8.

Change Figure 188-1 to add a PMD below the PMA.

Change 188.6 title to PMD electrical specifications

Replace first sentence with: "The 10BASE-T1M PMD converts between the logical DME signals specified in the PMA and the electrical signals on the mixing segment in both transmit and receive directions. It interfaces the PMA to the TCI, and this subclause defines the electrical characteristics of the PMD for a 10BASE-T1M PHY."

In 188.6.2.1 (P104 L7) Change "PMA's receiver" to "PHY's receiver"

In 188.6.2.2 (P104 L11) Change "PMA transmitter" to "transmitter"

In 188.6.5 (P105 L42), Change "PMA" to "PMD"

In Figure 188-17 (P110), Change "PMA" to "PHY" (2 instances)

In 188.9 (P115 L5) Change "DTE/PMA" to "DTE/PHY" and Change PMA to PHY at P115 L9, L11, L13, and L15

Change title of 188.12 to match new clause title for 188.

In title of 118.12.4.5 change "PMA" to "PMD", and in 118.12.4.5, change Item designations from PMAEn to PMDEn.

In PMAE16 Value/Comment (P124 L18) change PMA to PHY

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Clause, Subclause, page, line

<i>Cl</i> 188	<i>SC</i> 188.4.3.1	<i>P</i> 95	<i>L</i> 40	<i>#</i> 1
Zimmerman, George		ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse		
<i>Comment Type</i>	T	<i>Comment Status</i>	A	<i>Out-of-Scope</i>
I realize this is out of scope, but we broke the preamble nibble value when we transcribed it to binary. 22.2.3.2.2 has a string that starts with 1010, but 22.2.3.2.2 states that "the preamble is displayed using the bit order it would have if transmitted serially. This means that for each octet the leftmost 1 value represents the LSB of the octet, and the rightmost 0 value the octet MSB." - hence, we got it backwards, 1010 in binary would ordinarily be understood where "1" was the MSB.				
<i>SuggestedRemedy</i>				
Change "1010" to "0101, where the leftmost bit is the MSB of the octet"				
<i>Response</i>	<i>Response Status</i> C			
ACCEPT IN PRINCIPLE.				
Change "(1010 in binary)" to "(0x5)"				

<i>Cl</i> 188	<i>SC</i> 188.6.5.3	<i>P</i> 106	<i>L</i> 30	<i>#</i> 5
Zimmerman, George		ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse		
<i>Comment Type</i>	T	<i>Comment Status</i>	A	<i>Unsatisfied</i>
Pile on to unsatisfied comment 80 on d2p0. Specify jitter measurement.				
<i>SuggestedRemedy</i>				
Insert the following paragraph at the beginning of 188.6.5.3:				
Measurement of transmitter timing jitter is performed using a clock recovery unit (CRU) that acts as a high-pass jitter filter with a corner frequency of 1.25 MHz and a slope of 20 dB/decade. The clock recovery unit is fed either with the transmitted signal or with TX_CLK if it is provided.				
<i>Response</i>	<i>Response Status</i> C			
ACCEPT.				

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CI 188 SC 188.7 P109 L6 # 4
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A Unsatisfied

Pile on to unsatisfied comment 85 on d2p0. MDIO is optional.

SuggestedRemedy

Replace "10BASE-T1M uses the management interface as specified in Clause 45. The Clause 45 MDIO electrical interface is optional. Where no physical embodiment of the MDIO exists, provision of an equivalent mechanism to access the registers is recommended." with

"10BASE-T1M is specified using the management interface in Clause 45. The Clause 45 MDIO registerinterface and the registers are optional. Where the MDIO interface is not implemented, provision of an equivalent mechanism for the functions specified in connection to the register bits is required."

Response Response Status C

ACCEPT IN PRINCIPLE.

(Editor's note: Added a space between 'register' and 'interface'.)

Replace, "10BASE-T1M uses the management interface as specified in Clause 45. The Clause 45 MDIO electrical interface is optional. Where no physical embodiment of the MDIO exists, provision of an equivalent mechanism to access the registers is recommended."

with, "10BASE-T1M is specified using the management interface in Clause 45. The Clause 45 MDIO register interface and the registers are optional. Where the MDIO interface is not implemented, provision of an equivalent mechanism for the functions specified in connection to the register bits is required."

CI 188 SC 188.8 P109 L15 # 36
Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco
Comment Type E Comment Status A EZ

Trunk Connection Interface is capitalized

SuggestedRemedy

Replace, "trunk connection interface (TCI)" with "Trunk Connection Interface (TCI)"

Response Response Status C

ACCEPT.

CI 188 SC 188.8.1 P111 L9 # 10
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A Out-of-Scope

(note this is out of scope) - Use of "may" - may grants permission (allowing the condition to NOT be true as well), in the statement "If the mixing segment includes TCI connectors which are specified to use a simulated DTE load, this requirement may be met with simulated DTE load attached" (188.8.1, 188.8.2), I believe the intent is that in the condition stated (TCI connectors specified for a simulated DTE load), that is the ONLY permissible way to make a measurement, not just something that happens to be permitted. A mixing segment that passes the spec WITHOUT the loads, but fails with the loads would NOT be compliant.

SuggestedRemedy

Change "may be met" to "is met" at 188.8.1 P111 L9-10, and 188.8.2 P112 L8.

Response Response Status C

ACCEPT IN PRINCIPLE.

Change:

"If the mixing segment includes TCI connectors which are specified to use a simulated DTE load, this requirement may be met with simulated DTE load attached"

to

""If the mixing segment includes TCI connectors which are specified to use a simulated DTE load, this requirement is met with simulated DTE loads or DTE attached"

CI 188 SC 188.8.3 P113 L5 # 11
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type E Comment Status A Editorial

Requirement on the user - two shalls on the same requirement... "Measurements shall be made by substituting the measurement instrument..." is actually describing how the requirement for mode conversion loss is to be met. Needs to be reworded so it fits in a single "shall".

SuggestedRemedy

Change "Measurements shall be made by substituting the measurement instrument for the respective edge terminator." to "This requirement is to be met when the measurement instrument is substituted for the respective edge terminator."

Response Response Status C

ACCEPT IN PRINCIPLE.

Change "Measurements shall be made by substituting the measurement instrument for the respective edge terminator."

to "Measurements are made by substituting the measurement instrument for the respective edge terminator."

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CI 188 SC 188.8.3 P113 L5 # 14
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A Mixing Segment
Is there one requirement or two? Are BOTH TCI and ELTCL supposed to meet the same equation? This appears to be two requirements. I think TCL should be sufficient.
SuggestedRemedy
Delete "and between edge termination reference planes (ELTCTL)".
Insert PICS on MXS4 Value/Comment to be "TCL measured with a reference impedance of 100 /OHMs and a measurement instrument substituted for the respective edge terminator."
(/OHMs is the ohms symbol)
Response Response Status C
ACCEPT.

CI 188 SC 188.8.4 P114 L8 # 33
Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco
Comment Type E Comment Status A Editorial
It may not be obvious what the "E Group" is referring to. The 1, 2, and 3 following E should be subscript.
SuggestedRemedy
Replace "parameter within the E group "with, "parameter within the Environmental classification (i.e., E1, E2, or E3) group"
where 1, 2, and 3 are subscript
Make the "1", "2", and "3" following the Es on line 5 subscript.
Response Response Status C
ACCEPT.

CI 188 SC 188.8.5 P114 L21 # 2
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A Out-of-Scope
In comparing Clause 188 to 147, I noticed that we forgot Alien crosstalk specifications for the mixing segment. As such, multiple pairs of wires contained in a shielded jacket crosstalking to each other would be compliant - but clearly not what we want. NOTE that alien crosstalk was also left out of the clause 147 mixing segment specification....
SuggestedRemedy
Insert new sections 188.8.5 and 188.8.6 between 188.8.4 and 188.9
188.8.5 Power sum alien near-end crosstalk (PSANEXT).
The power sum alien near-end crosstalk (PSANEXT) loss for a 5-around-1 cable bundle, for the length of the mixing segment trunk shall meet Equation (147-6), with DTEs or representative simulated DTE loads attached, at each edge termination reference plane.

188.8.6 Power sum alien attenuation to crosstalk ratio far-end (PSAACRF)
The power sum alien attenuation to crosstalk ratio far-end (PSAACRF) loss for a 5-around-1 cable bundle, for the length of the mixing segment trunk shall meet Equation (147-7), with DTEs or representative simulated DTE loads attached, at each edge termination reference plane.

Response Response Status C
ACCEPT IN PRINCIPLE.

(Editor's note: No change to proposed text - PICS needed.)

Insert new sections 188.8.5 and 188.8.6 between 188.8.4 and 188.9

188.8.5 Power sum alien near-end crosstalk (PSANEXT)
The power sum alien near-end crosstalk (PSANEXT) loss for a 5-around-1 cable bundle, for the length of the mixing segment trunk, shall meet Equation (147-6), at each edge termination reference plane with DTEs or representative simulated DTE loads attached.

188.8.6 Power sum alien attenuation to crosstalk ratio far-end (PSAACRF)
The power sum alien attenuation to crosstalk ratio far-end (PSAACRF) loss for a 5-around-1 cable bundle, for the length of the mixing segment trunk shall meet Equation (147-7) at each edge termination reference plane with DTEs or representative simulated DTE loads attached.

License granted for Editor to work with Commenter to add PICS.

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CI 188	SC 188.9.1.1	P115	L38	# 12
Zimmerman, George		ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse		
Comment Type	ER	Comment Status	A	TCI
duplicate 'shall's' - the requirement for the TCI insertion loss has the primary 'shall', the added phrase "Whenever the requirement results in a value less than 0.2 dB, the requirement shall revert"... we don't put requirements (shall's) on requirements themselves... Additionally, this phrase conflicts with the preceding shall, making the requirement itself (maximum (0.2, Equation 188-6)) - something that wasn't desired in comment resolution. We have several examples of measurable requirements reverting to 0.1 dB in 802.3-2022, but none as high as 0.2 dB. The proposed adjustment of the text mirrors those sections. However, the intent was to allow for measurement instrument accuracy, and the proposed adjustment does NOT capture that aspect, still requiring 0.16 dB minimum, but allowing for measurement inaccuracies. FWIW, I'm inclined to think that if the test fixture can meet 75mOhms (188.9.1.4) from TC1 to TC2, then it can also measure 0.1 dB....				
SuggestedRemedy				
Change "the requirement shall revert" to "the requirement reverts"				
Response	Response Status C			
ACCEPT.				

CI 188	SC 188.9.1.6	P116	L48	# 58
Paul, Michael		Analog Devices		
Comment Type	T	Comment Status	A	TCI
TCIs need to maintain complinace with shalls in 188.9.1.1, 188.9.1.2, and 188.9.1.3 at 2A current levels, not just "with stand without damage"				
SuggestedRemedy				
This might be overkill...The TCI shall withstand without damage the application of any current between -2A and +2A in either polarity from TC1 to TC2 and while complying with insertion loss, return loss, and mode conversion loss as specified in subsections 188.9.1.1, 188.9.1.2 and 188.9.1.3.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Change, "The TCI shall withstand without damage the application of any current up to 2A in either polarity from TC1 to TC2."				
to, "The TCI shall withstand without damage the application of any current up to 2A in either polarity from TC1 to TC2. Additionally, the TCI shall maintain compliance with the specifications of 188.9.1.1, 188.9.1.2, 188.9.1.3, 188.9.1.4, and 188.9.1.5 at any current up to 2A in either polarity from TC1 to TC2."				
Add new PICS item TCI7 (and renumber existing TCI7 as TCI8). Feature = Compliance maintained when powering current is applied Section = 188.9.1.6 Value/Comment = Compliance with 188.9.1.1, 188.9.1.2, 188.9.1.4, and 188.9.1.5 with up to 2 A in any polarity from TC1 to TC2. Status: M				

CI 188	SC 188.12.4.6	P125	L18	# 13
Zimmerman, George		ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse		
Comment Type	ER	Comment Status	A	Mixing Segment
Missing Value/Comment for PICS MXS4				
SuggestedRemedy				
Insert appropriate text after dealing with TCL/ELTCL comment.				
Response	Response Status C			
ACCEPT IN PRINCIPLE. Accomodated by comment 14.				

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CI 188 **SC 188.12.4.6** **P125** **L21** # **37**
Maguire, Valerie Copperopolis; aff'l w/ CME Consulting and Cisco
Comment Type E **Comment Status A** **TCI**
Missing Value/Comment for Mode Conversion Loss PICS MXS5
SuggestedRemedy
Add/value comment: "Measured at each edge termination reference plane (TCL) and between edge termination reference planes (ELTCTL) in both directions"
Response **Response Status C**
ACCEPT IN PRINCIPLE.
(note comment text misidentifies the PICS as MXS5 - it is MXS4)
Accomdated by comment 14.
ACCEPT.

CI 189 **SC 189.1.2** **P128** **L45** # **15**
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type E **Comment Status A** **Editorial**
Use of "must" - must is to be avoided. Besides, this is the informative section and shouldn't have requirements. Also, stating whether the PHY requirements 'apply' would be parallel to the following sentence about when the power is not on the same conductors as data...
SuggestedRemedy
Change "must also meet the requirements for the TCI needed for the PHY (e.g., see 188.9)." to ", "the requirements for the PHY's TCI also apply (e.g., see 188.9)."
Response **Response Status C**
ACCEPT IN PRINCIPLE.

Replace, "When power and data are carried over the same pair, the MPI and the TCI are the same connection to the medium and the MPI must also meet the requirements for the TCI needed for the PHY (e.g., see 188.9)."

with, "When power and data are carried over the same pair, the MPI and the TCI are the same connection to the medium and the requirements for the PHY's TCI also apply to the MPI (e.g., see 188.9)."

CI 189 **SC 189.1.2** **P128** **L47** # **98**
Brandt, David Rockwell Automation
Comment Type E **Comment Status A** **Management**
MPSE/MPD may or may not be co-located with a DTE. In either case, management is via the DTE.
SuggestedRemedy
Add text to end of paragraph: "MPoE power entities may be managed by associated DTEs via LLDP TLVs (79.3.10 through 12) or layer management (30.17), regardless of whether the data and power are on the same or separate pairs."
Response **Response Status C**
ACCEPT IN PRINCIPLE.

(Editor's note: Reference 79.3.12 instead of just 12.)

Add text to end of paragraph: "MPoE power entities may be managed by associated DTEs via LLDP TLVs (79.3.10 through 79.3.12) or layer management (30.17), regardless of whether the data and power are on the same or separate pairs."

CI 189 **SC 189.3** **P130** **L33** # **59**
Paul, Michael Analog Devices
Comment Type T **Comment Status A** **Power-Gen**
Increase ipse type1 min current to maximize power delivery.
SuggestedRemedy
Change type 1 value to 2A (Actual value 1.97A, round to 2A)
Response **Response Status C**
ACCEPT IN PRINCIPLE.

Accomdated by comment 52:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

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CI 189 SC 189.3 P130 L36 # 52
Paul, Michael Analog Devices
Comment Type T Comment Status A Power-Gen
ppse type1 min for 4W devices
SuggestedRemedy
Change type 1 value to 90W
Response Response Status C
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

CI 189 SC 189.3 P130 L37 # 53
Paul, Michael Analog Devices
Comment Type T Comment Status A Power-Gen
ppmd_1u for type 1 devices = 4W
SuggestedRemedy
Change type 1 value to 4.4W
Response Response Status C
ACCEPT IN PRINCIPLE.
Accommodated by comment 52:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

CI 189 SC 189.4.4.2 P132 L45 # 16
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type E Comment Status A Power-SD
The state's name is DISCOVERY_HIGH_MARK, not DISCOVERY_HIGH. (3 instances), but more importantly, this variable is set by the function calls do_discovery_high and do_discovery_low - not simply by being in the states. It should be specified as a return from the function calls not as a separate variable. This also makes the text a lot simpler.

SuggestedRemedy
Move discover_fault description to P135 L25 (do_discovery_high) and P135 L34 (do_discovery_low) (duplicate it) to read:
"discover_fault
<indent below per variables, and braces {} indicate subscripts>
A variable indicating if I{Discovery} measured by the MPSE is equal to or greater than I{Discovery_LIM} as defined in Table 189-3.
Values: FALSE: Measured I{Discovery} was less than I{Discovery_LIM}
TRUE: Measured I{Discovery} was equal to or greater than I{Discovery_LIM}"

Change "variable:" to "variables:" at P135 L33 (do_discovery_low).

Response Response Status C
ACCEPT.

CI 189 SC 189.4.5 P138 L32 # 18
Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse
Comment Type T Comment Status A Power-SD
There is no state DISCOVER_HIGH_MARKx (this probably means HIGH_MARK and DISCOVERY_HIGH_MARK), and we've otherwise gotten rid of DISCOVERY_LOWx (which was previously explained.), but more importantly, I believe the current limit is meant to apply EXCEPT for when the MPSE is in the INRUSH or POWER_ON state (meaning it applies whenever mpi_powered = FALSE, and therefore also applies in DISABLE, IDLE, BACKOFF, HIGH_MARK, and ERROR_DELAY - note, lower current is meant in these states, but the limit would still apply). An easier way would be to say where the limit does NOT apply.

SuggestedRemedy
Change "DISCOVERY_LOWx and DISCOVER_HIGH_MARKx" to "meaning that it limits current except when it is in the INRUSH or POWER_ON states."

Response Response Status C
ACCEPT.

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CI 189 SC 189.4.5. P138 L18 # 17

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type T Comment Status A Power-SD

The text reads like the MPSE might randomly apply V{Discovery} in these states, and that timing somehow relates to entering each states. According to the state diagram, the voltage is applied at the call of present_low in DISCOVERY_LOW_PRESENT, and the timer expires at the exit of DISCOVERY_LOW.

This potentially leaves the voltage in the various checking states (ALL, TARE, TYPE, and EVAL) undefined, but these states are simply expressing the results of the discovery and are instantaneous.

SuggestedRemedy

change "The MPSE supplies VDiscovery voltage to the TCI subject to the TDiscovery_low timing specification in any of the following states shown in Figure 189–3 and Figure 189–4:" to

"The MPSE supplies VDiscovery voltage to the TCI in the DISCOVERY_LOW_PRESENT state through the expiration of the T{Discovery_low} timer in the DISCOVERY_LOW state."

Response Response Status C

ACCEPT IN PRINCIPLE.

Replace lines 18 through 27 on page 138 (from "The MPSE supplies..." up to and including list item f))

With, "During the DISCOVERY_LOW_PRESENT state, MPI voltage transitions between V{Mark} and V{Discovery}. Upon entering the V{Discovery} voltage range, the MPSE waits T{Discover_measure} before transitioning to the DISCOVERY_LOW state. This allows for internal voltages in the MPDs to settle. The MPSE supplies VDiscovery voltage to the TCI in the DISCOVERY_LOW state through the expiration of the T{Discovery_low} timer."

*Items in {} are subscript

CI 189 SC 189.4.6 P139 L33 # 19

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type E Comment Status A Out-of-Scope

(out of scope) Both shorts and opens are "bad" - why call the short circuit limit I{bad} and the open circuit limit I{open}?

SuggestedRemedy

change I{bad} to I{short} at P139 L34

Response Response Status C

ACCEPT.

CI 189 SC 189.4.6 P140 L15 # 54

Paul, Michael Analog Devices

Comment Type T Comment Status A Power-Gen

Pmpse needs to stay consistent with changes we make in section 189.3

SuggestedRemedy

90W, min

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 52:

ACCEPT IN PRINCIPLE.

Implement changes in

https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

CI 189 SC 189.4.6 P140 L19 # 55

Paul, Michael Analog Devices

Comment Type T Comment Status A Power-Gen

Ilim needs to stay consistent with changes we make in section 189.3

SuggestedRemedy

2.3A is already the max value, but we may need a 2A min value in which case item 4 needs to split into 2 lines so that type 0 and type 1 can have different limits

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 52:

ACCEPT IN PRINCIPLE.

Implement changes in

https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

Cl 189	SC 189.5.5	P149	L40	# 57
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Paul, Michael Analog Devices

Comment Type T Comment Status A Power-Gen

Update input power for type 1 consistent with decisions made in section 189.3

SuggestedRemedy

4.4W min, 70.4max

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 52:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

Cl 189	SC 189.5.5	P149	L40	# 56
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Paul, Michael Analog Devices

Comment Type T Comment Status A Power-Gen

Update unit power for type 1 consistent with descisions made in section 189.3

SuggestedRemedy

4.4W

Response Response Status C

ACCEPT IN PRINCIPLE.

Accommodated by comment 52:
ACCEPT IN PRINCIPLE.
Implement changes in
https://www.ieee802.org/3/da/public/0525/paul_01_da_20250513_v1.pdf

Cl 189	SC 189.5.5.5	P152	L28	# 20
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Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type T Comment Status R Power-Gen

Reading 189.5.5.5 makes me think that C{port} isn't actually a requirement, and is more an informative value. The text says it can be exceeded, but that the requirements for I{Inrush_MPD} and P{MPD} still apply. C{Port} is a requirement because everything in Table 189-9 is required by the first line of 189.5.5. Perhaps C{Port} should be listed separately as a note to the table, to make it clear that it is not actually a requirement.

SuggestedRemedy

Add note b attached to items 4 & 5 (Input Power and Inrush current) in Table 189-9.
Note b to read: "MPD MPI capacitance during power on, C{Port} is limited to a maximum 20 uF per unit load by these requirements with special consideration. See 189.5.5.2 for further details."
Delete row 10 (C{Port}) of Table 189-9.

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

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CI 189 SC 189.6.1 P153 L10 # 115

Schreiner, Stephan Rosenberger Hochfrequenztechnik GmbH & Co. KG

Comment Type TR Comment Status R TCI

Equation 189-1 causes issues with sections 188.8 and 188.9. For high unit loads greater than 11, the channel return loss specified in 188.8 cannot be met because the MPI RL limit is more lenient than the channel RL limit. For lower unit loads, it is assumed that the channel return loss will also not be met and therefore needs to be verified. For unit loads greater than 3, the return loss limit, in conjunction with the TCI insertion loss, is not passive, contradicting the concept of making the return loss dependent on the unit load in order to reduce implementation effort. The limit line at frequencies above 26.5 MHz is stricter than the TCI return loss, impacting technical feasibility. In summary, the introduction of 189.6.1 and its dependency on the Unit Load Concept affects multiple sections in 188.8 and 188.9 and will require significant effort to resolve and align the affected requirements.

SuggestedRemedy

Insert a note: Impairments to sections 188.8 and 188.9 have been identified. Adaptations to sections 188.8, 188.9, and 189.6.1 might be necessary to maintain consistency and technical feasibility.

Response Response Status W

REJECT.
Commenter offers insufficient information for a remedy.

Note: Chair to charter an ad hoc to review return loss as well as TCI requirements for carrying power. Ad hoc to report by July - in preparation for comments to initial SA ballot.

CI 189 SC 189.6.2.1.1 P154 L9 # 40

Jones, Chad Cisco Systems, Inc.

Comment Type E Comment Status A Isolation

Questioning if the separated sentences gives wiggle room for non-compliance: "A device incorporating at least one isolated MPD shall provide electrical power isolation between all MPis on the device. Note this includes MPis associated with either additional MPDs or any MPSE."

SuggestedRemedy

combine the sentences: "A device incorporating at least one isolated MPD shall provide electrical power isolation between all MPis on the device, including MPis associated with either additional MPDs or any MPSE."

Response Response Status C

ACCEPT.

CI 189 SC 189.6.2.1.1 P154 L18 # 23

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type E Comment Status A Isolation

The requirement about switching of MPSEs is a big difference in implementation and can easily be lost. It should be highlighted as its own section.

SuggestedRemedy

Create new section 189.6.2.1.2 MPSE switching for isolated MPoE systems (insert header at P154 L17 before "An isolated MPSE...")

Similarly, create new section 189.6.2.2.2 MPSE switching for grounded MPoE systems at P155 L21.

Move text "A Grounded MPSE shall switch the more positive conductor. It is allowed to switch both conductors." from M155 L6 (2nd paragraph of 189.6.2.2.1) to the new section. Change cross reference in 2nd paragraph of 189.6.2.2 P154 L44 from "189.6.2.2.1" to "189.6.2.2.2" (to point to new section).,

Change PICS AES4 to cross-reference the new section 189.6.2.2.2.

(change new PICS added for Isolated MPSE switching by comment on 189.8.4.4, as well if that comment is implemented)

Response Response Status C

ACCEPT.

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

CI 189 SC 189.6.2.2 P154 L22 # 41

Jones, Chad Cisco Systems, Inc.

Comment Type E Comment Status A Isolation

The first sentence is hard to parse and is missing a period. also, missing a comma after "figure 189-11". "Grounded MPoE systems, as shown in Figure 189-11 are recommended for mixing segments which, with all associated interconnected equipment, share a common, continuous ground" it's the "with all associated interconnected equipment" part that throws it off. suggest moving it to the end of the sentence or deleting it altogether.

SuggestedRemedy

change to: "Grounded MPoE systems, as shown in Figure 189-11, are recommended for mixing segments which share a common, continuous ground."

or to: "Grounded MPoE systems, as shown in Figure 189-11, are recommended for mixing segments which share a common, continuous ground with all associated interconnected equipment."

Response Response Status C

ACCEPT IN PRINCIPLE.

(Editor's note: Delete 'associated' - interconnected equipment, by default, is associated.)

Change to: "Grounded MPoE systems, as shown in Figure 189-11, are recommended for mixing segments which share a common, continuous ground with all interconnected equipment."

CI 189 SC 189.6.2.2 P154 L23 # 21

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type E Comment Status R Editorial

missing period at the end of the sentence

SuggestedRemedy

add a period after "continuous ground" at P154 L23

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Consider after comment #41 is resolved.

CI 189 SC 189.6.2.2 P154 L26 # 45

Jones, Chad Cisco Systems, Inc.

Comment Type T Comment Status A Isolation

Figure 198-11: This figure shows being allowed to ground at the MPSE. It doesn't show grounding the MPD, and I assume we don't want that, ground loops and all that. Should we go farther and say that MPDs SHALL NOT ground either conductor before the isolation barrier? Make it clear the system needs to rely only on the ground at the MPSE? Experience shows us that this one of the common mistakes made by PD designers.

SuggestedRemedy

Add sentences at page 154, line 23 (new second and third sentence): "Note that the MPD in Figure 189-11 has no ground connection that isn't across the required isolation barrier. This is intentional to prevent ground loops and to satisfy the isolation requirement specified in 189.6.2.2.1."

add sentence at page 155, line 16 (new second sentence): "To state this plainly, this requires that neither of the MPI conductors has an impedance less than 1 Mohm to ground provided through the MPD."

Response Response Status C

ACCEPT IN PRINCIPLE.

Add to Figure 189-10 and 189-11 showing "High Resistance - See Annex J" to ground on the line-side connections of the MPD.

Add Note to P154, L38 under figure:

"NOTE - The MPD in Figure 189-11 has no low-resistance ground connection on the line side of the required isolation barrier. This is intentional to prevent ground loops and to satisfy the isolation requirement specified in 189.6.2.2.1."

CI 189 SC 189.6.2.2 P154 L43 # 22

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type T Comment Status A Editorial

"MPDs are specified in 189.6.2.2.1 to switch their more positive conductor" - this is specified to be grounded MPSEs, not MPDs, and is in contrast to the isolated MPSE specification in 189.6.2.1.1.

SuggestedRemedy

Change MPDs to "grounded MPSEs" at P154 L43

Response Response Status C

ACCEPT IN PRINCIPLE.

(Editor's note: "grounded MPSEs" should be "Grounded MPSEs" in Suggested Remedy.)

Change MPDs to "Grounded MPSEs" at P154 L43

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Cl 189 **SC 189.6.2.2** **P154** **L43** # **43**

Jones, Chad Cisco Systems, Inc.

Comment Type E **Comment Status R** *Editorial*

"MPDs are specified in 189.6.2.2.1 to switch their more positive conductor..."
We do not specify this for MPDs. We do specify this for MPSEs, so perhaps this is a typo.
I'm going to assume the typo part and not try to read into that we were trying to say something else.

SuggestedRemedy
change to: "MPSEs are specified in 189.6.2.2.1 to switch their more positive conductor..."

Response **Response Status Z**

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 189 **SC 189.6.3** **P155** **L26** # **25**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type E **Comment Status A** *Out-of-Scope*

(out of scope) saying "tolerate I{LIM} for T{LIM}" here , so far from the values and with all that new intervening isolation text leaves the reader hunting for meaning.

SuggestedRemedy
Insert "(see Table 189-5)" after T{LIM}

Response **Response Status C**

ACCEPT.

Cl 189 **SC 189.6.3** **P155** **L26** # **42**

Jones, Chad Cisco Systems, Inc.

Comment Type E **Comment Status A** *Out-of-Scope*

"MPSEs tolerate ILIM for TLIM when connected..." - we're pretty far away (page wise) from where ILIM and TLIM are defined. It would be nice to provide a pointer.

SuggestedRemedy
change to: "MPSEs tolerate ILIM for TLIM, as defined in Table 189-5, when connected..."

Response **Response Status C**

ACCEPT IN PRINCIPLE.

Accommodated by comment #25.

Insert "(see Table 189-5)" after T{LIM}

Cl 189 **SC 189.7.8** **P157** **L14** # **27**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type T **Comment Status A** *Isolation*

Seems there is a word missing "shall also indicate the MPI(s) are internally grounded or intended to be grounded at an external connection point". Not quite sure, but I think is should be indicate WHETHER MPI(s) are internally grounded or whether they are intended to have external grounds - if so, this needs to apply to each MPI.

SuggestedRemedy
Change "indicate the MPI(s) are internally grounded or intended to be grounded" to "indicate whether each MPI is internally grounded or whether it is intended to be grounded"

Response **Response Status C**

ACCEPT.

Cl 189 **SC 189.7.8** **P157** **L26** # **44**

Jones, Chad Cisco Systems, Inc.

Comment Type E **Comment Status A** *Isolation*

"Grounded MPSEs and MPDs that are only compatible with grounded MPSEs as permitted in 189.6.2.2.1..."
Grounded MPSEs that are only compatible with grounded MPSEs??? What are we trying to say, why do we need MPSE and MPD in the subject?
I think we are trying to say if you have a grounded system, all components must be compatible with a grounded system. More importantly, that mixing a grounded component into an ungrounded system will cause problems. Perhaps we can find a better way to say this? The sentence gets easier to read if we remove "that are only compatible" part but stills says the same thing.

SuggestedRemedy
change to: "Grounded MPSEs and MPDs as permitted in 189.6.2.2.1..."

Response **Response Status C**

ACCEPT IN PRINCIPLE.

"MPSEs and MPDs that are only compatible with grounded MPoE systems, as described in 189.6.2.2, shall be clearly labeled to indicate..."

Adjust PICS L1 feature to align with the new wording, "Labeling for MPDs and MPSEs only compatible with Grounded MPoE systems"

IEEE P802.3da D2.2 10 Mbps Multidrop Enhancements

Cl 189 **SC 189.7.8** **P157** **L26** # **26**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type **E** **Comment Status** **R** **Isolation**

These requirements seem misplaced. They are requirements, whereas everything else here is a labeling recommendation. They are only applicable to grounded MPSEs so they should be in the grounded MPoE section (189.6.2.2.1)

SuggestedRemedy

Move paragraph after item i) (P157 L26-29) to after 3rd paragraph of 189.6.2.2.1 (before "An isolated MPSE shall switch...").
Delete "as permitted in 189.6.2.2.1" (because it is now unnecessary).
Delete 189.8.4.6, and move PICS L1` and L2 to (end of) 189.4.4 as new PICS AES 8, 9 (or higher if other PIC is added).
Update subclause reference in PICS to 189.6.2.2.1 (for both PICS).(note other comments change these PICS too)

Response **Response Status** **C**

REJECT.
Requirement is not misplaced.

Cl 189 **SC 189.8.4.4** **P161** **L35** # **24**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type **T** **Comment Status** **A** **Isolation**

Missing PICS for Isolated MPSE switched conductor, PICS must specify that an MPSE must support at least one of the options of grounded or isolated MPSE.

SuggestedRemedy

Add PICS after AES3 and renumber,
Feature: Isolated MPSE switched conductor
Subclause: 189.6.2.1.1 (new section 189.6.2.1.2 if comment to create new section for this requirement is adopted)
Value/Comment: Switch on the more negative conductor
Status: O.1: MPSE
Yes[] N/A[]

Change status for (what is in d2.2 AES4) to O.1: MPSE

Response **Response Status** **C**

ACCEPT.
(note, use 189.6.2.1.2 since comment 23 creating a new section was accepted.)

Cl 189 **SC 189.8.4.6** **P162** **L36** # **28**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type **E** **Comment Status** **A** **Isolation**

The Value/Comment doesn't seem to reflect the requirement

SuggestedRemedy

Change Value/Comment to read "Indicate that they are only compatible with grounded MPoE systems."

Response **Response Status** **C**

ACCEPT.

Cl 189 **SC 189.8.4.6** **P162** **L39** # **29**

Zimmerman, George ADI,APLgp,Cisco,Marvell,OnSemi,Sony,SenTekse

Comment Type **E** **Comment Status** **A** **EZ**

Missing subclause

SuggestedRemedy

Add 189.7.8 to Subclause

Response **Response Status** **C**

ACCEPT.