

IEEE P802.3da D1.3 10 Mbps Multidrop Enhancements

CI 169 SC 169.2 P98 L22 # 106
Paul, Michael Analog Devices
Comment Type T Comment Status D Mixing segment - LATE
12 Ohm channel number and text describing the mixing segment needs to be updated
SuggestedRemedy
See presentation paul_da_01_2024_07_16.pdf
Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation
Process with comment #4

CI 169 SC 169.3 P99 L9 # 108
Paul, Michael Analog Devices
Comment Type T Comment Status D MPD - LATE
Update unit load numbers to power mapping for type 0 and type 1 MPDs in this line: "For Type 0 MPDs, one unit load represents 1W. For Type 1 MPDs, one unit load represents 2W."
SuggestedRemedy
Change 1W to 1.2W. Change 2W to 4.5W as follows: "For Type 0 MPDs, one unit load represents 1.2W. For Type 1 MPDs, one unit load represents 4.5W." See presentation paul_da_01_2024_07_16.pdf

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

CI 169 SC 169.5.5 P118 L31 # 105
Paul, Michael Analog Devices
Comment Type T Comment Status D MPD - LATE
T_{Inrush backoff} timer needs to be longer because Discovery High Event Time was (proposed) set to 44ms and the voltage regions for discovery high and Type 0 VPort_MPD are overlapping.

SuggestedRemedy
Set min and max values to 60ms and 75ms respectively

Proposed Response Response Status W
PROPOSED ACCEPT.

CI 169 SC 169.5.5 P118 L36 # 103
Paul, Michael Analog Devices
Comment Type T Comment Status D MPD - LATE
Table 169-8 item 10 "Cport,Max" is TBD
SuggestedRemedy
Ilim and Cport are linked and dependent on the outcome of presentation paul_01 . See presentation paul_da_02_2024_07_16.pdf
Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

CI 169 SC 169.3 P99 L17 # 107
Paul, Michael Analog Devices
Comment Type T Comment Status D MPSE - LATE
Recalculate Table 169-1 using 24V nominal supply using and 40hms cable resistance. Also recalculate Type 1 power using 40hm cable

SuggestedRemedy
See presentation paul_da_01_2024_07_16.pdf

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

CI 169 SC 169.4.6 P107 L19 # 110
Paul, Michael Analog Devices
Comment Type T Comment Status D MPSE - LATE
Item 5, "Discovery low event time" max is TBD

SuggestedRemedy
Set to 44ms. 20ms is allocated to settling, another 20ms should be allocated for a 50Hz power line cycle length of measurement, the extra 4ms is for margin. See Paul_da_01_20240124_v2.pdf slides 17-20 for a description of Mark-Discover timing.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

IEEE P802.3da D1.3 10 Mbps Multidrop Enhancements

Cl 169	SC 169.4.6	P107	L19	# 109
--------	------------	------	-----	-------

Paul, Michael Analog Devices

Comment Type **T** Comment Status **D** MPSE - LATE

Item 4, "Discovery high event time" max is an emdash, but needs to be a number.

SuggestedRemedy

Set max to 44ms. 6.5ms is allocated to settling into the mark voltage, another 20ms should be allocated for a 50Hz power line cycle length of measurement, a further 12ms may be needed for compliance measurement ambiguity as the mixing segment settles back into discovery. The extra 6.5ms is margin. See Paul_da_01_20240124_v2.pdf slides 17-20 for a description of Mark-Discover timing.

Proposed Response Response Status **W**

PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

Cl 169	SC 169.4.6	P107	L21	# 111
--------	------------	------	-----	-------

Paul, Michael Analog Devices

Comment Type **T** Comment Status **D** MPSE - LATE

Total discovery time is a function of 5 discovery pulses that all have min / max timing specifications, this timer is not needed.

SuggestedRemedy

Remove Item 6 "Discovery Time" in table 169-3.

Proposed Response Response Status **W**

PROPOSED ACCEPT.

Cl 169	SC 169.4.7	P108	L12	# 104
--------	------------	------	-----	-------

Paul, Michael Analog Devices

Comment Type **T** Comment Status **D** MPSE - LATE

Item 3, "Output Slew Rate" has TBD for both the Min and Max values

SuggestedRemedy

See presentation paul_da_03_2024_07_16.pdf

Proposed Response Response Status **W**

PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation

Cl 169	SC 169.4.7	P108	L13	# 102
--------	------------	------	-----	-------

Paul, Michael Analog Devices

Comment Type **T** Comment Status **D** MPSE - LATE

Item 4 Ilim has TBD in the min and max columns.

SuggestedRemedy

Ilim and Cport are linked and dependent on the outcome of presentation paul_01 . See presentation paul_da_02_2024_07_16.pdf

Proposed Response Response Status **W**

PROPOSED ACCEPT IN PRINCIPLE.
TFTD - awaiting presentation
