**Table 169–5—PSE output requirements**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Item** | **Parameter** | **Symbol** | **Unit** | **Min** | **Max** | **Type** | **Additional Information** |
| 1 | DC output voltage during POW-  ER\_ON state | VMPSE(PON) | V | 26 | 30 | 0 | See 169.4.8 |
|  | 45 | 50 | 1 | Merge with above |
| 2 | Continuous output current capability in POWER\_ON  state | Pavg/VMPSE  (PON) | mA | TBD | 100 | 0 | See 169.4.9 |
| TBD | 100 | 1 | Merge with above |
| 3 | Output Slew Rate |  | dV/dt | TBD | TBD | ALL |  |
| 4 | Output current - at short circuit condi-  tion | ILIM | A | TBD | TBD | ALL | See 169.4.10 |
| 5 | Short-circuit time limit | TLIM | ms | 10 | 75 | ALL |  |
| 6 | Inrush time | TInrush | ms | 10 | 20 | ALL |  |
| 7 | MPD Maintain power signature dropout time limit | TMPSDO | ms | TBD | TBD | ALL | See 169.4.11 |
| 8 | PD MPS time for validity | TMPS | ms | 6 | - | ALL | Merge with above |
| 9 | DC MPS current | IHOLD | A | 4 | 9 | ALL | Merge with above |
| 10 | Error delay timing | TED | ms | 750 | - | ALL |  |
| 11 | Overload current | ICUT | A | TBD | TBD | ALL |  |
| 12 | Overload time limit | TCUT | ms | 50 | 70 | ALL |  |