

ID	CommenterName	CommenterCo	Clause	Subclause	Page	Line	Comm	Comment	SuggestedRemedy	Response	Topic
1	Lusted, Kent	Synopsys	98	98.5.2	36	49	TR	The timer for the 100BASE-T1L PHY is set to a very specific value of 85ms, without any allowance for variation in clock rates between partners. Also, an exact value of 85.00000000000000 ms would be difficult to meet in design. Allowing a narrow range would simplify the design and still follow the spirit of the timeout value.	Change "85 ms" to "85 ms to 86 ms" in the text as well as the PICS item SD21	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 253.	State Diagrams
2	Lusted, Kent	Synopsys	190	190.7.1.4.1	120	3	E	The abbreviation "TCL" is used as the title for subclause 190.7.1.4.1 and 190.7.1.4.2. However, the abbreviation is not defined anywhere and it is not clear to this reader as to what "TCL" is.	Provide the expanded abbreviation "TCL" at least once in the document. Consider adding to the Abbreviation list in Clause 1.4.	PROPOSED ACCEPT IN PRINCIPLE. TCL is already in the list in Clause 1.4, that definition is expanded and used in the change below:Change header for 190.7.1.4.1 from "TCL (shielded)" to "Transverse Conversion Loss Scd11/Scd22 (TCL) (shielded)"	Editorial
3	Martino, Kjersti	Inneos	190	190.2.1.2.3	49	38	E	Typo in Heading "Effect or receipt"	Change to "Effect of receipt"	PROPOSED ACCEPT.	EZ
4	Martino, Kjersti	Inneos	190	190.2.2.15.3	58	47	E	Typo in Heading "Effect or receipt"	Change to "Effect of receipt"	PROPOSED ACCEPT.	EZ
5	Martino, Kjersti	Inneos	190	190.2.2.16.3	59	22	E	Typo in Heading "Effect or receipt"	Change to "Effect of receipt"	PROPOSED ACCEPT.	EZ
6	Schicketanz, Dieter	Reutlingen University	190	190.5.4.1	112	38	T	1.0 Vpp operating mode (and 2.0 Volt) are defined here , but there is no explanation when to use each. In the link specification only 500m is specified. Fort what voltage level?	define somewhere where each Voltage is used and add in link spec a secon link like in dg.	PROPOSED ACCEPT IN PRINCIPLE. TFTD Consider proposals for an additional link segment for the 1Vpp mode - OR - add explicit language stating that it uses the same link segment, but that noise environments may require the increased voltage	Reduced TX level
7	Schicketanz, Dieter	Reutlingen University	190	190.1	44	28	T	RS-FEC is optional and mentioned in varios clauses. Explanation is given at line 28. Is this sufficient fort planers of cabling?	enhanced burst noise protection is not helpful in a standard. How many dB or other tecnical value ls needed.	PROPOSED REJECT. CRG disagrees with commenter. The standard specifies interoperability and capabilities. It is not a tutorial for use. Use of the RS-FEC capability may be varied among users. "Enhanced burst noise protection" conveys the discussions in the Task Force which introduced the feature.	RS-FEC
8	Schicketanz, Dieter	Reutlingen University	190	190.7	117	31	T	This clause specifies Link segment characteristics differently to cg. Why ? UTP starts at 1MHz, shielded from .5 MHz .Insertion loss from .1MHz	Using cg as example rearrange clause 190.7 . And separate Unshielded links by specifying it by TCL and shielded links by coupling attenuation	PROPOSED REJECT. CRG disagrees with commenter. The specification in this clause was driven by discussions and measurements and follows the model of clause 97 option A. Coupling attenuation is generally application environment specific and is left to the cabling specifications for shielded cable.	EMC
9	Schicketanz, Dieter	Reutlingen University	190	190.7.1.1	118	41	T	as 2 transmit voltages are specified there should be 2 corresponding links as in cg	as in cg, add second link	PROPOSED ACCEPT IN PRINCIPLE. TFTD While the commenter suggests a second link, a proposal is needed.	Reduced TX level
10	Schicketanz, Dieter	Reutlingen University	190	190.7.1.4.1	120	3	T	It is unusual to specify only TCL for shielded links	delete this subclause and replace by coupling attenuation.As starting values take cg values (extended to 60 MHz) and add E1 E2 and E3 and the electromagnetic noise environment . This would solve line 6 too. If TCL is kept match lower frequencies	PROPOSED REJECT. CRG disagrees with commenter. The values in this section were driven by measurements of shielded cabling.	EMC
11	Schicketanz, Dieter	Reutlingen University	190	190.7.1.4.2	121	2	T	It is unusual to specify a specific cable type in a system standard	delete from line 2 and 3: "and is specified to align with the use of Category 6 cables and components". Match starting frequencies to .1 MHz and add E1 and E2 as in cg.	PROPOSED REJECT. CRG disagrees with commenter. Cabling category is specified in other IEEE Std 802.3 BASE-T clauses. See, e.g., clauses 25, 33, 40, 55, 113, and 126.	Link Segment

12	Schicketanz, Dieter	Reutlingen University	00	0	121	35	T	electromagnetic classifications missing	add the subclause "146.7.1.6 Electromagnetic classifications" from cg in page 121 line 35 as new subclause. just a remark, as not specified there will be different connectors on the market from different vendors at the end equipment	PROPOSED REJECT. CRG disagrees with commenter. Electromagnetic classifications are not referenced in the specification, so repeating the re-iteration of ISO/IEC specifications, as is done in 146.7.1.6 is unnecessary.	EMC
13	Schicketanz, Dieter	Reutlingen University	190	190.8.1	124	26	E	MDI connectors		PROPOSED REJECT. Commenter does not offer sufficient remedy.	MDI
14	Schicketanz, Dieter	Reutlingen University	190	190.8.2	124	33	T	MDI electrical specifications start at 1MHz	should start from 0.1 MHz (various locations) to match link and cg	PROPOSED ACCEPT IN PRINCIPLE. TFTD There is no good technical reason to require 100BASE-T1L link segments to be a proper subset of 10BASE-T1L link segments. Many cables are only qualified to 1 MHz low frequency, which is sufficient for 100BASE-T1L. Suggest harmonizing all specifications to start at 1 MHz.	MDI
15	Brown, Matt	Alphawave Semi	FM	FM	12	26	E	The abstract for 802.3dj was updated in D2.0.	Update 802.3dj abstract with text from D2.0.	PROPOSED ACCEPT IN PRINCIPLE. Replace 802.3dj abstract with: This amendment includes changes to IEEE Std 802.3-2022, and adds Clause 174 through Clause 187 and Annex 174A through Annex 186A. This amendment includes Physical Layer specifications and management parameters for 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s operation.	EZ
16	Brown, Matt	Alphawave Semi	1	1.4.341a	21	40	E	These definitions are merged into the master IEEE definitions list. As written, this definition would not be resolvable. This definition should be self-standing and, if referencing clauses, subclauses, or annexes in 802.3, then the references should be prefaced with "IEEE Std 802.3". As written it is rather unclear what the definition is supposed to be.	Update the definition per comment.	PROPOSED ACCEPT IN PRINCIPLE. Accommodated by comment 59	Editorial
17	Brown, Matt	Alphawave Semi	1	1.4.371a	21	44	E	These definitions are merged into the master IEEE definitions list. As written, this definition would not be resolvable. This definition should be self-standing and, if referencing clauses, subclauses, or annexes in 802.3, then the references should be prefaced with "IEEE Std 802.3". As written it is rather unclear what the definition is supposed to be.	Update the definition per comment. Change: With the transmitter in test mode 3 and, if 2.0 Vpp mode is supported, in test mode 4, and using the transmitter test fixture shown in Figure 190–23. To: The transmitter output droop is measured with the transmitter in test mode 3 and in test mode 4 (if 2.0 Vpp mode is supported) using the transmitter test fixture shown in Figure 190–23.	PROPOSED ACCEPT IN PRINCIPLE. Accommodated by comment 59	Editorial
18	Slavick, Jeff	Broadcom	190	190.5.4.2	112	44	TR	Incomplete sentence, there is no "what to do"		PROPOSED ACCEPT.	EZ
19	Slavick, Jeff	Broadcom	190	190.3.4.3	84	30	E	The number 6 is less than 10 and so it should be spelled out.	Change "6 PAM2" to "six PAM2"	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE.	EZ
20	Slavick, Jeff	Broadcom	190	190.3.2.7	70	54	E	Is the equation of "normal" size, seems a bit small.	Check if the proper font is used for the $x^8 + x^4 + 1$.	Increase font size of equation at line 54 to align with text.	EZ

21 Slavick, Jeff	Broadcom	190 190.3.2.7	71	18	E	m(x) in the sentence should be italics	Italicize the m(x) after the word polynomial	PROPOSED ACCEPT.	EZ
22 Slavick, Jeff	Broadcom	190 190.3.2.7	71	24	E	The mi in the first sentence should be italics	Italicize the mi after the word symbol	PROPOSED ACCEPT.	EZ
23 Slavick, Jeff	Broadcom	190 190.3.2.7	71	24	TR	Which element is being identified?	Insert the following after the word element in italics with appropriate sub/superscripting "mi,5a^5 + mi,4a^4 + ... + mi,1a + mi,0" with a using the alpha character.	PROPOSED ACCEPT. (note, see 5th paragraph in 91.5.2.7)	EZ
24 Slavick, Jeff	Broadcom	190 190.3.2.6	70	30	T	We don't use ", " as a thousand seperator. The statement that mi,0 is the first bit transmitted is duplicative with the last sentence of this sub-section (pg71 lin 52).	Change "1,024" to "1024"	PROPOSED ACCEPT.	EZ
25 Slavick, Jeff	Broadcom	190 190.3.2.7	71	25	TR		Remove "mi,0 is the first bit transmitted" Delete: tx_RSmessage<975:0> prior to the RS-FEC(128,122) encoder is formed as follows: tx_RSmessage<975:0> = tx_group<975:0> Replace the two remaining instances of tx_RSmessage with tx_group.	PROPOSED ACCEPT.	RS-FEC
26 Slavick, Jeff	Broadcom	190 190.3.2.7	71	26	TR	tx_RSmessage<975:0> is defined after it's used.	Add the following before "where:" from the Transmit process level. The Reed-Solomon decoder extracts the message symbols from the codeword, corrects them as necessary and discards the parity symbols. The RS-FEC decoder shall be capable of correcting any combination of up to t=3 symbol errors in a codeword. The probability that the decoder fails to indicate a codeword with t+1 errors as uncorrected is not expected to exceed 10^-6. This limit is also expected to apply for t+2 errors, t+3 errors, and so on. The following counters shall be provided: FEC_corrected_cw_counter A 32-bit counter that increments by one for each RX_FRAME event (see 190.3.6.1.6) in which the FEC codeword contains errors and was corrected by the Reed Solomon decoder. FEC_uncorrected_cw_counter A 32-bit counter that increments by one for each RX_FRAME event (see 190.3.6.1.6) in which the FEC codeword contains errors that were detected but no corrected by the Reed Solomon decoder.	PROPOSED ACCEPT.	Editorial
27 Slavick, Jeff	Broadcom	190 190.3.3	78	54	TR	There is no sub-clause describing the operation of the RS-FEC decoder and any status indicators it produces or statistics it provides. The statement that pi,0 is the first bit transmitted is duplicative with the last sentence of this sub-section (pg71 lin 52).	FEC_cw_counter A 48-bit counter that increments by one for each RX_FRAME event (see 190.3.6.1.6).	PROPOSED REJECT. CRG Disagrees with the commenter. RS-FEC specifications integral to the PCS of BASE-T1 PHYs are different from those in high-speed PHYs where RS-FEC has been defined as a separate sublayer. Performance is integrated into the receiver. This has a long history with 1000BASE-T, MultiGBASE-T, and has continued in 1000BASE-T1 and MultiGBASE-T1 PHYs. Separate specification from the receiver performance is not required because the sublayer cannot be separated from the PHY.	RS-FEC
28 Slavick, Jeff	Broadcom	190 190.3.2.7	71	43	TR		Remove "pi,0 is the first bit transmitted"	PROPOSED ACCEPT.	Editorial

							Change: The parity polynomial p(x) is calculated as the remainder of polynomial division of m(x) by g(x). Its coefficients, p5 to p0, as shown in Equation (190–3), are the parity symbols.			
							To one of the following: Equation (190–3) defines the parity polynomial p(x) whose coefficients are the parity symbols p5 to p0. p(x) is the remainder of polynomial division of m(x) by g(x).	PROPOSED ACCEPT IN PRINCIPLE. Change The parity polynomial p(x) is calculated as the remainder of polynomial division of m(x) by g(x). Its coefficients, p5 to p0, as shown in Equation (190–3), are the parity symbols.		
							Or: The parity polynomial p(x) is calculated as the remainder of polynomial division of m(x) by g(x). Equation (190–3) defines the mapping of the parity symbols p5 to p0 to its coefficients.	to The parity polynomial p(x) is calculated as the remainder of polynomial division of m(x) by g(x). Equation (190–3) defines the mapping of the parity symbols p5 to p0 to its coefficients.		
29	Slavick, Jeff	Broadcom	190 190.3.2.7	71	37	T	Too many commas in the sentence		Editorial	
30	Slavick, Jeff	Broadcom	190 190.3.6.2	94	49	TR	The transtion from TX_WAKE is going to where? I don't usually see a state name as the destination.	Make the arrow from TX_WAKE actually just connect directly to TX_MII and remove the TX_MII text from line 49	EZ	
31	Slavick, Jeff	Broadcom	190 190.3.6.2	95	2	T	What does the dotted box mean? This is EEE machine and the NOTE describes its requirement.	Remove the dotted box from Figure 190-12 Make the arrow from SEND_WAKE actually just connect directly to SEND_NORMAL and remove the SEND_NORMAL text from line 45	EZ	
32	Slavick, Jeff	Broadcom	190 190.3.6.2	95	2	TR	The transtion from SEND_WAKE is going to where? I don't usually see a state name as the destination.		EZ	
33	Slavick, Jeff	Broadcom	190 190.3.6.2	96	13	TR	Convention is to use a circled letter and the same letter in a "house" to represent transitions that aren't drawn in (or would require overlapping lines).	In Figure 190-13 part a, replace RX_PKT on line 13 with an enclosed P, replace the path from RX_IDLE to RX_LPI with an enclosed L on line 22, replace the three RX_IDL arcs on lines 28, 34 and 44 with an enclosed I, add circled P going into state RX_PKT, add circled I going into state RX_IDL. In Figure 190-13 partb, add a circled L going into state RX_LPI (within the dotted box) and replace the two instances of RX_IDLE on line 30 with an enclosed I	PROPOSED ACCEPT IN PRINCIPLE. Follow convention in clause 145 which is more readable than single letter tags. In Figure 190-13, at P96 Lines 27, 34, & 44, and P97 L30 (twice) put RX_IDL in a flag, and add an entry 'house' into RX_IDL. Do similarly for RX_PKT and RX_LPI on pages 96 & 97. See e.g., Figure 145-13 for an example.	Editorial
34	Slavick, Jeff	Broadcom	190 190.3.6.1.2	90	38	TR	The definition of rx_lpi_sleep doesn't quite make sense. Isn't a character one thing or another, not a representation of something that looks like a character.	Change "when 32 consecutive rx_char values each represent /LI/" to "when the last 32 rx_char values recevied are /LI/ and EEE is supported and enabled"	Editorial	
35	Slavick, Jeff	Broadcom	190 190.3.6.1.2	90	38	TR	This note stats this "figure" is only mandatory when EEE is enabled. But isn't this a figure that has to be spread over multiple pages, so part a and part b are really "one" figure. Which means this figure is always necessary just the dotted box is only applicable when EEE is enabled (as is stated on part a).	In the definitinon of rx_wk_idle change "each represent" to "are"	Editorial	
36	Slavick, Jeff	Broadcom	190 190.3.6.2	97	32	TR		Replace the note in Figure 190-14, part b with the same note from part a	Editorial	
								PROPOSED ACCEPT IN PRINCIPLE. There is an MDIO register variable at 3.2296.14, which is read only. A variable used ink the encode/decode process would be appropriate if that capability were an option that could be enabled or disabled.		
								Add the following new second sentence to the 4th paragraph of 190.1 (P44 L28), "A PHY that implements the RS-FEC capability indicates it using the MDIO register bit 3.2296.14 or equivalent means. A request to use the RS-FEC capability is negotiated during startup.		
37	Slavick, Jeff	Broadcom	190 190.1	44	28	TR	Is the RS-FEC an optional to use or optional to implement?	If it's optional to implement, then add an RS-FEC Ability variable, mapping it to a MDIO register and in 190.3.2.7 and 190.3.3 qualify RS-FEC descriptions with that variable being TRUE for the encode and decode proceses. If it's mandatory to implement but optional to use, then change this sentence in 190.1 to be "This clause specifies a Reed-Solomon forward error correction (RS-FEC) capability that may be enabled or disabled. The RS-FEC provides enhanced burst noise protection at the expense of increased latency."	RS-FEC	

									PROPOSED ACCEPT IN PRINICIPLE. (typo corrected) Add the following to the last paragraph of 190.3.4.2.4	
38	Slavick, Jeff	Broadcom	190	190.3.4.2.4	83	47	TR	eee_adv and rs_adv are only referred to here, I don't see a section for PCS resolution process.	Add the following to the last paragraph of 190.3.4.2.4 "When the transmitted eee_adv is set to one and the received Oct10<1> is also a one, then EEE enabled. When the transmitted rs_adv is to one and the receieved Oct10<0> is also a one, then RS-FEC mode is enabled."	"When the transmitted eee_adv is set to one and the received Oct10<1> is also a one, then EEE enabled. When the transmitted rs_adv is set to one and the received Oct10<0> is also a one, then RS-FEC mode is enabled." RS-FEC
39	Slavick, Jeff	Broadcom	190	190.3.4.2.4	83	45	TR	Figure 190-6 is the side-stream scrambler figure.	Change the reference to Figure 190-8. The PHY capability bits Oct10<0> and Oct10<1> reflect the values specified by the 100BASE-T1L training register bits 3.2297.14 and 3.2297.15, respectively.	PROPOSED ACCEPT. EZ
								To one of the two following options: The PHY capability bits Oct10<0> and Oct10<1> indicate the PHYs request to enable RS-FEC and EEE modes of operation, respectively. rs_adv is set to one when the 100BASE-T1L PHY has the ability to operate in RS-FEC mode as indicated by status register 3.2296.14 and the 100BASE-T1L training register to request RS-FEC mode of operation is set to a one, 3.2297.14. eee_adv is set to one when the 100BASE-T1L PHY has the ability to operate in EEE mode as indicated by status register 3.2296.15 and the 100BASE-T1L training register to request EEE mode of operation is set to a one, 3.2297.15.	PROPOSED ACCEPT IN PRINCIPLE. TFTD Change: The PHY capability bits Oct10<0> and Oct10<1> reflect the values specified by the 100BASE-T1L training register bits 3.2297.14 and 3.2297.15, respectively.	
								Or alternatively use following changes which utilizes sub-layer variables and maps those variables to the associated MDIO registers, since MDIO is not mandatory, just an option. DJ has moved in this direction of using variables within the sub-layer and then mapping them to MDIO container.	To The PHY capability bits Oct10<0> and Oct10<1> indicate the PHYs request to enable RS-FEC and EEE modes of operation, respectively. rs_adv is set to one when the 100BASE-T1L PHY has the ability to operate in RS-FEC mode as indicated by status register 3.2296.14 and the 100BASE-T1L training register to request RS-FEC mode of operation is set to a one, 3.2297.14. eee_adv is set to one when the 100BASE-T1L PHY has the ability to operate in EEE mode as indicated by status register 3.2296.15 and the 100BASE-T1L training register to request EEE mode of operation is set to a one, 3.2297.15.	
40	Slavick, Jeff	Broadcom	190	190.3.4.2.4	83	41	TR	Only if you actually have the capability should you permit advertisement of EEE and RS-FEC	The PHY capability bits Oct10<0> and Oct10<1> indicate the PHYs request to enable RS-FEC and EEE modes of operation, respectively. rs_adv is set to one when the variables	is set to a one, 3.2297.15. RS-FEC
								Make lines 6 through 25 a new sub-clause titled “Transmit group encoding” that comes before the RS-FEC encoder sub-clause.		
								Insert this text after the first paragraph of 190.3.2.6: MII transfers are encoded into 8N + 1 bit blocks to create a group of 15N + 2 octets per <the newly created sub-clause>		
								Add “(see 190.3.2.7)” after “6 parity octets” on line 30		
								Add “(see 190.3.2.8 through 190.3.2.10)” after Sdn[7:0] on line 33		
								Add “(see 190.3.2.11)” after 8B6T encoding on line 34		
41	Slavick, Jeff	Broadcom	190	190.3.2.6	70	31	TR	If the 190.3.2.6 is to describe all the steps taken from the MII to PMA service interface without all the details, then the flow should be a list of steps with references to the sub-clauses that contain the details.	Make 190.3.2.7 through 190.3.2.11 plus the new sub-clause a sub-heading of 190.3.2.6. (Headings in suggested remedy based on D2.0 heading numbers)	PROPOSED ACCEPT. Editorial

							Bring in 30.5.1.1.15 and add “(or mode of operation)” after optional FEC sublayer in the first paragraph of the behavior and add Clause 190 to the list. Insert MDIO register 45.2.3.75b in the list of capability registers.			
42 Slavick, Jeff	Broadcom	30	30.5.1.1.15	24	54	TR	aFECAbiilty and aFECmode I think should be used rather than aRSFECBypassAbility and aRSFCBypassEnable to indicate in management objects if RS-FEC mode is enabled.	Bring in 30.5.1.1.16 and add “(or mode of operation)” after optional FEC sublayer in the first paragraph of the behavior and add Clause 190 to list. Insert MDIO register 45.2.3.75c to list of FEC operating mode registers.	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comments 246 & 247.	RS-FEC
43 Slavick, Jeff	Broadcom	30	30.5.1.1.17	24	54	TR	aFECUncorrectableBlocks and aFECCorrectedBlocks needs mapping A new abbreviation "ABBR" is being added but I don't see it being used anywhere	Insert and increment rate of 120 000 for 100 Mb/s implementations into the SYNTAX descriptions and add 100BASE-T1L to the list of PHYs in both 30.5.1.1.17 and 30.5.1.1.18	PROPOSED REJECT. CRG Disagrees with the commenter. RS-FEC specifications integral to the PCS of BASE-T1 PHYs are different from those in high-speed PHYs where RS-FEC has been defined as a separate sublayer. Performance is integrated into the receiver. This has a long history with 1000BASE-T, MultiGBASE-T, and has continued in 1000BASE-T1 and MultiGBASE-T1 PHYs. Separate specification from the receiver performance is not required because the sublayer cannot be separated from the PHY.	RS-FEC
44 Slavick, Jeff	Broadcom	1	1.5	22	34	ER		Remove it	PROPOSED ACCEPT.	EZ
45 Slavick, Jeff	Broadcom	190	190.1.3	45	12	T	were derived to is not necessary, 190.7 sepcifies segments that support that channel topology.	Remove "were derived to"	PROPOSED ACCEPT. PROPOSED REJECT.	Editorial
46 Slavick, Jeff	Broadcom	190	190.1.1	44	38	T	First sentence only lists one of the two modes.	Add "or disabled" to the end of the first sentence. Change the last sentence from: The same PMA and MDI specifications apply regardless of whether RS-FEC is enabled.	Disabled is the opposite of enabled. The sentence is clear.	Editorial
47 Slavick, Jeff	Broadcom	190	190.1.1	44	44	T	The PMA/MDI specifications apply for both modes. The number 6 is less than 10 and so it should be spelled out.	To: The same PMA and MDI specifications apply to both encoding methods.	PROPOSED ACCEPT	Editorial
48 Slavick, Jeff	Broadcom	190	190.3.2.7	70	40	E		Change "6 8-bit" to "six 8-bit" Change: The encoder processes 122 8-bit RS-FEC message symbols to generate 6 8-bit RS-FEC parity symbols, which are then appended to the message to produce a codeword of 128 8-bit RS-FEC symbols.	PROPOSED ACCEPT	EZ
49 Slavick, Jeff	Broadcom	190	190.3.2.7	70	41	T		To: The encoder processes 122 RS-FEC message symbols to generate six RS-FEC parity symbols that are appended to the message to produce a codeword of 128 RS-FEC symbols (1024bits	PROPOSED ACCEPT.	Editorial
50 Slavick, Jeff	Broadcom	190	190.3.2.1	62	7	T	We don't use ", " as a thousand seperator.	Change "1,024" to "1024"	PROPOSED ACCEPT	EZ

51	He, Xiang	Huawei Technologies	190	190.3.2	63	30	TR	In Figure 190-4. The "Low-latency/RS-FEC select" is never mentioned anywhere in the document, and the mux/switch box is not an accurate illustration in the figure. When RS-FEC is enabled, the RS-FEC encoder in the dashed box is used, and this mux has to be switched to the upper path. When RS-FEC is disabled, the RS-FEC in the dashed box is not used and the mux has to be switched to the lower path.	Suggest to rename "Low-latency/RS-FEC select" to "RS-FEC enable". Clearly mark 1 on the upper path, and 0 on the bottom path.	PROPOSED ACCEPT	Editorial
52	He, Xiang	Huawei Technologies	190	190.3.2	63	21	TR	"Used when N=8, bypassed when N=2" on top of the dashed box seems odd. In 190.3.2.1, line 5 of page 62, it clearly says "When RS-FEC is disabled, N is 2..... When RS-FEC is enabled, N is 8 ...". The actual thing determining which path is used is "RS-FEC enable". The number N is not an input, but a result.	Suggest to change the sentence on top of the dashed box as "Used when RS-FEC is enabled, bypassed when RS-FEC is disabled".	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE. Delete 190.3.7 header.	RS-FEC
53	He, Xiang	Huawei Technologies	190	190.3.7	99	1	ER	PCS management subclause is empty.	Add proper content to this subclause. Call it "PCS management variables" if this subclause is going to list all management variables with MDIO mapping.	Management variables are spelled out where they apply and in registers. There is no need for a third summary table, which creates the possibility for errors. PROPOSED REJECT. Commenter provides insufficient remedy. Management variables are spelled out where they apply and in registers. There is no need for a third summary table, which creates the possibility for errors.	Editorial
54	He, Xiang	Huawei Technologies	190	190.4	109	27	ER	Is there a subclause for PMA management variables? Clause 190 has both PCS and PMA, so the subclause title is better to clearly states whether this is for PCS or PMA, if this is not a PCS specific thing like "Training" or "LPI signaling". This also aligns better with the subclause title for 190.3.1 through 190.3.3.	Suggest to add a subclause for PMA management variables.		Editorial
55	He, Xiang	Huawei Technologies	190	190.3.6	88	33	ER		Change "Detailed functions and state diagrams" to "PCS detailed functions and state diagrams".	PROPOSED REJECT. Numbering makes the association clear. This is similar to numerous other clauses.	Editorial
56	He, Xiang	Huawei Technologies	190	190.4.9	103	19	ER	Clause 190 has both PCS and PMA, so the subclause title is better to clearly states whether this is for PCS or PMA. I also see the state diagrams for this subclause is for "PHY control", if these diagrams belong to the PMA subclause, and is part of PMA, please consider call them "PMA control state diagrams".	Change "Detailed functions and state diagrams" to "PMA detailed functions and state diagrams". Subsequently, consider to rename "PHY control state diagram" to "PMA state diagram" for the state diagram figures.	PROPOSED REJECT. Numbering makes the association clear. This is similar to numerous other clauses.	Editorial
57	Ran, Adee	Cisco Systems	FM	FM	1	33	E	"This adds"	Change to "This amendment adds"	PROPOSED ACCEPT.	EZ
58	Ran, Adee	Cisco Systems	1	1.3	21	7	E	There are no new normative references, so no change required in 1.3.	Remove subclause 1.3 from the amendment.	PROPOSED ACCEPT.	EZ

								The new definition FOLLOWER PHY incorrectly refers to 1.4.389 (which is "master") instead of 1.4.535 ("slave"). Also, the referenced definition says nothing about what "follower" is; the reader needs to read Annex K (which is informative) to find what this new term means. Also, existing definitions in 1.4 do not refer to other definitions by number but rather by name. For example, "1.4.204 Base Page: See: Base link codeword."			
								In this case the new term is synonymous to "Slave Physical Layer Device". in similar cases, the abbreviation "Syn:" is used (see 1.4.359 in-band signaling, 1.4.468 Physical Layer entity, 1.4.544 switch).			
59	Ran, Adee	Cisco Systems	1	1.4.341a	21	40	TR	Similarly for 1.4.371a "LEADER PHY" (where the reference isn't wrong, but the rest of the comment still applies). There are no abbreviations, so no change required in 1.5. The text of subclause 22.2 is included but there is no editorial instruction. I assume it is intended to be changed.	Change the definition in 1.4.341a to "syn: Slave Physical Layer Device. See also Annex K." Change the definition in 1.4.371a to "syn: Master Physical Layer Device. See also Annex K."	PROPOSED ACCEPT.	Editorial
60	Ran, Adee	Cisco Systems	1	1.5	22	33	E		Remove subclause 1.5 from the amendment.	PROPOSED ACCEPT.	EZ
61	Ran, Adee	Cisco Systems	22	22.2	23	5	E		Delete the text of 22.2.	PROPOSED ACCEPT.	EZ
62	Ran, Adee	Cisco Systems	45	45.2.1	25	17	E	The rows in the table seem to be new but are not underlined (except for the register address).	Format all new cells with underline.	PROPOSED ACCEPT.	EZ
63	Ran, Adee	Cisco Systems	45	45.2.1.236a.1	27	40	T	"NOTE—This operation may interrupt data communication" "may" is equivalent to "is allowed to"; but this sentence is within a NOTE so it should not allow or disallow anything. As an informative statement, you can say that a PMA reset _can_ interrupt data communication (or alternatively, _interrupts_ data communication). Also in the second instance of "may" in this NOTE. Also in the similar NOTES in 45.2.1.236a.3 and 45.2.3.75a.1.	Change "may" to "can", all instances in this NOTE and the ones in 45.2.1.236a.3 and 45.2.3.75a.1.	PROPOSED REJECT. Usage of may is proper here. Note reads correctly with "is allowed to" and is parallel to similar notes in IEEE Std 802.3	Editorial
64	Ran, Adee	Cisco Systems	45	45.2.1.236a.3	28	3	TR	"low-power ability" is not referenced anywhere in Clause 190 (although there is one instance of "low power mode", without a hyphen, in 190.4.1). Is it the same as "low-power idle" (part of EEE)? The definition of the Receive link status bit is inconsistent: when read as 0 it matches a "latching low" definition, but when read as 1 it just says "receive link is up". What if it is up now but was previously down? The rows in the table seem to be new but are not underlined.	If it is a separate function, it should be stated clearly to avoid confusion, and a specification of the behavior in this mode should be added in clause 190. If it is the LPI of EEE, please rename it or clarify in some other way. Change from "receive link is up" to "receive link is up continuously since the register was last read".	PROPOSED REJECT. This mode is described in nearly every PHY in 802.3 it is a low-power non-operational state. A change would make the reader question whether it was something different.	Management
65	Ran, Adee	Cisco Systems	45	45.2.1.236b.4	29	15	T			PROPOSED ACCEPT.	Management
66	Ran, Adee	Cisco Systems	45	45.2.3	30	22	E		Format all new cells with underline.	PROPOSED ACCEPT.	EZ
67	Ran, Adee	Cisco Systems	45	45.2.3.75b.2	32	3	E	"RS-FEC" is an overloaded term in 802.3. A reference to the specific subclause (as done in 45.2.3.75b.3) would be beneficial for the reader. Also in 45.2.3.75b.1, although "EEE" is more general.	Add a reference to 190.3.2 in 45.2.3.75b.2, and to 190.1.3.3 in 45.2.3.75b.1.	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comments 37 and 40.	RS-FEC

						A reference to the specific subclause that defines training for 10-BASE-TL1 would be beneficial for the reader.			PROPOSED ACCEPT IN PRINCIPLE. Add new final sentence to 45.2.3.75c (P32 L16) : "This register controls the PHY capability bits advertised in the infofield during 100BASE-T1L training (See 190.3.4.2.4)." Add new final sentence to 45.2.3.75d (P32 L48): "This register contains the values from the link partner advertised in the received infofield during 100BASE-T1L training (See 190.3.4.2.4)."		
68	Ran, Adee	Cisco Systems	45	45.2.3.75c	32	13	E	Also in 45.2.3.75d. "or Type G" seems to be newly inserted, but is only partially underlined.	Add references to 190.3.4 in both subclauses.	PMA	
69	Ran, Adee	Cisco Systems	104	104.5.7.4	39	33	E		Underline as necessary.	PROPOSED ACCEPT. EZ	
								The last sentence in the amended paragraph mentions only PDs, but the existing text in 104.6.2 says "The PI for Type E PSEs and PDs". I assume PSEs for Type E are out of scope of this amendment, so they should still be included; I assume also for type G, but this may be intentional?			
70	Ran, Adee	Cisco Systems	104	104.6.2	40	8	TR		Correct the text as necessary to address PSEs.	PROPOSED ACCEPT IN PRINCIPLE. (this text was amended by 802.3dd - the editing instruction neglects that. PSE's were excluded by 802.3dd insert "(as amended by IEEE Std 802.3dd-2022)" in editing instruction, to read: Change the first paragraph of 104.6.2 (as amended by IEEE Std 802.3dd-2022) as shown:	Editorial
								This subclause is titled "nomenclature" but it mostly talks about modes of operation, and does not seem to define a nomenclature, except for the constant N. These modes are initially described as modes of the PHY, but the last sentence says the PMA and MDI specifications are not affected; So it seems that these are modes of the PCS, not of the PHY. Also, the text describes encoding of TXD, TX_EN, and TX_ER, but does not mention the decoding and the RX signals. Also, the description of the modes is repeated in 190.1.3, and the meaning of N (and its two values) is repeated in 190.3.2.1. Everything seems to be written again in 190.3.2.3 (in a more complete form). This duplication is not helpful.	Either delete this subclause, or move this subclause to the PCS section, or merge its content into one of the other subclauses where the same information appears. If this subclause is retained, focus it on the nomenclature and values of N, clarify that it pertains specifically to the PCS, and delete the last sentence about PMA and MDI specifications		
71	Ran, Adee	Cisco Systems	190	190.1.1	44	36	T			PROPOSED ACCEPT IN PRINCIPLE. Delete subclause 190.1.1 in its entirety	Editorial
72	Ran, Adee	Cisco Systems	190	190.1.2	45	6	TR	Clause 4 specifies a CSMA-CD MAC (half duplex) but this PHY operates in full-duplex (as stated in 190.1.3). Shouldn't it be Annex 4A instead?	Change to Annex 4A and the appropriate title.	PROPOSED REJECT. CRG disagrees with the commenter. The Clause 4 MAC supports full duplex operation. Annex 4A is the simplified full duplex MAC.	Editorial
73	Ran, Adee	Cisco Systems	190	190.1.3	45	48	E	"Each PHY advertises the RS-FEC capability during training" is redundant, having been stated in the previous paragraph. Similarly for "Each PHY advertises the EEE capability during training" in the next paragraph.	Remove the redundancy.	PROPOSED ACCEPT IN PRINCIPLE. Delete "Each PHY advertises the RS-FEC capability during training."	Editorial
								"RS-FEC is enabled only if both PHYs advertise it" "Only if" suggests that it a necessary (but not required) condition. I assume if both advertise it, then it is enabled without other conditions (if not, it should be written clearly).	Change the quoted sentence to "If both PHYs advertise RS-FEC, it is enabled" Similarly in the next paragraph.	PROPOSED ACCEPT IN PRINCIPLE. Accomdated by comment 38.	RS-FEC
74	Ran, Adee	Cisco Systems	190	190.1.3	45	49	E	Similarly for "EEE is enabled only if both PHYs advertise it" in the next paragraph.			

75	Ran, Adee	Cisco Systems	190 190.1.3	45	51	TR	"RS-FEC is not compatible with all applications since it results in a significant increase in latency" This is not a normative statement, and it goes without saying (this PHY as a whole, or any PHY, or anything, isn't compatible with _all_ applications). Similarly for the statement "EEE is not compatible with all applications since it may result in a significant increase in latency and in latency variability" in the next paragraph.	Move these sentences into an informative NOTE, or delete them altogether.	PROPOSED ACCEPT IN PRINCIPLE. Delete ""RS-FEC is not compatible with all applications since it results in a significant increase in latency" and "EEE is not compatible with all applications since it may result in a significant increase in latency and in latency variability" in the next paragraph.	Editorial
76	Ran, Adee	Cisco Systems	190 190.1.3	46	34	T	"NOTE 2—Auto-Negotiation is mandatory " Can't have a normative requirement in a NOTE. Also, a sublayer stack diagram is not the place to state that something is mandatory - everything is mandatory unless defined otherwise.	Delete NOTE 2.	PROPOSED REJECT. The NOTE is a statement of fact. The requirement is in 190.6.1	Editorial
77	Ran, Adee	Cisco Systems	190 190.2.2.5.1	54	6	TR	For PMA_UNITDATA.indication, the possible values of rx_symb are not provided (unlike PMA_UNITDATA.request in 190.2.2.4.1). Are these the same set (ternary symbols)? Or is it a soft input for the PCS to decode? "PCS Transmit shall pass a vector of zeros at each symbol period to the PMA" PMA_UNITDATA.request sends a single symbol on each transfer, not a vector. Based on the possible values of tx_symb in 190.2.2.4.1, the value "0" should be sent.	Please clarify.	PROPOSED ACCEPT IN PRINCIPLE. Insert :The rx_symb parameter takes on one of the following values: {-1, +1} when the PHY is in training mode {-1, 0, +1} when the PHY is in idle mode or in normal operation	PMA
78	Ran, Adee	Cisco Systems	190 190.3.2	61	31	T	"adaptative" is never used in 802.3 (although it is apparently a dictionary word).	Change "a vector of zero" to "a value of 0".	PROPOSED ACCEPT.	EZ
79	Ran, Adee	Cisco Systems	190 190.3.2	61	46	E		change "adaptative" to "adaptive".	PROPOSED ACCEPT.	EZ
80	Ran, Adee	Cisco Systems	190 190.3.2	61	44	E	"Normal Inter-Frame" is used before it is defined, and the term is not self-explanatory. The reference to 190.3.2.4 isn't helpful because the term is not used there. I had to search the document to find that it is a symbol code (in 190.3.2.5.2) that has the mnemonic //, and then realize that // is indeed used in 190.3.2.5.2 (in Table 190–3). Please make it easier for the reader.	Change "Normal Inter-Frame" to "// symbols (see Table 190–3)". Or clarify in some other way.	PROPOSED ACCEPT IN PRINCIPLE. Change "PCS Transmit shall use ... 190.3.2.4 to represent Normal Inter-Frame (as defined in 190.3.2.5.2)."	Editorial
81	Ran, Adee	Cisco Systems	190 190.3.2.2	63	44	E	The commas in the NOTE are inconsistent. Also, NOTE in a figure should be formatted in sans serif font like all other content, to distinguish it from a NOTE in the clause text. This applies to some additional figures (e.g. Figure 190-11)	Delete the comma after "or a 64B/65B block". Change the NOTE to use sans serif font, in this figure and others.	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE.	EZ
82	Ran, Adee	Cisco Systems	190 190.3.2.4	65	19	TR	The value "-" for "previous transfer" in the 4th and 5th rows is not one of the categories defined in Table 190–1. "The control code indicates the type of the control symbol" Earlier in the same paragraph there is "control octet". "control symbol" appears twice, here and in the subsequent paragraph (line 41), while "control octet" appears 7 times.	Clarify or correct if necessary.	Add at the bottom of the table, "NOTE - and em-dash indicates that any value quaifies."	Editorial
83	Ran, Adee	Cisco Systems	190 190.3.2.4	67	31	T	I assume the terms "control symbol" and "control octet" mean the same thing? if not, more clarification is required instead of the suggested remedy.	Change "control symbol" to "control octet", twice.	PROPOSED ACCEPT IN PRINCIPLE. Globally change "control symbol" and "control character" to "control octet"	Editorial

						<p>"The bits of a transmitted or received block are labeled tx_coded<0:2N> and rx_coded<0:2N>"</p> <p>The notations tx_coded<0:2N> and rx_coded<0:2N> do not appear anywhere other than in this subclause.</p> <p>In 190.3.2.6 tx_coded has two indices, e.g., tx_coded<i><j>, where j is from 0 to 8N, so apparently tx_coded is an array of blocks; the size is different and the bit order is reversed, tx_coded<i><8N:0>.</p> <p>In 190.3.6.1.2 it is tx_coded<0:8N> (same order here but different size).</p>		<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>Change tx_coded<0:2N> to tx_coded<0:8N> (the block has 8N+1 bits).</p> <p>delete "and rx_coded<0:2N>" and "and rx_coded<0>" (there is no reference to rx_coded).</p> <p>In 190.3.2.6.1, change "tx_coded<i><8N:0> is the i-th (8N)B/(8N+1)B block" to "tx_coded<i><0:8N> is the i-th (8N)B/(8N+1)B block"</p>	
						<p>I assume the size is 8N+1, and the order should be consistent; MSB on the left is more common.</p>	<p>Change to tx_coded<8N:0> and rx_coded<8N:0>. Make the bit order consistent across the clause.</p>	<p>Txcoded<0:8N>> is correct with the nomenclature. Consider whether other entries are incorrect (in reverse order). One example is tx_group<120N+15:0> in Figure 190-4.</p>	
84	Ran, Adee	Cisco Systems	190 190.3.2.3	64	16 TR	<p>Note that rx_coded doesn't appear anywhere else. Should it be rx_mii?</p> <p>"The first step converts two MII transfers at a time into a control symbol indication, TS, and an octet, TOCT"</p> <p>The mnemonic "TOCT" can be understood to mean "transmitted octet" (and there is a corresponding ROCT in Table 190–6). But "TS" does not seem to convey the meaning of this value; "CS" (for "control symbol") or "CSI" ("indicator") would be easier to understand.</p>	<p>Change rx_coded to whatever it should be.</p>	<p>Discuss with comment 274 which might re-introduce rx_coded.</p>	PCS
85	Ran, Adee	Cisco Systems	190 190.3.2.4	64	30 E		<p>Rename "TS" to "CS" (or "CSI") across the clause, including its variants in the Python code.</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>TFTD. Expand and revise the mnemonics.</p>	Editorial
86	Ran, Adee	Cisco Systems	190 190.3.2.5	69	3 T	<p>"A subset of control characters defined at the MII is supported by the 100BASE-T1L PCS"</p> <p>Which control characters are defined at the MII? Which subset is supported? And what about the other characters?</p> <p>Assuming there are only a few non-supported characters, stating it as "The 100BASE-T1L PCS supports all characters defined at the MII (See <reference>) except for <list of unsupported characters>" would be more readable.</p>	<p>Add a reference to the "control characters defined at the MII", and list the ones that are not supported.</p> <p>Consider rephrasing as suggested in the comment.</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>Change "A subset of control characters defined at the MII is supported by the 100BASE-T1L PCS." to "The 100BASE-T1L PCS supports the following control characters defined at the MII (see 22.2.2 and Table 22-1 for MII definitions): Normal Inter-Frame, Assert LPI, Assert remote fault, Start, Terminate, and Transmit Error Propagation. Other encodings are not defined for the 100BASE-T1L PCS."</p>	PCS
87	Ran, Adee	Cisco Systems	190 190.2.2.13.1	57	44 TR	<p>Is "control character" (here, also used in 190.3.2.2 and 190.3.2.3) identical to "control octet" (used in 190.3.2.4, 11 times)? Neither of these terms seems to be defined.</p>	<p>If the terms are identical, please use one term consistently.</p> <p>If not, please add text to clarify the difference.</p> <p>Preferably, add a definition or a reference to an existing one.</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>Accomodated by comment 83</p> <p>PROPOSED ACCEPT IN PRINCIPLE.</p>	Editorial
88	Ran, Adee	Cisco Systems	190 190.3.2.5	69	7 T	<p>"may be inferred"</p> <p>This is not just permitted behavior.</p>	<p>Change to "is inferred".</p>	<p>Change "may be inferred" to "can be inferred" (note it is not always inferred)</p> <p>PROPOSED REJECT.</p>	Editorial
89	Ran, Adee	Cisco Systems	190 190.3.2.5.7	69	49 T	<p>There are two instances of "may" in this subclause, but it does not seem to be just permitted behavior (at least for the second one).</p>	<p>Change the second instance "the RS may request" to "the RS requests".</p> <p>Consider changing the first instance to "the RS can require".</p>	<p>Text is correct - the RS is permitted to require that the PHY deliberately corrupt a frame, AND, in this case, the RS is permitted to request Transmit Error Propagation.</p>	Editorial
90	Ran, Adee	Cisco Systems	190 190.3.2.7	70	53 E	<p>Inline equation is small</p>	<p>Increase the equation size</p>	<p>PROPOSED ACCEPT.</p>	EZ
91	Ran, Adee	Cisco Systems	190 190.3.2.7	71	36 E	<p>Parentheses should not be in italics</p> <p>In "pi,0 is the first bit transmitted" the "0" should be</p>	<p>Remove italics from parentheses, 3 times in this line, also 4 more instances on this page, and other places.</p>	<p>PROPOSED ACCEPT.</p>	EZ
92	Ran, Adee	Cisco Systems	190 190.3.2.7	71	43 E	<p>a subscript</p>	<p>Change to subscript</p>	<p>PROPOSED ACCEPT.</p>	EZ

						"as in Clause 40" Reference is not specific enough. I assume the intent is 40.3.1.3.2, which contains the same equations for Sy_n and Sx_n, but it does not seem to be exactly the same for Sg_n. For Sy_n and Sx_n, either refer to an existing specification or note (informatively) that it is the same as an existing one. The paragraph starting with "A balanced code-group" seems to have a smaller font size than the rest of the text.					PROPOSED ACCEPT IN PRINCIPLE. Change "as in Clause 40" to "as specified in 40.3.1.3.2". Add at P73 L25 (after paragraph): "NOTE—The specification for Sy_n and Sx_n is identical to the one in 40.3.1.3.2".	
93	Ran, Adee	Cisco Systems	190	190.3.2.8	73	23	ER		Either change to "as specified in 40.3.1.3.2", or delete this phrase and add a paragraph "NOTE—The specification for Sy_n and Sx_n is identical to the one in 40.3.1.3.2".			PCS
94	Ran, Adee	Cisco Systems	190	190.3.2.11	76	36	E	I interpret the symbol "^" (used in many expressions) as XOR, but this is not stated anywhere. In Equation (190–6) the "+" symbol is used for the same purpose. In 190.1.6.1 it is stated that "A plus symbol within a circle denotes a bit-wise exclusive OR (XOR) operation"; using three different symbols for the same operation is confusing.	Correct the formatting.		PROPOSED ACCEPT.	EZ
95	Ran, Adee	Cisco Systems	190	190.3.2.9	73	30	E		Either change "^" to the circled-plus symbol (Unicode U+2295, ?) or (preferably) add "the character ^ denotes bitwise XOR operation" prior to the first expression. Change "using the following generator polynomial: <equation>" to "using the generator polynomial g(x)=x^3+x^8".		PROPOSED REJECT. The symbol ^ is used extensively to represent bitwise XOR in IEEE Std 802.3-2022, in multiple clauses, without need for further definition.	Editorial
96	Ran, Adee	Cisco Systems	190	190.3.2.9	73	36	E	Equation (190–6) is not referenced anywhere; it does not need to be numbered.	(^ denotes superscript).		PROPOSED ACCEPT.	EZ
								In the equation for SX_n there is an unusual asterisk-like character (?) that seems to denote logical AND, and "+" seems to denote logical OR, although in other expressions in this subclause (for DS_n and RD_n) it seems to denote addition. This is confusing.				
								Note that Table 21–1 specifies usage of the unusual character as "Binary AND" but it is specific for state diagrams. Also, similar expressions in 40.3.1.3.4 use "and", and the state diagrams in clause 190 use the regular asterisk (which is preferable).	Add a sentence after the expression for DS_n: "where + denotes arithmetic addition". In the expression for SX_n, replace the symbols with the words "AND" and "OR". Add parentheses to avoid ambiguity.			
97	Ran, Adee	Cisco Systems	190	190.3.2.11	76	32	T	Also in 190.3.4.1 and 190.3.4.3 "RS" is used elsewhere as an acronym of "reconciliation sublayer".	Implement similar changes in the other mentioned expressions.		PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 262	Editorial
98	Ran, Adee	Cisco Systems	190	190.3.3	78	42	E		Change "RS" to "RS-FEC" or to "Reed-Solomon".		PROPOSED ACCEPT IN PRINCIPLE. Change "RS" to "RS-FEC"	EZ
								"may use"... "to determine..." "and generates" - syntax mismatch, and standard language mismatch - is "generates accordingly" optional or required?	Change "and generates" to "and to generate". Alternatively, rephrase to make the "generate" part mandatory and the rest optional.		PROPOSED REJECT. Text is clear. (several clauses in 802.3 use this same text). What is used to make a determination is optional, but after it makes a determination, the pcs_status is generated according to the determination.	Editorial
99	Ran, Adee	Cisco Systems	190	190.3.3	78	43	T	Similarly in 190.4.3 for the PMA receive function.	Apply similarly in 190.4.3.			
								Figure 190-7 includes text with unreadably small font. Note that the terms "LL frame" and "6-tuple" in the small-print labels are not defined anywhere. The numbers appear in different font than the rest of the text, and the vertical alignment of the numbers in the first row is inconsistent.	Modify the figure to use at most 8-point font as in the style manual. This can be achieved by using vertical text and/or separating the "LL frame" and "6-tuple" labels into a detail callout attached to the first RS-FEC frame. Change the numbers to sans serif font and align the first row correctly.		PROPOSED ACCEPT IN PRINCIPLE. Consider breaking figure into two rows (one with 0 to 15 and the second with 16 to 31) and increasing font size).	Editorial
100	Ran, Adee	Cisco Systems	190	190.3.4.2	81	4	E	Labels in Figure 190-8 are in "Times New Roman" font	Change to sans serif font		PROPOSED ACCEPT.	EZ
101	Ran, Adee	Cisco Systems	190	190.3.4.2	82	3	E					

							The equation for FTFC includes the symbol ">>" which is undefined. I assume it is a right-shift operator, but if that's the case, it's applied to the result of mod(), which is a number. So why not just divide by 16.				
102	Ran, Adee	Cisco Systems	190	190.3.4.2.3	83	20	T		Change ">> 4" to "/" 16"	PROPOSED ACCEPT.	EZ
103	Ran, Adee	Cisco Systems	190	190.3.4.2.4	83	41	E	training register	MDIO training register	PROPOSED ACCEPT.	EZ
										PROPOSED ACCEPT IN PRINCIPLE. Delete the first two sentences of the paragraph that begins "Transmission of the sleep signal may start..." P77 L51 through P78 L1. Add to the end of the paragraph. " See 190.3.5.1 for synchronization of LPI signals, including when sleep and alert may start."	
104	Ran, Adee	Cisco Systems	190	190.3.2.12	77	51	E	"Transmission of the sleep signal may start"... "that follows the refresh period." This text is repeated in 190.3.5.1	Consider deleting one of the duplicates.		Editorial
105	Ran, Adee	Cisco Systems	190	190.3.6.1.1	88	39	E	The element ordering in E_MII_R<0:1><0:5> is inconsistent with the bit ordering in RXD<3:0>. Similarly in many other constants and variables. RFRX_CNT_LIMIT result in hi_rfer being asserted when the RS-FEC block error ratio is about 16/88 or about 18% (assuming uncorrectable codewords occur randomly). This means 18% of the traffic can be lost (frame loss ratio higher than 1e-1!) without asserting hi_rfer, which makes it a very crude indication (the link will likley become useless at this performance or even lower BER) and does not match the stated BER/FLR requirements in 190.5.5.1. Allowing a link to operate with such high error probability would raise MTTFPA concerns, because there is a non-negligible probability (with this codeword error probability and simple error model assumptions, estimated as ~0.2%) that a codeword with more than 3 errors is not detected as uncorrectable, but instead miscorrected to create 2t=6 symbol errors. It practically becomes an indication of a dropped link, but this should already be detected by other means (pcs_status, implementation dependent) for the case where RS-FEC is not available.	Consider using a consistent order. Increase RFRX_CNT_LIMIT to create a ratio based on the expected worst-case performance (e.g. frame loss ratio). For example, assuming the maximum allowed frame loss ratio is 1e-6 (very relaxed compared to about 1e-10 in BASE-R PHYs), RFRX_CNT_LIMIT should be RFER_CNT_LIMIT*1e6 or about 2^24. If the current value is retained, add a NOTE stating that with random error assumptions, high_rfer will be asserted at a codeword error ratio of approximately 18% or above. (if the value is changed, add the note with the resulting probability).	PROPOSED REJECT. Bit ordering needs to be consistent with the bit ordering of rx_mii, not RXD<3:0>	Editorial
106	Ran, Adee	Cisco Systems	190	190.3.6.1.1	89	38	TR	Note that the PCS in clause 119 and similar ones asserts loss of alignment (and			RS-FEC

								There is no specification of the RS-FEC decoder correction capability. I assume there is an expectation that the decoder actually corrects errors, but this is not written anywhere.			
								with the current specifications, the decoder could just ignore the parity symbols and extract the payload, and this would be compliant. Or it could just mark codewords as invalid if any error is detected (nonzero syndrome), never correcting anything. This would have very low latency but it's not what people would expect.		PROPOSED REJECT. CRG Disagrees with the commenter. RS-FEC specifications integral to the PCS of BASE-T1 PHYs are different from those in high-speed PHYs where RS-FEC has been defined as a separate sublayer. Performance is integrated into the receiver. This has a long history with 1000BASE-T, MultiGBASE-T, and has continued in 1000BASE-T1 and MultiGBASE-T1 PHYs. Separate specification from the receiver performance is not required because the sublayer cannot be separated from the PHY.	RS-FEC
107	Ran, Adee	Cisco Systems	190	190.3.3.2	79	22	TR	The code specified in 190.3.2.7 has 2t=128-122=6 so a decoder is expected to be able to correct up to t=3 symbol errors (with 8-bit symbols).	Add a requirement that the RS-FEC decoder shall be able to correct up to t=3 symbol errors (the text in 119.2.5.3 can be used as a reference).		
								The NOTE in Figure 190-12 reads as a mandatory requirement, in violation of the style manual (18.1): "Notes provide additional information to assist the reader with a particular passage and shall not include mandatory requirements".			
								Similarly in Figure 190-15, but with RS-FEC instead of EEE.	Change the note to read "NOTE—This state diagram is only required when EEE is enabled for the link".	PROPOSED REJECT. The note is not a requirement, it does not contain a shall. It reflects a requirement elsewhere in the text.	
108	Ran, Adee	Cisco Systems	190	190.3.6.2	95	47	E	The suggested remedy is based on notes in other state diagrams.	Apply the corresponding change (with RS-FEC) in Figure 190-15.		Editorial
								The NOTE in Figure 190-14 reads as a mandatory requirement, in violation of the style manual (18.1): "Notes provide additional information to assist the reader with a particular passage and shall not include mandatory requirements".			
								Also, this is part b of the PCS receive state diagram; the state diagram is always mandatory, only the states in this part are conditional.		PROPOSED REJECT. The note is not a requirement, it does not contain a shall. It reflects a requirement elsewhere in the text.	
109	Ran, Adee	Cisco Systems	190	190.3.6.2	97	32	E	The suggested remedy is based on notes in other state diagrams.	Change the note to read "NOTE—Signals and functions shown with dashed lines are only required when EEE is enabled for the link".	Additionally, there is only a dashed line used around the entire figure, no dashed lines or separate boxes, so the proposed note would be misleading, whereas the existing note is clear.	Editorial
110	Ran, Adee	Cisco Systems	190	190.3.7	98	1	E	The subclause "PCS management" has no content.	Delete the heading.	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by Comment 234.	Management
										PROPOSED ACCEPT IN PRINCIPLE. Move "Under normal circumstances the 100BASE-T1L PHY Control state diagram takes no longer than 100 ms to enter the SEND_IDLE_OR_DATA state after exiting from reset or low power mode (see Figure 190–19). However, in conditions of high noise, more than one attempt may be required to establish a valid link." (P100 L9 to 13) to 190.3.4 PMA training (currently empty top-level header).	PMA
111	Ran, Adee	Cisco Systems	190	190.4.1	100	10	E	The sentences starting with "Under normal circumstances..." (describing the time to link) are irrelevant for the PMA reset function; the time to link is measured starting from the exit from reset.	Move the text to a better location.		
								A better location for these (informative?) statements would be somewhere below 190.3.4 or in 190.4.4.2.			
112	Ran, Adee	Cisco Systems	190	190.4.2	100	23	E	Incorrect cross-reference: the jitter requirements are in 190.5.4.3.	Change 190.5.4.4 to 190.5.4.3, twice in this paragraph.	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE.	EZ
								Some variables communicated through primitives are called "variable" while others are called "parameter".		TFTD.	
113	Ran, Adee	Cisco Systems	190	190.4.9.1.1	103	29	E		Unify the definitions across this subclause.	Needs specific remedy	State Diagrams

						The definition of pam3_detected is repetitive, unnecessarily complicated, and the description of FALSE is badly phrased.	Change to "TRUE: a compatible signal detected", "FALSE: a compatible signal is not detected".	PROPOSED ACCEPT.	EZ	
114	Ran, Adee	Cisco Systems	190	190.4.9.1.1	103	42 E	FALSE is badly phrased.			
115	Ran, Adee	Cisco Systems	190	190.4.9.1.1	104	30 E	Stray colon after "timing_locked"	Delete it	PROPOSED ACCEPT.	EZ
116	Ran, Adee	Cisco Systems	190	190.4.9.1.1	104	43 E	Small numbers in the text should be spelled out	Change "3" to "three", twice, and change "3rd" to "last"	PROPOSED ACCEPT.	EZ
								PROPOSED ACCEPT IN PRINCIPLE. At P104 L41 replace definition of tx_info_countdown_done with "Variable set by the PHY Control function to indicate whether the countdown is complete. Values: TRUE: Countdown has been completed, i.e., transmission of the third of the three training frames associated with the countdown has begune. FALSE: The transmission of the training frames is in process.		
								At P105 L10, add an assignment of FALSE to the tx_info_countdown_done variable in the INFO_COUNTDOWN state (see below): tx_info_countdown_done <= FALSE		
						The definitions of other variables either include a list of values and meanings (e.g. in ready_to_transmit) or a reference to a subclause that contains such a list (e.g. in rem_phy_idle). Here (tx_info_countdown_done) the meaning is not described, only the conditions when each value is assigned are listed (which is redundant, since the state diagrams already specifies them). Similarly for lpi_refresh_detect.	For both variables, write the possible values (FALSE and TRUE) and their meaning, as in other variables. Add the conditions for setting if necessary.	for lpi_refresh_detect At P105 L3, replace definition of lpi_refresh_detect with "Variable to indicate whether the receiver has reliably detected refresh signaling while the receive function is in LPI receive mode." Values: TRUE: Refresh signaling has been detected. FALSE: all other times.	State Diagrams	
117	Ran, Adee	Cisco Systems	190	190.4.9.1.1	104	43 E				
						Figure 190–20 (Link Monitor state diagram) is equivalent to an assignment of link_status = FAIL if (link_control=DISABLE) or (pma_reset) or (tx_mode=SEND_N), or OK otherwise. The text in 190.4.5 (Link Monitor function) repeats the definition of the state diagram in too many words, making it look more complicated than it is.	Consider replacing the state diagram with an assignment statement in 190.4.5 and simplifying the text description.	PROPOSED REJECT. TFTD - consider whether simplification of the text is appropriate. Commenter provides insufficient remedy. Link Monitor state diagrams are present in most similar clauses (BASE-T and BASE-T1) in IEEE Std 802.3. Changing the format is unusual.	Editorial	
118	Ran, Adee	Cisco Systems	190	190.4.9.2	108	31 T				
						The entry condition to DISABLE_TRANSMITTER "link_control = DISABLE + pma_reset" is ambiguous; The state diagram conventions in 21.5 do not assign operator precedence, but has parentheses to indicate precedence. In this case, the reader could deduce the precedence because DISABLE is not a Boolean value, but it is not friendly. Note that parentheses are used in other cases (e.g. in this figure, the transition to INFO_EXCHANGE). This should be done consistently. A similar issue exists in other diagrams and other conditions.	Change the entry condition to "(link_control = DISABLE) + pma_reset" in this case. Add parentheses similarly in all cases that may appear ambiguous.	PROPOSED ACCEPT IN PRINCIPLE. Change entry condition to DISABLE_TRANSMITTER to add parentheses around link_control = DISABLE Editorial license to add parentheses in other cases where there is a conditional expression ("=", "<", ">", etc.) followed by a logical operation, as appropriate.	Editorial	
119	Ran, Adee	Cisco Systems	190	190.4.9.2	106	3 T				
							One solution is to move 190.5 to be a subclause under 190.4 (possibly grouping the existing subclauses under "Functional specifications"). An alternative is to change the title of 190.4 from "Physical Medium Attachment (PMA) sublayer" to "PMA functional specifications" (this title is subject of another comment).	PROPOSED REJECT. PMA electrical specifications are a separate subsection in most (if not every) BASE-T and BASE-T1 clause of IEEE Std 802.3. Making it different here would confuse the reader familiar with similar technologies in 802.3		
120	Ran, Adee	Cisco Systems	190	190.5.	106	29 E	PMA electrical specifications should be part of the PMA sublayer specification.			Editorial

121	Ran, Adee	Cisco Systems	190 190.3.	60	1 E	<p>The title of 190.3 is " Physical Coding Sublayer (PCS)".</p> <p>The title of 190.4 is "Physical Medium Attachment (PMA) sublayer".</p> <p>The acronyms PMA and PCS have already been expanded in their first appearance in this clause (in 190.1), and need not be expanded again.</p>	<p>Change the titles to "PCS specifications" and "PMA specifications".</p>	<p>PROPOSED REJECT.</p> <p>Structure of clause 190 aligns with all other BASE-T and BASE-T1 clauses in the existing titles.</p> <p>Editorial</p>
122	Ran, Adee	Cisco Systems	190 190.5.1	109	33 T	<p>This subclause says nothing about the EMC tests, using convoluted sentences. (What does "during the test" and "specified device"?)</p>	<p>Delete the subclause.</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>Change "Applications for the specified device" to "Expected applications for 100BASE-T1L"</p> <p>Change "during the test" to "during EMC test conditions"</p> <p>TFTD - are there additional specifications, e.g., cable clamp or conducted immunity that we should require here? perhaps for use with the RS-FEC?</p> <p>EMC</p>
123	Ran, Adee	Cisco Systems	190 190.5.2	109	43 TR	<p>I assumed that all test modes described are normatively required, but then realized that the even-numbered modes are optional, conditional of "increased transmit level" which is not defined anywhere. And it is not explicitly stated that the odd-numbered test modes are normatively required. The RS-FEC support adds another level of complexity.</p> <p>It looks like there are actually 2 PMA-specific test modes (1 and 3) and 5 PMA+PCS test modes (5, 7, 9, 11, and 13; RS-FEC enable or disable is purely a PCS control), plus a bit that controls the transmit level. I assume there are reasons to define the test modes this way, and the suggested remedy is based on that (but a cleaner scheme separating the PCS test modes from the PMA test modes should be considered).</p> <p>The test modes already include numbers. The list letters are unnecessary.</p>	<p>Change from</p> <p>"The test modes described in this subclause are provided to allow testing of the transmitter"</p> <p>"The test modes described in this subclause are provided to allow testing of the transmitter. Test modes 1, 3, 5, 7, and 11 shall be provided by all PHYs. Test modes 2, 4, 6, and 12 shall be provided if the PMA supports the optional increased transmit level (see <reference>). Test modes 9, 10, 13, and 14 shall be provided if the PCS supports RS-FEC (see <reference>)".</p> <p>Use references to the subclause that specify the increased transmit level and RS-FEC as options (are there MDIO bits to indicate support?), or add new subclauses if there are no such specifications.</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>TFTD - after resolution of increased transmit level comments.</p> <p>(avoid adding a new overarching requirement, as the proposed language would, PMA and PCS are combined in BASE-T and BASE-T1 PHYs, making all test modes PHY test modes.)</p> <p>Resolve references to RS-FEC and definition of increased transmit level after considering resolution to comments on control of those two features.</p> <p>Test Modes</p>
124	Ran, Adee	Cisco Systems	190 190.5.2	109	49 E		<p>Change from lettered list to dashed list.</p>	<p>PROPOSED ACCEPT.</p> <p>EZ</p>
125	Ran, Adee	Cisco Systems	190 190.5.4.4	113	26 TR	<p>"For the 1.0 Vpp operating mode, in test mode 7 <...> the transmit power shall be 1.0 ± 1.2 dBm"</p>		
126	Ran, Adee	Cisco Systems	190 190.5.5.3	116	21 E	<p>1 V PtP (specified in 190.5.4.1) with PAM2 modulation on a 100 Ohm load delivers $V^2/R=1^2/100 = 0.01\text{ W} = 10\text{ mW}$; this is 10 dBm prior to pulse shaping. The PSD mask in figure 190-26 shows a mild low-pass response with about 4 dB attenuation at the Nyquist frequency (40 MHz) - not a lot more than square pulse shaping - how does that get anywhere near 1 dBm?</p> <p>I may have got something completely wrong but it seems that the voltage and power specs don't match.</p>	<p>Similarly for the 2.0 Vpp mode (which should be just 6 dB higher - why is it 7 dB?)</p> <p>"to these noise sources"</p> <p>If I'm not wrong - update whatever is necessary. (If I am wrong but it's not easy to explain why - consider adding a clarifying NOTE).</p> <p>"to this noise source"</p>	<p>PROPOSED REJECT.</p> <p>Commenter makes an error in his calculation and uses 1 Vpeak, PAM2 not 1Vpp PAM3 (0.5Vp, with 1.76dB PAR). $V^2/100\text{ohm} = 2.5\text{mW}$ (4dBm) minus 1.76dB PAR = 2.2 dBm, which fits the upper end fo the transmit power limit. The lower limit is for pulse shaping. Note that the difference between a 1st order nyquist filter and unfiltered pulse is > 1 dB...</p> <p>PMA Electrical</p>
127	Ran, Adee	Cisco Systems	190 190.5.5.3	116	23 E	<p>"This specification
may be considered"</p>	<p>Remove the break</p>	<p>PROPOSED ACCEPT.</p> <p>EZ</p>

128	Ran, Adee	Cisco Systems	190 190.5.5.3	116	41	TR	The NOTE includes an allowed ("may") modification the test conditions; this is not informative text.	Move this paragraph to normal subclause text. If desired, add a NOTE to explain the motivation for this allowance (e.g. "this allowance is provided to address limitations in noise generators").	PROPOSED ACCEPT IN PRINCIPLE. Change "may be adapted" in the NOTE below figure 190-28 to "should be adapted". (the note should be a recommendation of what to do, not a permission)	PMA Electrical
129	Ran, Adee	Cisco Systems	190 190.5.5.3	116	34	T	"< 0.5 m" - between which points? The subclause text does not address this requirement at all.	Add appropriate subclause text and make the relevant points to the figure.	PROPOSED ACCEPT IN PRINCIPLE. Add "from noise coupling fixture to the connection to the cabling" by 0.5m in Figure 190-28.	PMA Electrical
130	Ran, Adee	Cisco Systems	190 190.5.6	116	45	E	The subclause "PMA local loopback" has no content.	Delete the heading.	PROPOSED ACCEPT IN PRINCIPLE. TFTD with Comment 218 - is this a hanging header or missing content?	PMA
131	Ran, Adee	Cisco Systems	190 190.5.2	109	45	E	"The test modes can be enabled by setting bits 1.2302.15:12 <...> If MDIO is not implemented, a similar functionality shall be provided by equivalent means" This requirement is covered by the text of 190.6 and need not be repeated. It does not appear in other subclauses that mention MDIO (190.4.2, 190.4.3). [auto-negotiation is used] "To negotiate EEE capabilities as specified in 190.1.3.3." But per 190.1.3.3 EEE capability are negotiated in InfoField as part of the training - which is after auto-negotiation.	Change to "If the MDIO interface is implemented, the test modes can be enabled by setting bits 1.2302.15:12 <...>"	PROPOSED ACCEPT.	EZ
132	Ran, Adee	Cisco Systems	190 190.6.1	117	15	TR	[auto-negotiation is used] "To negotiate the low <...> and high <...> operating modes ..." How is that done? (I reckon Table 98B–1 has something to do with it but what are the rules for the negotiation? There should probably be a new subclause in clause 98)	Delete item d)	PROPOSED ACCEPT.	EZ
133	Ran, Adee	Cisco Systems	190 190.6.1	117	16	TR	The placement of 190.6.1 "Support for Auto-negotiation" under 190.6 "Management interface" seems inappropriate. AN and MDIO are completely different functions, one is optional and one is mandatory. "and shall be capable of operating as LEADER or FOLLOWER"	Provide a reference to the subclause that contains the information (add a new one if necessary). Promote 190.6.1 to become 190.7, and keep the existing 190.6.2 as a subclause below it.	PROPOSED ACCEPT IN PRINCIPLE. Add appropriate reference after resolution of Increased Transmit Level comments. PROPOSED REJECT. MDIO is optional, but the ubiquitous management interface is mandatory. Auto-Negotiation is found under the management section in all BASE-T and BASE-T1 PHYs which use it. (see e.g., 40.5, 55.6, or 97.8) PROPOSED REJECT.	Reduced TX level
134	Ran, Adee	Cisco Systems	190 190.6.1	117	1	E	This requirement seems to belong in 190.6.2.	Move this requirement to 190.6.2	No need to change...	Editorial
135	Ran, Adee	Cisco Systems	190 190.6.1	117	3	E				Editorial
136	Ran, Adee	Cisco Systems	190 190.6.2	117	22	TR	"One PHY should be configured as LEADER and one PHY should be configured as FOLLOWER" This is not just a recommendation ("should"); it is an unavoidable situation if proper operation is assumed, as described in the next paragraph.	Change to "For successful operation of a link between two PHYs, one PHY must be configured as LEADER and the other as FOLLOWER". Move this sentence to the second paragraph before "In the case where <...>".	PROPOSED REJECT. The configuration is not necessarily a forced configuration. It may be resolved as a preference in auto-negotiation, according to Table 98-4. This same language and technique has been used successfully for over 20 years (including 1000BASE-T) and resulting in successful BASE-T PHY links without misunderstanding.	Management
137	Ran, Adee	Cisco Systems	190 190.7.1.1	120	6	TR	"Each 100BASE-T1L link segment" - within what set of segments? I initially interpreted it as "each segment between connectors", but based on the text in 190.7.1.4.2 I suspect the intent is each differential pair within a bundle of differential pairs (as in a CAT6 cable). But I'm not sure this is relevant in general.	If there is no special meaning to "each", change "each link segment" to "a link segment". Otherwise, clarify what "each" refers to (within what set of segments?) Apply in all instances of "each 100BASE-T1L link segment".	PROPOSED ACCEPT IN PRINCIPLE. Change "each 100BASE-T1L segment" to "the link segment" in 190.7.1.2, 190.7.1.4.1 and 190.7.1.4.2 (capitalize as appropriate). Note - the language of "each" seems to have slipped over from multi-pair BASE-T to single-pair ethernet in clause 97, 149, and 165. Commenter may consider maintenance.	Link Segment

								"The term "link segment" used in this clause refers to a single balanced pair of conductors operating in full duplex." This reads like a length of cable, and connectors are not mentioned; but the next paragraph talks about "supports up to five in-line connectors". It is unclear whether a channel comprising several cables with connectors between them is considered one link segment or multiple link segments. Also I think "operating in full duplex" is a property of the PHY (and the protocol used), not of the link segment.		PROPOSED REJECT. Link Segment is defined in 1.4. The medium is capable of full-duplex conduction of signals. It doesn't have one-way amplifiers or directional couplers in it. This same language has been used successfully for over 20 years (including 1000BASE-T) and resulting in successful BASE-T PHY links without misunderstanding.	
138	Ran, Adee	Cisco Systems	190	190.7.	117	35	TR		Please specify more clearly what a link segment is. A figure showing the boundaries of the link segment in a connectorized channel would help.		Link Segment
139	Ran, Adee	Cisco Systems	190	190.7.1.4.1	117	6	T	"Each 100BASE-T1L segment"	Delete "operating in full duplex". "Each 100BASE-T1L link segment"	PROPOSED ACCEPT.	EZ
140	Graber, Steffen	Pepperl+Fuchs SE	190	190.3.2.2	63	4	E	"(2N)th transfer" needs to be placed on top of the right nibble block (the left block where the text is actually placed would be the "(2N - 1)th transfer") Joint dot between the two arrows for the signal "PAM2/PAM3 select" is missing, related to the linebreak in "PAM2/PAM3 select" text the "/" should be at the end of "PAM2" and not the beginning of "PAM3".	Place "(2N)th transfer" on top of the right nibble block.	PROPOSED ACCEPT	EZ
141	Graber, Steffen	Pepperl+Fuchs SE	190	190.3.2.2	64	32	E		Add joint dot and change position of "/" as per comment.	PROPOSED ACCEPT.	EZ
142	Graber, Steffen	Pepperl+Fuchs SE	190	190.3.2.2	64	11	E	Font size differs between "Output of" and "block encoder".	Align font size.	It should be on p63	EZ
143	Graber, Steffen	Pepperl+Fuchs SE	104	104	38	1	T	A common PoDL Power Type for 10BASE-T1L and 100BASE-T1L is suggested, to allow the operation of both PHYs using the same PoDL powering type (similar as Power Type C for 100BASE-T1 and 1000BASE-T1). See document "Clause 104 Changes for Type H PSE or PD.pdf" for suggested text to add a Type H PSE/PD.	If agreed, add text as suggested by comment. If not agreed, add at least the changes marked in blue in the referenced document related to Power Type G, which have been missed by previous text provided for Clause 104 and are needed for consistency: "Modify entry of the Powered Device (PD) table in Clause 104.9.4.3 in line PD24" and "Modify entry COMEL2 in table in Clause 104.9.4.4" for Type G. Increase the distance between "PMA SERVICE" and "INTERFACE" to align with "MEDIA INDEPENDENT INTERFACE (MII)" at the top of the figure.	PROPOSED ACCEPT IN PRINCIPLE. TFTD with presentation.	Power
144	Maguire, Valerie	Copperopolis; aff'l w/ C	190	190.3	60	38	E	Cramped text.		PROPOSED ACCEPT.	EZ
145	Maguire, Valerie	Copperopolis; aff'l w/ C	190	190.3.2.7	71	50	E	Prefer not to see 'x' just floating here.	Insert non-breaking space between "of" and "x".	PROPOSED ACCEPT.	EZ
146	Maguire, Valerie	Copperopolis; aff'l w/ C	190	190.3.4.2	82	1	E	Paragraph formatting error.	Set the paragraph on line 1 to "start anywhere" so it will being right after Figure 190-8. Grant Editor's license to adjust placement of remaining paragraphs in the clause as needed so the paragraphs flow smoothly.	PROPOSED ACCEPT.	EZ
147	Maguire, Valerie	Copperopolis; aff'l w/ C	190	190.3.4.2.5	84	10	E	Prefer not to see 'S0' just floating here.	Insert non-breaking space between "value" and "S0".	PROPOSED ACCEPT.	EZ
148	Maguire, Valerie	Copperopolis; aff'l w/ C	1	1.3	21	4	E	There are no normative references.	Delete clause 1.3 header and contents.	PROPOSED ACCEPT.	EZ
149	Maguire, Valerie	Copperopolis; aff'l w/ C	1	1.5	22	30	E	There are no abbreviations.	Delete clause 1.5 header and contents.	PROPOSED ACCEPT.	EZ
150	Maguire, Valerie	Copperopolis; aff'l w/ C	98	98.2.1	36	14	E	Missing underline for added space	Extend underline to include the space after "or 100BASE-T1L,".	PROPOSED ACCEPT.	EZ
151	Maguire, Valerie	Copperopolis; aff'l w/ C	98	98.2.1	36	15	E	Missing underline for added space	Extend underline to include the space after "and 100BASE-T1L".	PROPOSED ACCEPT.	EZ
152	Maguire, Valerie	Copperopolis; aff'l w/ C	98	98.5.1	36	30	E	Existing space marked with underline	Remove the underline after, "register bit 1.2300.11,".	PROPOSED ACCEPT.	EZ
153	Maguire, Valerie	Copperopolis; aff'l w/ C	98	98.5.2	36	36	E	Missing underline for added space	Extend underline to include the space after "GOOD CHECK state.".	PROPOSED ACCEPT.	EZ
154	Maguire, Valerie	Copperopolis; aff'l w/ C	104	104.1.3	38	38	E	Missing underline for added space	Extend underline to include the space before " A Type G PSE".	PROPOSED ACCEPT.	EZ
155	Maguire, Valerie	Copperopolis; aff'l w/ C	104	104.6.2	40	8	E	Missing underline for added space	Extend underline to include the space after " Type G ".	PROPOSED ACCEPT.	EZ
156	Zimmerman, George	CME Consulting/ADI,AF FM	FM	FM	12	21	E	Fill in clause TBD on 802.3dk abstract.	Replace "TBD" with "168".	PROPOSED ACCEPT.	EZ

								The font sizes for 96, 97, 146, and 147 appear to be smaller than the text. It appears systematic, and also occurs on line 36, and P22 line 22, but only seems to show up in clause 1. There are no new abbreviations in 802.3dg. The contents of 1.5 are a placeholder				
157	Zimmerman, George	CME Consulting/ADI,Af	1	1.4.206	21	22	E	Make font size consistent for external "Clause #" references on P21 L22 and P22 L22	PROPOSED ACCEPT.		EZ	
158	Zimmerman, George	CME Consulting/ADI,Af	1	1.5	22	33	E	Remove 1.5 and "ABBR" from the draft.	PROPOSED ACCEPT.		EZ	
159	Zimmerman, George	CME Consulting/ADI,Af	45	45.2.1.7.4	25	32	E	Editing instruction should reference that table 45-9 was modified by amendments.	Change editing instruction to read: "Insert a new row in Table 45–9 (as modified by IEEE Std 802.3db-2022, IEEE Std 802.3ck-2022, IEEE Std 802.3cy-2023, IEEE Std 802.3df-2024, and IEEE Std 802.3dk-202x) after the row for 100BASE_T1 as follows (unchanged rows not shown):" Change editing instruction to read: "Insert a new row in Table 45–10 (as modified by IEEE Std 802.3db-2022, IEEE Std 802.3ck-2022, IEEE 802.3df-2024, and IEEE 802.3dk-202x) after the row for 100BASE_T1 as follows (unchanged rows not shown):"	PROPOSED ACCEPT.	EZ	
160	Zimmerman, George	CME Consulting/ADI,Af	45	45.2.1.7.5	26	3	E	Editing instruction should reference that table 45-10 was modified by amendments.	PROPOSED ACCEPT.		EZ	
161	Zimmerman, George	CME Consulting/ADI,Af	45	45.2.1.16.1aa	26	35	E	Editing instruction is in error in several ways - first, a typo - 42.1.16.1 should read 45.2.1.16.1, second, 802.3cy and 802.3da did not modify the 45.2.1.16.1). 802.3cy inserted 45.2.1.16a, to describe bit 7. Draft 3.0 of 802.3da omits 45.2.1.16aa describing the added bit 8. so there is currently no 45.2.1.16.aa. The resolution assumes that this error will be fixed in initial SA ballot where a parallel comment is being filed.	Change editing instruction to read: "Insert new subclause 45.2.1.16.1aaa before 45.2.1.16aaa (inserted by IEEE Std 802.3da-202x) as follows: Change Note to read: "NOTE—This operation may interrupt data communication. The data path of the 100BASE-T1L PHY, depending on	PROPOSED ACCEPT.	EZ	
162	Zimmerman, George	CME Consulting/ADI,Af	45	45.2.3.75a.1	31	12	E	It seems the note on the PCS reset should be parallel to the PMA reset, since it would reset the PHY control state diagram. See 45.2.1.236a.1.	implementation, may take many seconds to run at optimum error ratio after exiting from reset."	PROPOSED ACCEPT.	EZ	
163	Zimmerman, George	CME Consulting/ADI,Af	78	78.1.4	34	7	E	Tables 78-1, 78-2, and 78-4 were modified by 802.3cy	Change editing instruction at P34 L8 to read, "Insert new row in Table 78-1 as modified by IEEE Std 802.3cy-2023 after 10BASE-T1L as follows (unchanged rows not shown):" Change editing instruction at P34 L22 to read, "Insert new row in Table 78-2 as modified by IEEE Std 802.3cy-2023 after 10BASE-T1L as follows (unchanged rows not shown):" Change editing instruction at P35 L1 to read, "Insert new row in Table 78-4 as modified by IEEE Std 802.3cy-2023 after 10BASE-T1L as follows (unchanged rows not shown):"	PROPOSED ACCEPT.	EZ	
164	Zimmerman, George	CME Consulting/ADI,Af	98	98.6.9	37	18	E	Editing instruction should just insert the new PICS item. Renumber happens on fold into the revision	Change the editing instruction to, "Change row for SD19 and insert new row 20a State diagram and variable definitions PICS table as shown (unchanged rows not shown)"" Replace "..." row under SD19 with (existing, unchanged, no underline) row SD20 to the table after SD19: SD20 link_fail_inhibit_timer_[HCD] for 10BASE-T1L PHY 98.5.2 Expires 3030 ms to 3090 ms after entering the AN LINK GOOD CHECK state" 10T1L:M Yes[] N/A[] Change "SD21" to "SD20a" on next row.	Delete renumbered rows SD22 through SD30 from the draft.	PROPOSED ACCEPT.	EZ

165	Zimmerman, George	CME Consulting/ADI,Af	104	104.1.3	38	14	E	Text of 104.1.3 modified by 802.3cy was not included.	Change Editing instruction at P38 L8 to read "Change second paragraph of 104.1.3 as modified by IEEE Std 802.3cy-2023 as shown:" Change line 14 (second to last sentence) to read "A Type F PSE and Type F PD are compatible with 2.5GBASE-T1, 5GBASE-T1, 10GBASE-T1, and 25GBASE-T1 PHYs." Change PD20 to PD20a, Revert PD22 to PD21 (but keep change on spacing in Value/Comment) Change Editing instruction (line 14) to reference Type F PD item PD21, not PD22...	PROPOSED ACCEPT.	EZ
166	Zimmerman, George	CME Consulting/ADI,Af	104	104.9.4.3	42	20	E	New PICS item should be inserted as PD20a, without renumbering PICS in amendment.	Delete rows below (now) PD21, as they aren't renumbered in the amendment. Add PICS per contribution zimmerman_PICS_3dg_20250901.pdf with editorial license to align with other resolved comments.	PROPOSED ACCEPT. PROPOSED ACCEPT.	EZ
167	Zimmerman, George	CME Consulting/ADI,Af	190	190.11	129	1	ER	PICS are needed for clause 190	Insert new item below, summing the power of the individual pair-to-pair differential alien NEXT loss values over the frequency range 0.1 MHz to 60 MHz as follows in Equation (190–4)." with text below, adapted from 146.7.2.1 "PSANEXT loss is determined by summing the power of the individual pair-to-pair differential alien NEXT loss values over the frequency range 0.1 MHz to 60 MHz as follows in Equation (190–XX)." (insert new equation 190-XX, identical to Equation 146-13) "where the function AN(f) _{j,N} represents the magnitude (expressed in dB) of the alien NEXT loss at frequency f of the disturbing 100BASE-T1L link segment j (1 to m) for the disturbed 10BASE-T1L link segment N. The power sum ANEXT loss between a disturbed 100BASE-T1L link segment and other disturbing 100BASE-T1L link segments shall meet the values determined using Equation (190–17) or 60 dB, whichever is less." (note to editor, Equation 190-17 above refers to the current numbering of the equation at P122 L13 - it will obviously be renumbered) Add new PICS item to Link Segment, "Power sum ANEXT loss between a disturbed 100BASE-T1L link segment and the disturbing 100BASE-T1L link segment" 190.7.2.1 Meets equation 190-17 or 60 dB whichever is less Yes[] No[]	Editorial license to adjust PICS per comment resolution and changes in text.	PICS
168	Zimmerman, George	CME Consulting/ADI,Af	190	190.7.2.1	122	8	TR	The requirement that the link segment meet the alien NEXT is missing.		PROPOSED ACCEPT.	Link Segment

									Replace "as follows in Equation (190–5)." at P123 L11 with text below, adapted from 113.7.3.2.1 "as follows in Equation (190–YY)." (insert new equation 190-YY, identical to Equation 113-29, except the subscripted index "i" and the sum over index "i" is omitted) "where AACRF(f) _j , N is the magnitude in dB of the alien ACRF at frequency f of the disturbing link j (1 to m) into the 100BASE-T1L link segment N. The PSAACRF between a disturbed duplex channel in a link segment and the disturbing duplex channels in other link segments shall meet the values determined using Equation (190–18)." (note to editor, Equation 190-18 above refers to the current numbering of the equation at P123 L14 - it will obviously be renumbered) Add new PICS item to Link Segment, "Power sum PSAACRF loss between a disturbed 100BASE-T1L link segment and the disturbing 100BASE-T1L link segment" 190.7.2.2 Meets equation 190-18 or 60 dB whichever is less Yes[] No[]			
169	Zimmerman, George	CME Consulting/ADI,Af	190	190.7.2.2	122	8	TR	The requirement that the link segment meet the alien NEXT is missing.		PROPOSED ACCEPT.	Link Segment	
170	Zimmerman, George	CME Consulting/ADI,Af	190		95	8	T	the variable tx_lpi_alert_active in states SEND_NORMAL, SEND_ALERT,and SEND_WAKE isn't listed in the variables, and appears to be the variable tx_alert_active (otherwise there is no way tx_alert_active is set)... Untestable shall: The identification of invalid characters is an untestable shall. The thing that is testable is the replacement o fthese with /E/, which is a second shall. Therefore, remove the shall on the "identification" - it is only a definition of what is to be replaced.	change tx_lpi_alert_active to tx_alert_active in SEND_NORMAL, SEND_ALERT, and SEND_WAKE states of Figure 190-12.	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 280	State Diagrams	
171	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.3.1	79	6	T	Untestable shall: State diagrams aren't "implemented" per se - the behavior is implemented. The diagrams are conformed to, as in the previous sentence.	Change "Received characters shall be identified as invalid characters" with "Received characters are defined as invalid characters"	PROPOSED ACCEPT IN PRINCIPLE. TFTD - need to determine whether there should be a different requirement here.	PCS	
172	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.3	78	12	E		Change "shall implement the RFER Monitor" to "shall conform to the RFER Monitor"	PROPOSED ACCEPT.	Editorial	
173	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.4.2	82	23	T	Untestable shall: whether the follower uses the FTFC value or not to determine the alignment is unobservable. It can (and probably does), but the alignment itself, specified in 190.3.5 is what is required - not that the FTFC is used... descriptive language is appropriate here.	change "shall use the FTFC" to "uses the FTFC" Change "shall implement the CRC polynomial" (at line 3) to "implements the CRC polynomial"	PROPOSED ACCEPT IN PRINCIPLE. Accomated by comment 230.	PCS	
174	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.4.2.5	84	3	E	there are several duplicative shalls in the description of the CRC. Only one is needed. The others describe the figure.	Change "shall be initialized to zero" (at line 6) to "are initialized to zero".	PROPOSED ACCEPT.	EZ	
175	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.6.1.4	92	21	E	The 'shalls' on DECODE_MII and ENCODE are duplicative of the 'shalls' in 190.3.3.3 and 190.3.2.4, which require the decoding of the received characters and encoding of the MII inputs. Since the entire PCS state diagram is required, the functions described for DECODE_MII and ENCODE are already specified.	Change "shall generate" to "generates" (P92 L21) and "shall encode" to "encodes" (P92 L24)	PROPOSED ACCEPT.	EZ	

176	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.6.1.3	91	51	E	The state diagram is already required with a shall, and the behavior of the timers is specified within the state diagram - does each timer duration really need a "shall"? Note - this is a stylistic difference between many BASE-T/BASE-T1 clauses and the rest of 802.3. While this is useful in autoneg where the link_fail_inhibit_timer has different durations for different PHY types (and hence this results in different phy-specific compliance points for the autoneg compliance), it really doesn't seem useful here, where the durations are fixed.	Change "This timer shall have a period equal to" to "This timer's period is" for lpi_rx_wake_timer (P91 L53), lpi_tx_alert_timer (P92 L4), lpi_tx_sleep_timer (P92 L9), and lpi_tx_wake_timer (P92 L14). Change "This timer shall expire" to "This timer expires" in 190.4.9.1.2 for follower_initi_timer (P105 L12), min_follower_silent_timer (P190 L16), min_pam3_tuning_timer(P105 L19), silent_timer (P105 L23), and lpi_refresh_rx_timer (P105 L29)	PROPOSED ACCEPT.	EZ
177	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.2	100	24	T	Duplicate shall: The loop timing relationship is already specified by the requirement that the FOLLOWER shall source from the recovered clock... (note all BASE-T clauses don't have this as a shall. Clauses 97 & 149 included it, as a duplicate)	change "shall include loop timing" to "includes loop timing"	PROPOSED ACCEPT. Consider with comment 235	Editorial
178	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.2	100	30	E	45.2.1.7.4 is included in the draft - this should be a direct cross reference, not an External reference (green)	Remove External flag on 45.2.1.7.4 and replace with a cross reference	PROPOSED ACCEPT.	EZ
179	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.3	101	9	T	There is no register 45.2.1.252.7, and no copy of the receive fault bit in the PMA status register. (45.2.1.236b). There is no need to copy the bit. 45.2.1.7.5 is included in the draft - this should be a direct cross reference, not an External reference (green)	Change "the receive fault bit specified in 45.2.1.7.5 and 45.2.1.252.7." to "the receive fault bit specified in 45.2.1.7.5."	PROPOSED ACCEPT.	Management
180	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.3	101	9	E	Duplicate shall: compliance with state diagrams in 190.4.9.2 is currently required already under 190.4.4.2 whether or not the PHY is in the startup sequence.	Remove External flag on 45.2.1.7.5 and replace with a cross reference	PROPOSED ACCEPT.	EZ
181	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.4.2	101	36	T	Duplicate shall: Figure 190-20 is included in 190.4.9.2 which is already required under 190.4.4 PHY Control.	change "shall comply with the state diagrams" to "behaves as specified in the state diagrams"	PROPOSED ACCEPT.	PICS
182	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.5	102	11	T	Duplicate shall: Figure 190-20 is included in 190.4.9.2 which is already required under 190.4.4 PHY Control.	change "shall comply with the state diagram of Figure 190-20" to "behaves as specified by the state diagram of Figure 190-20"	PROPOSED ACCEPT.	PICS
183	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.6	102	11	T		change "shall comply with the state diagram of Figure 190-20" to "behaves as specified by the state diagram of Figure 190-20"	PROPOSED ACCEPT.	PICS
184	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.7	102	35	T	Untestable shall - what is a "clock suitable for signal sampling" should be specified in the jitter and frequency stability specifications.	change "shall provide" to "provides"	PROPOSED ACCEPT.	PICS
185	Zimmerman, George	CME Consulting/ADI,Af	190	190.4.8.1	103	2	T	Duplicate shall: 190.4.4 already requires the transmitted symbols to comply with 190.5.4 at the MDI.	Delete: "This symbol response shall comply with the electrical specifications given in 190.5.4."	PROPOSED ACCEPT.	PICS
186	Zimmerman, George	CME Consulting/ADI,Af	190	190.5.4.3	113	13	T	Requirements on the user: the jitter measurement interval and measurement bandwidth are conditions of the measurement, but are stated as requirements on the user (with a 'shall'). Duplicate (& duplicate again) shalls. Both sentences here just say we meet the requirements that are required elsewhere... why are we duplicating the SHALLs so much? Rewriting this text to be descriptive and cover the fact that the link segments for the tests describe all need to meet 190.7.	Change "Jitter shall be measured over an interval of 1 ms ± 10%. The bandwidth of the measurement device shall be larger than 200 MHz." to "These requirements apply when measured over an interval of 1 ms ± 10% with a measurement device of at least 200 MHz bandwidth." Replace P116 L3 & 4 with "The receiver electrical tests exercise the PMA Receive function and test performance to electrical specifications of a link partner's transmitter as well as performance in noise. Link segments used in the test configurations for this subclause shall be within the limits specified in 190.7."	PROPOSED ACCEPT.	Test Modes
187	Zimmerman, George	CME Consulting/ADI,Af	190	190.5.5	116	3	T			PROPOSED ACCEPT.	PMA

188	Zimmerman, George	CME Consulting/ADI,Af	190	190.1.3	45	38	T	Duplicate shall: the requirement that all PHYs are capable of operating as a LEADER or FOLLOWER is correctly placed in 190.6.1. Here, in the overview, it should be descriptive.	Change "A 100BASE-T1L PHY shall be capable of operating as a LEADER or FOLLOWER." to "100BASE-T1L PHYs are mandated to be capable of operating as a LEADER or FOLLOWER (see 190.6.1)."	PROPOSED ACCEPT.	Management
189	Zimmerman, George	CME Consulting/ADI,Af	190	190.3.2.7	70	39	TR	Somewhere along the way we seem to have missed stating the requirement for the RS-FEC encoder.	at P70 L39, change "When RS-FEC is enabled for the link, the group of 122 octets contained in the vector tx_group are encoded..." to "When RS-FEC is implemented and enabled for the link, the group of 122 octets contained in the vector tx_group shall be encoded..." Add PICS item to PCS Transmit. Feature: RS-FEC encoder Subclause 190.3.2.7 Description: See 190.3.2.7 Status: FEC:M Support: Yes[] N/A []	PROPOSED ACCEPT.	RS-FEC
190	Zimmerman, George	CME Consulting/ADI,Af	98B	98B.3	131	28	TR	There is missing information on how the transmit and receive level ability bit is resolved. This is accomplished by 98B.3.1 10BASE-T1L-specific bit assignments for 10BASE-T1L (which points to clause 146) I suggest we do the same here. [note - we may wish to have additional management & visibiltiy, but I've only covered minimal control here]	After Table 98B-1, add the following to the draft: <Editing instruction> Insert 98B.3.2 following 98B.3.1 as follows: </end Ed Inst> "98B.3.2 100BASE-T1L increased transmit/receive level ability Bit A21 shall be set to one when the PHY has the ability to transmit and received at the increased transmit level, and set to zero when the PHY does not have the ability to transmit and receive the increased transmit level, or the ability is not advertised. When MDIO is implemented, the ability of the PHY can be determined by bit 1.2301.12 (see 45.2.1.236b). Note that setting bit A21 to zero is a way of explicitly requesting the lower transmit level. If bit A21 is one for both the PHY and the link partner, increased transmit level shall be selected. If bit A21 is zero for either the local PHY or the link partner, the lower transmit level is selected."	PROPOSED ACCEPT IN PRINCIPLE. Discuss with comment 244 and other increased transmit level comments	Reduced TX level
191	Zimmerman, George	CME Consulting/ADI,Af	190	190.5.4.1	112	32	TR	Unlike clause 146, we have made each test mode explicit to the transmit mode - hence the electrical specs are all written as though they only apply to the test modes. We need to link the auto-neg output to the transmitter level (we have descriptive text, but no requirement)	Insert new first sentence in 190.5.4.1 (P112 L32) "When not in test mode, the transmitter output voltage mode shall be as determined by the result of auto-negotiation as specified in 98B.3.2. See 190.6.1." Add new PMA Electrical PICS Item PMAE 2 - Feature = "Transmitter level control" Subclause= 190.5.4.1 Value/Comment = "Determined by autonegotiation per 98B.3.2." Status M Support: Yes[] No[]	PROPOSED ACCEPT IN PRINCIPLE. TFTD - consider after resolution of increased transmit level comments to see if it is necessary.	Test Modes
192	Marris, Arthur	Cadence Design Syster	45	45.2.1	25	18	E	Missing underlining of inserted text in Table 45-3	Underline the inserted register names and subclause numbers. Make similar change to Table 45–233 on page 30.	PROPOSED ACCEPT.	EZ
193	Huber, Thomas	Nokia	1	1.3	21	4	E	If there are no new normative references, this clause should not be present.	Delete clause 1.3	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE.	EZ
194	Huber, Thomas	Nokia	1	1.4.341a	21	40	T	The new definition in this subclause is for follower, so it should probably point to the old definition for slave	Change 1.4.389 to 1.4.535	Accomodated by comment 59	Editorial
195	Huber, Thomas	Nokia	1	1.5	22	29	E	If there are no new abbreviations, this clause should not be present.	Delete clause 1.5	PROPOSED ACCEPT.	EZ

196	Huber, Thomas	Nokia	30	30.5.1.1.4	24	35	E	The proposed change appears to be correct, but the quoted text of the sentence has a typo - the existing text of the sentence in question in 802.3-2022 is: 'For 10BASE-T1L and 100BASE-T1, a link_status of OK maps to the enumeration “available”.' The text in this amendment says: 'For 10BASE-T1L, 100BASE-T1L, and 1000BASE-T1, a link_status of OK maps to the enumeration “available”.'	Change 1000BASE-T1 to 100BASE-T1, aligning with the existing text in 802.3-2022, so the amendment text reads: 'For 10BASE-T1L, 100BASE-T1L, and 100BASE-T1, a link_status of OK maps to the enumeration “available”.'	PROPOSED ACCEPT.	EZ
197	Huber, Thomas	Nokia	45	45.2.1.16.1aa	26	35	E	The editing instruction is not aligned with the style guide. A new subclause that replaces the existing X.Y.Z.1 is inserted as X.Y.Z.a. In this case, 802.3cy-2023 inserted 45.2.1.16.a between 45.2.1.16 and 45.2.1.16.1. 802.3da will add 45.2.1.16.aa between 45.2.1.16 and 45.2.1.16.a (as inserted by 802.3cy-2023). As such, 802.3dg needs to insert 45.2.1.16.aaa between 45.2.1.16 and 45.2.1.16.aa (as inserted by 802.3da-20xx).	Change the instruction to read: Insert new subclause 45.2.1.16.aaa between 45.2.1.16 and 45.2.1.16.aa (as inserted by 802.3da-20xx) as follows:	PROPOSED ACCEPT.	EZ
198	Huber, Thomas	Nokia	45	45.2.3.75a	30	42	E	The table that is currently in 45.2.3.75 is Table 45-301 rather than table 45-297.	Change Table 45-297a to Table 45-301a. Make similar changes to Tables 45-297b, 45-297c, 45-297d	PROPOSED ACCEPT IN PRINCIPLE. There are two misnumberings here: Change the editing instruction at P30 L32 from reading "after 45.2.1.75" to "after 45.2.3.75" Change Table 45-297a to Table 45-301a (crossrefs and subsequent tables should renumber)	EZ
199	Huber, Thomas	Nokia	45	45.2.3.75b.2	32	3	T	Since there are many RS FECs specified in 802.3, it would be useful to clarify which one is the subject of bit 3.2296.14	Change the first line of the Description for bit 3.2296.14 to say: 1 = PCS has RS-FEC ability per clause 190.3.2.7	CRG disagrees with commenter. This is a bit in a register specific to 100BASE-T1L. It is clear which RS-FEC ability the bit is referring to - there is only one in 100BASE-T1L	RS-FEC
200	Huber, Thomas	Nokia	78	78.2	34	20	E	Typo in the clause title	Change 'desrcption' to 'description'	PROPOSED ACCEPT.	EZ
201	Huber, Thomas	Nokia	104	104.5.7.4	39	33	E	"Type G" is new text, so it should be underlined.	Underline "Type G". Change to read "An auxiliary bit is added to each group of 15 16B/17B blocks to create a PCS frame..."	PROPOSED ACCEPT.	EZ
202	Huber, Thomas	Nokia	190	190.1.3	45	21	E	Singular/plural disagreement in "An auxiliary bit is added to each 15 16B/17B block to create a PCS frame..."	Make a similar change in the next paragraph at line 24 as well.	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 224.	EZ
203	Huber, Thomas	Nokia	190	190.4.2	100	30	E	Subclause 45.2.1.7.4 is part of this amendment, so it should not be shown as an external reference	Change the character format of 45.2.1.7.4 back to the default paragraph format	PROPOSED ACCEPT.	EZ
204	Huber, Thomas	Nokia	190	190.4.3	101	9	E	Subclause 45.2.1.7.5 is part of this amendment, so it should not be shown as an external reference	Change the character format of 45.2.1.7.5 back to the default paragraph format	PROPOSED ACCEPT.	EZ

								The MDI RL Specification is requiring 16 dB up to 40 MHz and then rolling off with 20 dB per decade for higher frequencies. This MDI RL specification has been derived from 1000BASE-T, where the existing 1000BASE-T transformers meet this specification and typically the PHY chip and also the transformers are mounted very close to the RJ45 connector (or the transformers are even integrated), so that PCB capacitances are low. Also the powering is applied as common mode powering to the data pairs. For 100BASE-T1L the powering is applied differentially on the data pair, using a separate power feeding inductor, which has additional inter- and intrawinding capacitances. For higher power ports, these inductors, but also a typically needed common mode choke have a significantly larger size typically also causing additional capacitive load. Due to the differentially applied supply voltage also the EMC protection circuits, which need to be able to withstand higher voltages, typically provide a higher capacitance than low voltage ESD clamping diodes designed for 1000BASE-T.	Due to the higher needed capacitance in a practical circuit, it is suggested, to move the start the roll-off of the MDI RL at the high frequency side from 40 MHz to 20 MHz (leading to a similar MDI RL at Nyquist (10 dB @ 40 MHz) than for 10BASE-T1L (10.4 dB @ 3.75 MHz)). This would result in higher signal reflections and thus a lower signal energy at the receiver (about 10 %), nevertheless for powered systems it seems to be necessary to be able to do a practical circuit design. If accepted, please change the second line in the formula 190-19 from "16 2 <= f < 40" to "16 2 <= f < 20" and the third line in the formula from "10 - 20 * log10(f/80) 40 <= f <= 100" to "16 - 20 * log10(f/20) 20 <= f <= 100" (at least for powered systems). Needs also discussion, if there is need to distinguish powered and non-powered systems related to the maximum possible link segment length/IL (due to the higher signal losses and additional reflections caused by the powering circuit).	PROPOSED ACCEPT IN PRINCIPLE. TFTD. Presentation Requested.	MDI	
205	Graber, Steffen	Pepperl+Fuchs SE	190	190.8.2.1	12	7	T					
206	Wienckowski, Natalie	IVN Solutions LLC	FM	FM	12	21	E	P802.3dk is not in SA ballot. It adds Clause 168.	Change "TBD" to 168.	PROPOSED ACCEPT.	EZ	
207	Wienckowski, Natalie	IVN Solutions LLC	FM	FM	12	28	E	P802.3dj is in WG ballot, v 2.1, and has finalized the Annexes.	Change "<annexes>" to Annex 174A through Annex 186A.	PROPOSED ACCEPT.	EZ	
208	Wienckowski, Natalie	IVN Solutions LLC		1	1.3	21	4	E	Delete empty subclause	Delete 1.3 heading and editing instructions.	PROPOSED ACCEPT.	EZ
209	Wienckowski, Natalie	IVN Solutions LLC		22	22.2	22	3	E	Delete unchanged content of subclause	Delete paragraph below 22.2 heading as there are no changes. Keep the heading.	PROPOSED ACCEPT. PROPOSED REJECT. BASE-T1L PHYs are grouped together because they are more likely to be contained in a multi-speed PHY.	EZ
210	Wienckowski, Natalie	IVN Solutions LLC	98	98.5.2	36	45	T	Why is 100BASE-T1L between 10BASE-T1L and 10BASE-T1S.	Move 100BASE-T1L to be before 10BASE-T1L to be consistent with the ordering of the PHY types.		Editorial	
211	Wienckowski, Natalie	IVN Solutions LLC	190	190.1.3	45	36	E	100BASE-T1L is breaking across the line. Use a nonbreaking hyphen in the middle of a PHY name.	Use a nonbreaking hyphen in the middle of a PHY name. Esc hyphen h	PROPOSED ACCEPT.	EZ	
212	Wienckowski, Natalie	IVN Solutions LLC	190	190.3.1	60	50	T	It is defined when PCS Reset is set to "TRUE", but not false.	Between the first and third sentences of the second paragraph add the sentence: It is set FALSE otherwise. Make consistent. P61L44: Normal Inter-Frame P66L34: Normal Inter-Frame P69L18: Normal Inter-Frame P90L13: Normal inter-frame P110L28: normal inter-frame	PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 266.	PCS	
213	Wienckowski, Natalie	IVN Solutions LLC	190	190.3.2	61	44	E	Inconsistent capitalization of "Normal Inter-Frame".	P110L33: normal inter-frame To add (continued) to table title on the second page when a table is split across pages: Place the cursor at the end of table title on first page. Then click on the Variables Tab and insert "Table Continuation" variable. This will add the (continued) on subsequent pages.]	With editor's license to check and update all Normal Inter-Frame to "Normal Inter-Frame".	EZ	
214	Wienckowski, Natalie	IVN Solutions LLC	190	190.3.4.3	85	1	E	Should be a continued table.		PROPOSED ACCEPT.	EZ	
215	Wienckowski, Natalie	IVN Solutions LLC	190	190.3.6.1.2	90	9	T	Boolean variable with no defininition of "FALSE".	At the end of the description add: It is set FALSE otherwise. Add at the end of the sentence fragment: the following transmitter droop measurements apply in test modes 3 and 4, respectively.	PROPOSED ACCEPT.	EZ	
216	Wienckowski, Natalie	IVN Solutions LLC	190	190.5.4.2	112	45	T	The first sentence is not a complete sentence.		PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 18	EZ	
217	Wienckowski, Natalie	IVN Solutions LLC	190	190.5.5.3	116	23	E	Extraneous carriage return.	Remove the carriage return after "specification".	PROPOSED ACCEPT.	EZ	

218	Wienckowski, Natalie	IVN Solutions LLC	190	190.5.6	116	45	E	Heading with no contents May we consider any features from the 802.3da clause 189 as optional for power over 100BASE-T1L?	Delete 190.5.6 Open question that would require further work and consensus. I am not power expert, but willing to participate if such option is to be considered. Change the resistor "100ohm" to a generic value "Rs ohm", with a note "The combination of Rs and the two 500 ohm resistors matches the source impedance of the noise source.". Refer as an example to 802.3da clause 188.6.6.2 Figure 188-16.	PROPOSED ACCEPT IN PRINCIPLE. TFTD with Comment 130 - is this a hanging header or missing content?	PMA
219	Brychta, Michal	Analog Devices	104	104	38	1	T	(Figure 190-28-Alien crosstalk noise rejection test set-up) The output of the Noise Source may not be correctly terminated. More work may need to be done to see if the limits are feasible, specifically when adding power coupling.		TFTD - presentation requested.	Power
220	Brychta, Michal	Analog Devices	190	190.5.5.3	116	28	T	More work may need to be done to see if the limits are feasible, specifically when adding power coupling.	Not in a position to give specific proposal, but willing to work on this topic.	PROPOSED ACCEPT.	PMA Electrical
221	Brychta, Michal	Analog Devices	190	190.8.2.1	125	7	T	More work may need to be done to see if the limits are feasible, specifically when adding power coupling.	Not in a position to give specific proposal, but willing to work on this topic.	PROPOSED ACCEPT IN PRINCIPLE. TFTD - Presentation requested.	MDI
222	Brychta, Michal	Analog Devices	190	190.8.2.2	126	7	T		Change the Value/Comment text of Item SD21: For all technolgies except 100BASE-T1L the expiration time of the link_fail_inhibit_timer_[HCD] is specified in the form of a range. For 100BASE-T1L the exact value 85 ms is specified. This potentially creates a compliance condition that cannot be satisfied.	PROPOSED ACCEPT IN PRINCIPLE. TFTD - Presentation requested.	MDI
223	Murray, Brian	Analog Devices	98	98.6.9	37	30	T		"Expires 85 ms after entering the AN GOOD CHECK state" to: "Expires 84 ms to 85 ms after entering the AN GOOD CHECK state"	PROPOSED ACCEPT. (align with comment 253)	State Diagrams
224	Murray, Brian	Analog Devices	190	190.1.3	45	21	E	The text "An auxiliary bit is added to each 15 16B/17B block ..." is confusing since "block" is singular.	Change the following text: "An auxiliary bit is added to each 15 16B/17B block ..." to: "One auxiliary bit is added to every 15 16B/17B blocks ..."	PROPOSED ACCEPT.	EZ
225	Murray, Brian	Analog Devices	190	190.1.3	45	24	E	The link_status parameter is missing in Figure 190-3.	Change the following text: "An auxiliary bit is added to each 15 64B/65B block ..." to: "One auxiliary bit is added to every 15 64B/65B blocks ..."	PROPOSED ACCEPT.	EZ
226	Murray, Brian	Analog Devices	190	190.3	60	36	E		Add and arrow going into the bottom of the PCS RECEIVE block labeled link_status	PROPOSED ACCEPT.	EZ
227	Murray, Brian	Analog Devices	190	190.3.2.4	65	1	E	The text in the first sentence of the fist paragraph of page 65 states: "Any MII transfer in Table 190–1 for which TX_EN is 0, including Assert LPI and Assert remote fault, is categorized as IDL". However, only Assert remote fault is shown in Table 190-1; Assert LPI is not explicitly shown, because it is not required in Table 190-2 below.	Remove "Assert LPI" from that sentece, changing the text to: "Any MII transfer in Table 190–1 for which TX_EN is 0, including Assert remote fault, is categorized as IDL"	PROPOSED ACCEPT.	PCS

					Change the text to:				
228	Murray, Brian	Analog Devices	190 190.3.2.4	66	23 E	The text states "Table 190–2 shows the TOCT values for control symbols using symbolic representations for clarity. The mapping from these symbolic representations to the associated numerical values is shown in Table 190–3.". Table 190-3 showns additional symbols, /Ix/ and /LI/ which are not defined in Table 190-2, but are used in the PCS.	"Table 190–2 shows the TOCT values for control symbols using symbolic representations for clarity. The mapping from these symbolic representations, to the associated numerical values is shown in Table 190–3. The table also shows the /Ix/ (see Clause 190.3.2.5.1.) and /LI/ (see Clause 190.3.2.5.3) symbolic representations which are used in the PCS state diagrams (see Clause 190.3.6). Change the following text: "... conveys an Assert LPI symbol (/L/) ..." to:	PROPOSED ACCEPT.	PCS
229	Murray, Brian	Analog Devices	190 190.3.2.5.3	69	24 E	The symbolic representation of the Assert LPI symbol is incorrectly written as /L/ instead of /LI/.	"... conveys an Assert LPI symbol (/LI/) ..."	PROPOSED ACCEPT.	EZ
						In clause 190.3.5 the detailed specification for PFC alignment is in 190.3.5.1 and is provided by the following text: "A PHY in FOLLOWER mode is responsible for synchronizing its PFC to the PFC of the LEADER during PAM2 training. See 190.3.4.2 for the requirements on the FOLLOWER alignment with reference to the LEADER." However, 190.3.4.2 contains the text below: "When the config parameter is FOLLOWER and EEE is enabled for the link, the FOLLOWER shall use the FTFC value received from the LEADER to align its quiet-refresh cycle to that of the LEADER as specified in 190.3.5." This creates a circular reference. My preference is to keep all of the requirements on frame alignment in clause 190.3.4.2 since this is all connected to the formatted training frame exchange.	In clause 190.3.4.2 change the paragraph that starts on line 16 of page 82 to the following: "The start of the training frame transmitted by the FOLLOWER shall be delayed by not more than 1 PCS partial frame with reference to the start of the training frame received from the LEADER, as seen at the MDI of the FOLLOWER. When EEE is enabled for the link, the FOLLOWER shall align its PFC to that of the LEADER as shown in Figure 190-12." On page 82 line 22 change the following text: "When the config parameter is FOLLOWER and EEE is enabled for the link, the FOLLOWER shall use the FTFC value received from the LEADER to align its quiet-refresh cycle to that of the LEADER as specified in 190.3.5." to the text shown below: "When the config parameter is FOLLOWER and EEE is enabled for the link, the FOLLOWER uses the FTFC value received from the LEADER to align its PFC to that of the LEADER."	PROPOSED ACCEPT.	PCS
230	Murray, Brian	Analog Devices	190 190.3.4.2	82	24 T	In Table 190–8 the 4B6B NND code-groups for PAM-2 training are listed. The entry [0010] = [-1 1 1 1 1 1] has a running disparity of +4. All other entries in the table have a running disparity of 0 or +2. The result of this is a difference between the running disparity bound during PAM-2 training (+/-7) and during data (+/-5). There are 14 unused 6-tuples with running disparity of +2 (and their inverse) available to use as an alternative 6-tuples in the 4B6B table. Propose to use the 6-tuple [-1 1 -1 1 1 1] which has a running disparity of +2, is well behaved with no significant concern over data correlation.This keeps the range of running disparity the same in training and data.	Replace the 6-tuple [-1 1 1 1 1 1] for entry [0010] in Table 190- TFTD. This is a technical improvement. Presentaiton requested.	PROPOSED ACCEPT IN PRINCIPLE.	PCS
231	Murray, Brian	Analog Devices	190 190.3.4.3	84	41 TR				

232	Murray, Brian	Analog Devices	190	190.3.4.3	85	14	E	The text "... keeps the running sum of the transmitted PAM3 symbols within bounds ..." refers to PAM3 symbols. However, 4B6B encoding uses PAM2.	Change "PAM3" to "PAM2".	PROPOSED ACCEPT.	EZ
233	Murray, Brian	Analog Devices	190	190.3.6.2	95		E	The variable name "tx_lpi_alert_active" is incorrectly used in 3 places in Figure 190-12.	Change "tx_lpi_alert_active" to "tx_alert_active" in states SEND_NORMAL, SEND_ALERT and SEND_WAKE.	PROPOSED ACCEPT.	EZ
234	Murray, Brian	Analog Devices	190	190.3.7	99	1	E	Clause 190.3.7 (PCS Management) is empty. I don't think that we need this clause. If we do decide to keep the PCS management clause, then we should have an equivalent clause for PMA.	Merge Clause 190.4.4.1 and Clause 190.3.7 in a new subclause under Clause 190.6 with a Table showing the PMA and PCS MDIO registers for 100BASE-T1L	PROPOSED ACCEPT IN PRINCIPLE. Contributions requested with text for table.	Management
235	Murray, Brian	Analog Devices	190	190.4.2	100	23	E	The text states: "When the PMA_CONFIG.indication parameter config is LEADER, the PMA Transmit function shall source TX_TCLK from a local clock source while meeting the transmit jitter requirements of 190.5.4.4. The LEADER-FOLLOWER relationship shall include loop timing. If the PMA_CONFIG.indication parameter config is FOLLOWER, the PMA Transmit function shall source TX_TCLK from the recovered clock of 190.4.7 while meeting the jitter requirements of 190.5.4.4". But TX_TCLK is not defined nor used anywhere. Also the jitter requirements clause reference is incorrect (it should be 190.5.4.3).	Change the text to: "When the PMA_CONFIG.indication parameter config is LEADER, the PMA Transmit function shall source the transmit clock from a local clock source while meeting the transmit jitter requirements of 190.5.4.3. The LEADER-FOLLOWER relationship shall include loop timing. If the PMA_CONFIG.indication parameter config is FOLLOWER, the PMA Transmit function shall source the transmit clock from the recovered clock of 190.4.7 while meeting the jitter requirements of 190.5.4.3." Remove the reference to 45.2.1.252.7 in the the last sentence of the last paragraph in Clause 190.4.3 changing the text to: "If the MDIO interface is implemented, then this function shall contribute to the receive fault bit specified in 45.2.1.7.5"	PROPOSED ACCEPT. TFTD (to double check - there's a lot in here)	Editorial
236	Murray, Brian	Analog Devices	190	190.4.3	101	9	E	The PMA Receive fault function is mapped to the receive fault bit specified in clause 45.2.1.252.7 which does not exist. Likely it meant to refer to 45.2.1.236b 100BASE-T1L PMA status register (Register 1.2301). But there is no receive fault bit specified in that clause.	"If the MDIO interface is implemented, then this function shall contribute to the receive fault bit specified in 45.2.1.7.5"	PROPOSED ACCEPT.	EZ
237	Murray, Brian	Analog Devices	190	190.4.4.1	101	31	E	In Table 190-12, the "Transmit disable" MDIO control variable is mapped to the PMA control variable "PMA_transmit_disable", but in Clause 190.4.2.1 is named "pma_transmit_disable", which is inconsistent. Also the "Register/bit number" for the "Reset" variable is incomplete. It should be "1.0.15/1.2300.15"	In Table 190-12: Change the second row of the "PMA control variable" column to: "pma_transmit_disable" Change the first row of the of the "Register/bit number" columnt to "1.0.15/1.2300.15"	PROPOSED ACCEPT.	EZ
238	Murray, Brian	Analog Devices	190	190.4.5	102	8	E	The text states that the link_status variable is communicated to the PHY Control function through the PMA_LINK.indication primitive, but the PHY Control is a PMA function. Furthermore, in the 100BASE-T1L PHY Control function, link_status is not used.	Change the text, in the second sentence of the first paragraph in 190.4.5, to remove the reference to the PHY Control function, as shown: "This variable is communicated to the PCS and the Auto-Negotiation function through the PMA_LINK.indication primitive as specified in 190.2.1.2"	PROPOSED ACCEPT.	EZ
239	Murray, Brian	Analog Devices	190	190.4.7	102	37	T	The text states that "The received clock signal is supplied to the PMA Transmit function by received_clock". The "received_clock" signal is only used in the PMA reference diagram of Figure 190-16 and it goes from the "PMA RECEIVE" function to the "CLOCK RECOVERY" function. The "recovered_clock" signal is the one that goes from the "CLOCK RECOVERY" to the "PMA TRANSMIT" function.	Change the text to: "When the PMA_CONFIG.indication parameter config is FOLLOWER, the received clock signal is supplied to the PMA Transmit function".	PROPOSED ACCEPT IN PRINCIPLE. This is actually an insert... Insert "When the PMA_CONFIG.indication parameter config is FOLLOWER, " so that P102 L37 reads ""When the PMA_CONFIG.indication parameter config is FOLLOWER, the received clock signal is supplied to the PMA Transmit function by received_clock."	PMA

						Change the following text:			
						"The power spectral density of the transmitter, measured into a 100 W load using the test fixture shown in Figure 190–23, shall be between the upper and lower masks specified in Equation (190–9) and Equation (190–10) for the 1.0 Vpp transmit amplitude and by Equation (190–11) and Equation (190–12) for the 2.0 Vpp transmit amplitude"			
						to:			
						"The power spectral density of the transmitter, measured into a 100 W load using the test fixture shown in Figure 190–23, shall be between the upper and lower masks specified in Equation (190–9) and Equation (190–10) for the 2.0 Vpp transmit amplitude and by Equation (190–11) and Equation (190–12) for the 1.0 Vpp transmit amplitude"	PROPOSED ACCEPT.	EZ	
240	Murray, Brian	Analog Devices	190 190.5.4.4	113	29 E	The PSD masks equations references for 2.0 Vpp and 1.0 Vpp are reversed. There is an unintended like break at line 23:			
						"[...]. This specification			
241	Murray, Brian	Analog Devices	190 190.5.5.3	116	23 E	may be considered satisfied [...]" Item d) in the enumerated list is incorrect. Auto-negotiation is not used to negotiate EEE.	Remove the line break to merge the first and second paragraphs in 190.5.5.3	PROPOSED ACCEPT.	EZ
242	Murray, Brian	Analog Devices	190 190.6.1	117	15 T		Remove item d) from the enumerated list.	PROPOSED ACCEPT.	EZ
						802.3dg is proposing to use 2 of the available 15 technology ability bits and 802.3dm is proposing to use a further 6 bits. We are rapidly approaching the point where next page exchange will be required.			
						This is primarily arising because the standard allows all different kinds of PHYs to coexist on the same link.			
243	Murray, Brian	Analog Devices	98B 98B.3	131	14 T	We should try to use the 15 remaining technology bits more efficiently.	A detailed presentation has been provided.	PROPOSED ACCEPT IN PRINCIPLE. TFTD - with presentation.	AutoNeg
						At present there is an implicit assumption that A21 can only be set if A10 is set. The ability to support increased voltage in 100BASE-T1L is regarded as a qualifier of the base 100BASE-T1L ability.	Change "100BASE-T1L ability" to "100BASE-T1L standard transmit/receive level ability". At line 35 changed the single entry in the dashed list to two entries as follows: - 100BASE-T1L increased transit/receive level - 100BASE-T1L standard transmit/receive level		
						There is no need to restrict 100BASE-T1L PHYs in this way. For applications where significant interference (EFT, for example) is expected, it may be beneficial to allow the PHY to decline support for operation at 1 Vpp. It is felt to be better to not bring up a link than to bring up an intermittently unreliable link.	On page 24 change the single entry for 100BASE-T1L to two entries. On page 28 add a new status bit, 1.2301.13, for standard transmit/receive level.	PROPOSED ACCEPT IN PRINCIPLE. TFTD - with presentation.	Reduced TX level

									Change the BEHAVIOUR DEFINED AS section of 30.5.1.1.10 as follows:		
245	Murray, Brian	Analog Devices	30	30.5.1.1.10	T		The aFalseCarriers MAU attribute should be updated to add 100BASE-T1L.		"A count for the number of false carrier events during IDLE in 100BASE-X, 100BASE-T1L and 1000BASE-X links. This counter does not increment at the symbol rate. For 100BASE-X and 100BASE-T1L, it can increment after a valid carrier completion at a maximum rate of once per 100 ms until the nextCarrierEvent"	PROPOSED ACCEPT.	Management
									Change the BEHAVIOUR DEFINED AS section of 30.5.1.1.15 as follows:		
									"A read-only value that indicates if the PHY supports an optional FEC sublayer or ability for forward error correction across the MDI (see 65.2, Clause 74, Clause 91, and Clause 108 and Clause 190).		
246	Murray, Brian	Analog Devices	30	30.5.1.1.15	T		The aFECAbility attribute should be updated to add 100BASE-T1L.		If a Clause 45 MDIO Interface is present, then this attribute maps to the FEC capability register (see 45.2.10.2 or, 45.2.1.107 or 45.2.3.75b).;"	PROPOSED ACCEPT.	RS-FEC
									In the BEHAVIOUR DEFINED AS section of 30.5.1.1.15:		
									Modify the first paragraphs as follows:		
									"A read-write value for a PHY that supports an optional FEC sublayer or ability that indicates the mode of operation of the FEC sublayer or ability for forward error correction across the MDI (see 65.2, Clause 74, Clause 91, and Clause 108 and Clause 190)."		
									Add a new paragraph after the third paragraph as follows:		
247	Murray, Brian	Analog Devices	30	30.5.1.1.16	T		The aFECmode attribute should be updated to add 100BASE-T1L.		"For a 100BASE-T1L PHY, a SET operation is not allowed, and for a GET operation the condition where the RS-FEC is enabled for the link, maps to the enumeration "enabled", and the condition where RS-FEC is not enabled for the link maps to the enumeration "disabled"."	PROPOSED ACCEPT.	RS-FEC
									Add the following sentence after the fifth sentence of the third paragraph of the BEHAVIOUR DEFINED AS section of 30.5.1.1.4:		
248	Murray, Brian	Analog Devices	30	30.5.1.1.4	24	36	T	The proposed text update for the aMediaAvailable attribtte "For 10BASE-T1L, 100BASE-T1L, and 1000BASE-T1, a link_status of OK maps to the enumeration "available"." is incorrect (1000BASE-T1 should be 100BASE-T1) and may not be appropriate or enough for 100BASE-T1L which supports link fault indication.	"For 100BASE-T1L, the RX Assert remote fault encoding maps to the enumeration "remote fault" and the RX Assert local fault encoding maps to the enumeration "not available". Other encodings map to the enumeration "available"."	PROPOSED ACCEPT.	Management
									Change the following text:		
								The text "The control and management interface shall be restored to operation ..." is ambiguous.	"The control and management interface shall be restored to operation within 0.5 s from the setting of bit 1.2300.15."		
								Also, the time of 0.5 s that is specified is much too long for industrial applications and is inconsistent with the time of 10 ms that is specified for bit 3.2295.15.	to:		
249	Murray, Brian	Analog Devices	45	45.2.1.236a	27	35	T		"The MDIO interface or its equivalent for accessing control and status bits shall be restored to operation within 10 ms from the setting of bit 1.2300.15."	PROPOSED ACCEPT.	Management

250	Murray, Brian	Analog Devices	45	45.2.1.236a.1	27	43	T	Bit 1.2300.15 is defined to be a copy of 1.0.15, but there is really no need to. In general it does not seem a great idea to make management bits copies of other management bits.	Remove the last paragraph in clause 45.2.1.236a.1: "Bit 1.2300.15 is a copy of bit 1.0.15, and setting or clearing either bit shall set or clear the other bit. Setting either bit shall reset the 100BASE-T1L PMA."	PROPOSED REJECT. All of the BASE-T1 PHYs are managed in this way - where they have their own registers for common PMA or PCS functions in the base PMA & PCS registers, the bits are considered copies. That way the user management experience is consistent.	Management
									Remove the last paragraph in clause 45.2.1.236a.3: "Bit 1.2300.11 is a copy of bit 1.0.11, and setting or clearing either bit shall set or clear the other bit. Setting either bit shall put the 100BASE-T1L PMA in low-power mode."	PROPOSED REJECT. All of the BASE-T1 PHYs are managed in this way - where they have their own registers for common PMA or PCS functions in the base PMA & PCS registers, the bits are considered copies. That way the user management experience is consistent.	Management
251	Murray, Brian	Analog Devices	45	45.2.1.236a.3	28	13	T	Bit 1.2300.11 is defined to be a copy of 1.0.11, but it does not have to be. In general it does not seem a great idea to make management bits copies of other management bits.	Register 1.2300.11 and 1.0.11 should be added to Table 190-12 Remove the last paragraph in clause 45.2.3.75a.1:	PROPOSED REJECT. All of the BASE-T1 PHYs are managed in this way - where they have their own registers for common PMA or PCS functions in the base PMA & PCS registers, the bits are considered copies. That way the user management experience is consistent.	Management
									"Bit 3.2295.15 is a copy of 3.0.15, and setting or clearing either bit shall set or clear the other bit. Setting either bit shall reset the 100BASE-T1L PCS." Register 2.2295.15 and 3.0.15 should be added to a new table similar to Table 190-12. Change the following text:	PROPOSED REJECT. All of the BASE-T1 PHYs are managed in this way - where they have their own registers for common PMA or PCS functions in the base PMA & PCS registers, the bits are considered copies. That way the user management experience is consistent.	Management
252	Murray, Brian	Analog Devices	45	45.2.3.75a.1	31	15	T	Bit 3.2295.15 is defined to be a copy of 3.0.15, but it does not have to be. In general it does not seem a great idea to make managment bits copies of other management bits.	"For a 100BASE-T1L PHY, this timer shall expire 85 ms after entering the AN GOOD CHECK state."	PROPOSED REJECT. All of the BASE-T1 PHYs are managed in this way - where they have their own registers for common PMA or PCS functions in the base PMA & PCS registers, the bits are considered copies. That way the user management experience is consistent.	Management
									"For a 100BASE-T1L PHY, this timer shall expire 84 ms to 85 ms after entering the AN GOOD CHECK state."	PROPOSED ACCEPT.	State Diagrams
253	Murray, Brian	Analog Devices	98	98.5.2	36	49	T	For all technolgies except 100BASE-T1L the expiration time of the link_fail_inhibit_timer_[HCD] is specified in the form of a range. For 100BASE-T1L the exact value 85 ms is specified. This potentially creates a compliance condition that cannot be satisfied.	"For a 100BASE-T1L PHY, this timer shall expire 84 ms to 85 ms after entering the AN GOOD CHECK state."	PROPOSED ACCEPT.	State Diagrams
254	McClellan, Brett	Marvell	00		0	12	E	change 'Clause TBD' to 'Clause 168'	change 'Clause TBD' to 'Clause 168'	PROPOSED ACCEPT.	EZ
255	Jones, Peter	Cisco	98B	98B	131	1	TR	Add Downshift/upshift to the draft as described in jones_3dg_august_2025_01.pdf	Make changes as per attached jones_3dg_august_2025_01.pdf pages 8 to 17.	PROPOSED ACCEPT IN PRINCIPLE. TFTD - with presentation.	Downshift
256	Jonsson, Ragnar	Infineon	190	190.3.2.3	64	14	E	It is not clear what is referred to as subject in the sentence "Contents of block type fields, data octets, and control characters are shown as hexadecimal values". Furthermore, this is not true if it refers to the following text, because it also uses binary and decimal representation.	"Hexadesimal values are prefixed with "0x" in the following text"	PROPOSED ACCEPT.	EZ
										PROPOSE REJECT. The text is clear - ARF is both IDL and it's own category, meaning that when an ARF is encoded, you consider IDL AND ARF at the same time. The only time ARF is not the same as just getting an IDL is when you get the sequence IDL ARF ARF.	PCS
257	Jonsson, Ragnar	Infineon	190	190.3.2.4	65	2	E	The use of ARF is ambiguous, since "Assert Remote fault" it is a special case of IDL	Change the text "For example, Assert remote fault belongs to the categories ARF and IDL." to something like "ARF is a special case of IDL"	PROPOSE REJECT. !ERR can be DAT. Therefore, IDL DAT DAT is the same as IDL DAT !ERR - this is the first line in the table	PCS
258	Jonsson, Ragnar	Infineon	190	190.3.2.4	65	10	TR	Table 190-2 does not have any case for "IDL DAT DAT"	Add code for "IDL DAT DAT" or add note if this is not a possible case.		PCS
259	Jonsson, Ragnar	Infineon	190	190.3.2.4	66	15	E	The description states that TS and TOCT are set according to table 190-2, but "Next dly_enc" is also set according to this table.	Change "... TS and TOCT are set in accordance with ..." to "... TS, TOCT, and "Next dly_enc" are set in accordance with ...".	PROPOSED ACCEPT.	PCS

									Change the paragraph starting at line 32 to "An octet, Tx _{bn} [7:0], is taken from the PCS frame every 6 transmit clock cycles. The octet is scrambled using a 33-bit scrambler (see Clause 190.3.2.8-11) and the 8 scrambled bits, S _{dn} [7:0], are converted to a code-group consisting of 6 PAM3 symbols using 8B6T encoding (see Clause 190.3.2.11) that keeps the running sum of the transmitted PAM3 symbols within bounds. It takes 6 PMA_UNITDATA transfers to send each code-group."			
260	Jonsson, Ragnar	Infineon	190 190.3.2.6	70	32	E	The overall encoding process is described at a high level in the paragraph starting in line 32. The description would be better if it provided reference to the detailed description of each step.		PROPOSED ACCEPT IN PRINCIPLE. Accomodated by comment 41	Editorial		
261	Jonsson, Ragnar	Infineon	190 190.3.2.8	72	42	E	The wording "In no case shall the scrambler state be initialized to all zeros." is unclear, because it could imply that there are different "cases" that need to be considered. In particular, an implementer may struggle to understand what the "no case" is that is referenced in this text.	Change "In no case shall the scrambler state be initialized to all zeros." to "The scrambler shall never be initialized to all zeros."	PROPOSED ACCEPT.	EZ		
262	Jonsson, Ragnar	Infineon	190 190.3.2.11	76	29	ER	The meaning of "+" and ">" is not clear in the formulas in lines 29-34. The operands are sequences of -1, 0, and 1, and there is no obvious definition for "+" for this kind of operands.	Add explanation of what "+" and ">" mean in the context of this text	PROPOSED ACCEPT IN PRINCIPLE. Insert line between line 30 and 32: "where + indicates an integer addition." Replace line 32 with "-1 if (DS _n > 0) AND (RD _{n-1} > 0 OR (RD _{n-1} = 0 AND Sg _n = 1))) Meaning of ">" is clear in the context of a conditional.	Editorial		
263	Jonsson, Ragnar	Infineon	190 190.3.2.11	76	39	ER	The meaning of "x" is not clear in the formulas in lines 39-44. The operands are a scalar and a sequences of -1, 0, and 1, and there is no obvious definition for "x" for this kind of operands.	Add explanation of what "x" mean in the context of this text	PROPOSED REJECT. (the multiplication symbol) is a defined parameter in the IEEE SA style guide.)	Editorial		
264	Jonsson, Ragnar	Infineon	190 190.3.4.3	85	19	ER	The meaning of "+", ">", and "x" is not clear in lines 19-34. See comments on page 76.	Add explanation of what "+", "x", and ">" mean in the context of this text	PROPOSED ACCEPT IN PRINCIPLE. Insert line between line 19 and 21: "where + indicates an integer addition." Replace line 21 with "-1 if (DS _n > 0) AND (RD _{n-1} > 0 OR (RD _{n-1} = 0 AND Sg _n = 1))) Meaning of ">" is clear in the context of a conditional.	Editorial		
265	Jonsson, Ragnar	Infineon	190 190.4.4.2	102	1	T	The statement "At any time during start-up, if the local receiver status (indicated by loc_rcvr_status) transitions to NOT_OK, PHY Control returns to the LINK_FAIL state and waits for the link_fail_inhibit_timer to expire and Auto-Negotiation to restart." is not entirely consistent with the state diagram in Figures 190-17 through 190-19, where there are states that cannot transition to the LINK_FAIL state.	Make the text and the state diagrams consistent.	PROPOSED REJECT. Figure 190-20 shows that link_status goes to FAIL in this condition. Auto-negotiation will then set link_control to DISABLE, resetting, figure 190-17, per figure 98-7. (see arc from AN_GOOD to TRANSMIT DISABLE state).	State Diagrams		
266	Law, David	HPE	190 190.3.1	60	50	T	Subclause 190.3.1 'PCS Reset function' defines when pcs_reset = TRUE but not when pcs_reset = FALSE.	For completeness, suggest that '... while any of the above reset conditions holds true.' should be changed to read '... while any of the above reset conditions holds true, and set pcs_reset = FALSE' otherwise.	PROPOSED ACCEPT.	PCS		

267	Law, David	HPE	190	190.3.2	60	54	T	I could not find a specification of the TX_CLK and RX_CLK clocks generated by the PCS transmit and receive functions, respectively, illustrated in Figure 190-3. Suggest that similar text to that found in the second paragraph of IEEE Std 802.3-2022 subclause 24.2.2.3 'Data delay' is included, with a reference to 190.4.2 for TX_CLK.	Suggest inserting a new subclause as follows: 190.3.2 PCS Clock function The PCS shall generate the TX_CLK (see 190.4.2) and RX_CLK in accordance with Clause 22.	PROPOSED ACCEPT.	PCS
268	Law, David	HPE	190	190.3.2.2	63	6	T	Figure 190–4 'PCS Transmit bit ordering' labels the initial transfer TXD<0> to TXD<3> bits across the MII as the 1st transfer, the following MII transfer as the 2nd and then the penultimate MII transfer as the (2N)th transfer, since it appears to be above the leftmost 4 bits of the 8 bits shown. Isn't the penultimate MII transfer (leftmost 4 bits of the 8 bits) the (2N -1) transfer, and the final MII transfer (rightmost 4 bits of the 8 bits) should be the (2N)th transfer?	Suggest that: [1] The text '(2N)th transfer' should be changed to read '(2N - 1)th transfer' and centred over the middle of the leftmost 4 bits of the 8 bits. [2] The text '(2N)th transfer' should be added above the middle of the rightmost 4 bits of the 8 bits.	PROPOSED ACCEPT.	Editorial
269	Law, David	HPE	190	190.3.2.2	63	11	T	Figure 190–4 'PCS Transmit bit ordering' shows tx_coded as the 'Output of block encoder'. Isn't, however, tx_coded the output of the Figure 190–11 'PCS (8N)B/(8N+1)B Transmit state diagram', and the block encoding, defined in subclause 190.3.2.4, performed by the ENCODE(tx_mii) function in the 'PCS (8N)B/(8N+1)B Transmit state diagram'. Furthermore, aren't there cases when block coding of tx_mii isn't performed, for example, after reset, before tx_mode is set to SEND_N, tx_code is set to RBLOCK_T.	Suggest that 'Output of block encoder' should be changed to read 'Output of PCS (8N)B/(8N+1)B Transmit state diagram'.	PROPOSED ACCEPT.	PCS
270	Law, David	HPE	190	190.3.2.6	70	7	E	The terminology 'auxiliary bit' (page 70, line 7, 'aux' (page 70, line 13) and 'aux bit' (page 70, line 24) is used interchangeably. Further, 'auxiliary bit' is defined as 'aux' (page 61, line 17) and then 'aux' is defined as 'the auxiliary bit' (page 70, line 21). If 'aux' is defined as the 'auxiliary bit', wouldn't the expansion for 'aux bit' (page 70, line 24) 'auxiliary bit bit'?	Since 'aux bit' is only used three times, suggest it is expanded to 'auxiliary bit' and that '... an auxiliary bit (aux) to ...' on page 61, line 17 is changed to read '... an auxiliary bit to ...'.	PROPOSED ACCEPT.	EZ
271	Law, David	HPE	190	190.3.2.12	77	13	E	Suggest that the source of eee_low_snr parameter should be noted.	Suggest that 'The eee_low_snr parameter communicated through the PMA_EEE_LOW_SNR.indication primitive ...' should be changed to read 'The eee_low_snr parameter generated by the PMA receive function and communicated through the PMA_EEE_LOW_SNR.indication primitive ...'.	PROPOSED ACCEPT.	EZ
272	Law, David	HPE	190	190.3.6.1.2	89	47	T	Based on the description in subclause 190.3.1 'PCS Reset function' and its use in the state diagrams, it appears that pcs_reset is a Boolean. Suggest that a cross-reference to subclause 190.3.1 be added to the definition of the pcs_reset variable since subclause 190.3.1 'PCS Reset function' defines the conditions under which pcs_reset is set to TRUE.	Suggest that 'Variable used by ...' should be changed to read 'Boolean variable used by ...'.	PROPOSED ACCEPT.	State Diagrams
273	Law, David	HPE	190	190.3.6.1.2	89	48	T		Add the text 'See 190.3.1' to the end of the definition of the pcs_reset variable.	PROPOSED ACCEPT.	EZ

								<p>The description of the rx_char variable in subclause 190.3.6.1.2 'Variables' says that it is a 'Structure representing one of the N characters that are output by the (8N)B/(8N + 1)B decoder' without defining which of the N characters. I believe that it is the reverse of the process described in subclause 190.3.2.4 'Block encoding' and involves unpacking the N values from an 8N + 1 bit block every 2N RX_CLK cycles.</p> <p>I believe that this is covered in the penultimate paragraph of 190.3.3 'PCS Receive function' which says 'Every 2N RX_CLK cycles, an (8N+1)B block is received and is decoded to generate a list of N characters, each of which represents either a data octet or a control symbol. These characters are mapped one at a time into the rx_char structure, which is processed in accordance with Figure 190–13 to generate signals at the MII.'</p>	<p>Suggest that since rx_coded, including the transmission order, is defined in subclause 190.3.2.3 'Notation conventions', the following is added to the description of the rx_char variable:</p> <p>A (8N+1)B block represented by rx_coded<0:8N> (see 190.3.2.3) is received every 2N RX_CLK cycles. The 9-bit character represented by rx_char is extracted from rx_coded<0:8N> every 2 RX_CLK cycles. The Boolean value of rx_char is extracted from rx_coded<0>, the 8-bit numerical value of rx_char is extracted from rx_coded<8N + 1:8N + 9>.</p> <p>Change '... encoder as described in 190.3.3.4' to read '... encoder as described in 190.3.2.4.'</p> <p>.....</p> <p>'Variable set by the <function_name> function and communicated through the <parameter_name> parameter of the <primitive name> primitive. See <primitive definition subclause>:.'</p> <p>As a result, suggest that the following variables are updated to read as noted:</p> <p>tx_info_frame_end</p> <p>Variable set by the PCS Transmit function and communicated through the tx_info_frame_end parameter of the PMA_TXINFOFRAMEEND.request primitive. See 190.2.2.14.</p> <p>tx_mode</p> <p>Variable set by the PHY control function and communicated through the tx_mode parameter of the PMA_TXMODE.indication primitive. See 190.2.2.2.</p> <p>eee_low_snr</p> <p>Variable set by the PMA Receive function and communicated through the eee_low_snr parameter of the PMA_EEE_LOW_SNR.indication primitive. See 190.2.2.17.</p> <p>rx_lpi_active</p> <p>Variable set by the PMA Receive function and communicated through the rx_lpi_active parameter of the</p>	<p>PROPOSED ACCEPT IN PRINCIPLE.</p> <p>TFTD.</p> <p>Suggested remedy appears to be correct, but re-introduces rx_coded and may create other issues.</p> <p>Discuss with comment 84.</p> <p>PROPOSED ACCEPT.</p>	<p>PCS</p> <p>EZ</p>	
274	Law, David	HPE	190	190.3.6.1.2	89	49	TR					
275	Law, David	HPE	190	190.3.6.1.2	90	5	E	Incorrect cross-reference.				
276	Law, David	HPE	190	190.3.6.1.2	90	25	T	<p>The definition of variables passed in primitives across the PMA service interface seems to vary. As an example, eee_low_snr is defined as a 'Parameter set by the PMA Receive function and communicated through the PMA_EEE_LOW_SNR.indication primitive.', yet tx_mode is described as a 'Variable set by the PHY control function and communicated through the PMA_TXMODE.indication primitive.'. While both are communicated through a primitive, these are state diagram variables as noted by the subclause 190.3.6.1.2 title 'Variables'. Further, subclause 190.2.2.2.2 'When generated' says 'The PHY Control function generates this primitive to indicate a change in tx_mode.', and subclause 190.2.2.17.2 'When generated' says 'The PMA generates PMA_EEE_LOW_SNR.indication messages to indicate a change in the eee_low_snr variable.'</p>	<p>PROPOSED ACCEPT.</p>	<p>State Diagrams</p>		

									The definition of rem_eee_low_snr says that it is a 'Variable set by the PMA Receive function ...'. Subclause 190.3.2.12 'EEE capability' says that 'The aux bit of every group of transmit bits, tx_group, is set to 1 when eee_low_snr is TRUE and is set to 0 otherwise.' and 'The variable rem_eee_low_snr indicates the value of the eee_low_snr variable communicated by the remote PHY.'. Since the PMA Receive function operates at a symbol level, generating rx_symb parameters communicated to the PCS through the PMA_UNITDATA.indication primitive, I don't believe the PMA Receive function can extract the aux bit. Instead, I believe that the rem_eee_low_snr variable is extracted by the PCS Receive function. In addition, it should be noted that rem_eee_low_snr is a Boolean variable. The definition of the rx_lpi_active variable says that it is '... set by the PMA Receive function ...', but that 'The parameter is set ... in each state of the PCS Receive state diagram ...'. The latter seems correct since subclause 190.2.2.15 'PMA_PCS_RX_LPI_STATUS.request' says the PMA_PCS_RX_LPI_STATUS.request primitive, which passes the rx_lpi_active parameter, '... is generated by the PCS Receive ...' and Figure 190–21 'EEE Refresh monitor state diagram', a PMA state diagram uses the rx_lpi_active value in state transitions.	Suggest that:	[1] The text 'Variable set by the PMA Receive function ...' should be changed to read 'Boolean variable set by the PCS Receive function ...'. [2] The text 'See 190.3.2.12.' should be added to the end of the description of the rem_eee_low_snr variable. [3] A line from the PCS RECEIVE block to the PCS TRANSMIT block labelled 'rem_eee_low_snr' should be added to Figure 190-3 'PCS reference diagram'.	PROPOSED ACCEPT.	PCS
277	Law, David	HPE	190	190.3.6.1.2	90	30	TR						
278	Law, David	HPE	190	190.3.6.1.2	90	33	T			Suggest that '... set by the PMA Receive function ...' is changed to read '... set by the PCS Receive function ...'.	PROPOSED ACCEPT.	PCS	
										Suggest that the following definition be added to subclause 190.3.6.1.2 'Variables':			
										loc_phy_ready			
279	Law, David	HPE	190	190.3.6.2	94	3	T		The variable loc_phy_ready is used in Figure 190–11 'PCS (8N)B/(8N+1)B Transmit state diagram' but does not appear to be defined in the associated subclause 190.3.6.1.2 'Variables'.	Variable set to the value of the loc_phy_ready parameter generated by the PHY Control function and communicated through the PMA_LOCPHYREADY.indication primitive. See 190.2.2.12.	PROPOSED ACCEPT.	State Diagrams	
									Figure 190–12 'EEE Transmit state diagram' uses the tx_lpi_alert_active variable, setting it TRUE in the SEND_ALERT state, then FALSE in the SEND_WAKE state. The viable tx_lpi_alert_active is not defined in 190.3.6.1.2 'Variables'. The variable tx_alert_active is defined in 190.3.6.1.2 'Variables' but is not used in any of the state diagrams.				
									Since the description of the tx_alert_active variable says it '... is set TRUE in the LPI transmit mode, when the PHY is transmitting alert signaling ...' and '... set FALSE otherwise.', this appears to be the same as the tx_lpi_alert_active variable used in Figure 190–12	Since the other LPI signalling related variables include _lpi_ (e.g., tx_lpi_active, tx_lpi_qr_active, rx_lpi_active, and rx_lpi_sleep), suggest that all instances of tx_alert_active be changed to read tx_lpi_alert_active.	PROPOSED ACCEPT. See comment 170	State Diagrams	
280	Law, David	HPE	190	190.3.6.2	95	8	TR						

281	Law, David	HPE	190	190.3.6.2	98	3	T	The variable link_status is used in Figure 190–1 'PCS Receive state diagram' and Figure 190–15 'PCS RFER Monitor state diagram' but does not appear to be defined in the associated subclause 190.3.6.1.2 'Variables'.	Suggest that the following definition is added to subclause 190.3.6.1.2 'Variables': link_status Variable set to the value of the link_status parameter generated by the Link Monitor function and communicated through the PMA_LINK.indication primitive. See 190.2.1.2.	PROPOSED ACCEPT.	State Diagrams
282	Law, David	HPE	190	190.4.1	100	7	T	Subclause 190.4.1 'PMA Reset function' defines when pma_reset = TRUE but not when pma_reset = FALSE.	For completeness, suggest that '... while any of the above reset conditions holds TRUE.' Should be changed to read '... while any of the above reset conditions holds true, and set pma_reset = FALSE' otherwise.	PROPOSED ACCEPT. PROPOSED ACCEPT IN PRINCIPLE. (typo in response said PCS Reset) Add to subclause 190.4.9.1.1 'Variables':	PMA
									Suggest that the following be added to subclause 190.4.9.1.1 'Variables':		
283	Law, David	HPE	190	190.4.9.1.1	103	22	T	The variable pma_reset appears to be missing from subclause 190.4.9.1.1 'Variables' list defining the PMA state diagram variables.	pma_reset Boolean variable used by PCS Reset to initialize all PCS functions. See 190.4.1. Suggest that the following be added to subclause 190.4.9.1.1 'Variables':	pma_reset Boolean variable used by PMA Reset to initialize all PMA functions. See 190.4.1.	State Diagrams
284	Law, David	HPE	190	190.4.9.1.1	103	22	T	The variable rx_lpi_active, used in Figure 190-21 'EEE Refresh monitor state diagram', appears to be missing from subclause 190.4.9.1.1 'Variables' list.	rx_lpi_active Variable set by the PCS Receive function and communicated through the rx_lpi_active parameter of the PMA_PCS_RX_LPI_STATUS.request primitive. See 190.2.2.15.	PROPOSED ACCEPT. PROPOSED ACCEPT.	State Diagrams
285	Law, David	HPE	190	190.4.9.1.1	104	30	E	Change 'timing_locked:' to read 'timing_locked'. Change 'SEND_IDLE_NOT_READY' to read 'SEND_IDLE_NOT_READY' (remove space between 'IDLE' and '_NOT').	See comment.	PROPOSED ACCEPT.	EZ
286	Law, David	HPE	190	190.4.9.2	107	16	E	Change 'loc_phy_ready <= true' to read 'loc_phy_ready <= TRUE'.	See comment.		
287	Law, David	HPE	190	190.4.9.2	108	11	E	The Clause 22 MII TX_CLK is sourced by the PHY (see IEEE Std 802.3 subclause 22.2.2.1). Consequently, the arrow on TX_CLK in Figure 190–2 is incorrectly oriented.	See comment.	PROPOSED ACCEPT.	EZ
288	Law, David	HPE	190	190.2.2	51	8	T		Correct the direction of the TX_CLK arrow.		

										<p>Subclause 190.2.2.15</p> <p>'PMA_PCS_RX_LPI_STATUS.request' says '... this primitive is generated by the PCS Receive function ...' and that '... PMA_PCS_RX_LPI_STATUS.request conveys to the PCS Transmit and PMA Receive functions ...'. Since the PMA_PCS_RX_LPI_STATUS.request primitive is part of the PMA service interface between the PCS and PMA, and since both the PCS Transmit function and PCS Receive function are above the PMA service interface, I don't believe that the '... PMA_PCS_RX_LPI_STATUS.request conveys to the PCS Transmit ...'. Instead, if the rx_lpi_active variable is used by the PCS Transmit function, the rx_lpi_active variable generated in the PCS Receive function by the PCS Receive state diagram can be connected directly to the PCS Transmit function.</p>										
										<p>However, upon reviewing the PCS Transmit function and its associated state diagrams, I don't believe the rx_lpi_active variable is utilised by the PCS Transmit function. As a result, reference to the PCS Transmit function should be removed. In addition, PMA_PCS_RX_LPI_STATUS.request is a primitive, not a parameter.</p>	<p>Suggest that 'The parameter PMA_PCS_RX_LPI_STATUS.request conveys to the PCS Transmit and PMA Receive functions information regarding whether the PCS Receive function is in the LPI receive mode.' is changed to read 'The PMA_PCS_RX_LPI_STATUS.request primitive conveys whether the PCS Receive function is in the LPI receive mode to the PMA Receive function.'</p>	<p>PROPOSED ACCEPT.</p> <p>TFTD (review whether there is something missing here)</p>								
289	Law, David	HPE	190	190.2.2.15	58	29	T				PCS									

Topic	Count
AutoNeg	1
Downshift	1
Editorial	59
EMC	4
EZ	122
Link Segment	5
Management	13
MDI	5
PCS	20
PMA	8
PMA Electrical	4
Power	2
Reduced TX level	5
RS-FEC	17
State diagrams	14
Test modes	3

Clause	Count
FM	5
0	2
1	14
22	2
30	7
45	19
78	2
98	9
104	9
190	216
98B	4

Category	Count
TR	52
ER	10
GR	0
T	89
E	138
G	0