

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl FM SC FM P11 L3 # R1-270
 Dawe, Piers J G NVIDIA
Comment Type E Comment Status A (B) (CG)
 Amendment:
SuggestedRemedy
 Amendment 13,
Response Response Status C
 ACCEPT.

Cl 00 SC 0 P14 L16 # R1-190
 Maguire, Valerie Copperopolis
Comment Type E Comment Status A (B) (CG)
 Final FM files have been generated by Pete Anslow and reviewed by the P802.3dg Editorial team. It seems impossible that IEEE Std 802.3dg won't publish this year.
SuggestedRemedy
 Replace "20xx" with "2026"
Response Response Status C
 ACCEPT IN PRINCIPLE.
 Change all instances of "802.3dg-20xx" to "802.3dg-2026".

Cl 45 SC 45.2.1.168c P102 L28 # R1-171
 Slavick, Jeff Broadcom Inc
Comment Type E Comment Status R (Bp) (L)
 on lane # is not necessary in the description for bit 7:0 as the Name column already specifies which lane it is
SuggestedRemedy
 Remove "on lane #" from the description column for bits 7:0 in Table 45-133c
Response Response Status C
 REJECT.
 This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.
 The draft is correct as written. There is no consensus to make the recommended change.

Cl 45 SC 45.2.1.168e P104 L13 # R1-173
 Slavick, Jeff Broadcom Inc
Comment Type ER Comment Status A (B) (L)
 In Table 45-133e adjacent los mode uses RW while the other bits (and footnote) use R/W
SuggestedRemedy
 make the RW a R/W
Response Response Status W
 ACCEPT.

CI 45 SC 45.2.1.272 P126 L # R1-3

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A (B) (L)

D3.1 renamed Table 45-212u and its register-row descriptions/variables from generic "PMA test block error bin" to "PMA transmit test block error bin" (variable suffix `_tx_`), but the enclosing narrative was not updated. The subclause heading 45.2.1.272 ("PMA test block error bin counter registers"), the descriptive and intro sentences on p126, the ToC entry (p18), and the 45.8 register-address summary (p78) all still read generic "PMA test block". With a tx/rx register split present (rx registers 1.3308+ also map to 45.2.1.272), the lone "transmit"-titled table does not match its generic-named subclause and no parallel receive table is shown. In D3.0 (Table 45-212t, 45.2.1.271) everything was consistently generic.

SuggestedRemedy

Make naming consistent across the subclause: either (a) keep "PMA transmit" on the tx table, add a matching "PMA receive test block" table/text for the `_rx_` registers, and update the 45.2.1.272 heading and intro to describe both directions; or (b) revert Table 45-212u title and row descriptions to generic "PMA test block". In either case align the heading (45.2.1.272), the p126 narrative, the ToC (p18), and the 45.8 summary row (p78).

Response Response Status C

ACCEPT IN PRINCIPLE.

In Table 45-3

Change "1.2900 through 1.3715 | PMA test block error bin counters | 45.2.1.272"

To: "1.2900 through 1.3307 | PMA transmit test block error bin counters | 45.2.1.272"

Insert new row below this in Table 45-3:

"1.3308 through 1.3715 | PMA receive test block error bin counters | 45.2.1.272"

In 45.2.1.272:

Create new "Table 450212vùLane 0 PMA receive test block error bin register definitions (for 4 of the 17 bins)" similar to "Table 450212uùLane 0 PMA transmit test block error bin register definitions (for 4 of the 17 bins)"

In this new table:

replace "transmit" with "receive" and "tx" with "rx"

offset the register numbers offset by 408. So for example "1.2900.15:0" becomes

"1.3308.15:0"

Change the third paragraph of 45.2.1.272 from:

Table 450212u shows the assignment of bits for the lane 0 PMA test block error bin 0 to bin 3 registers in the transmit direction. Lane 0 error bin registers end at 1.2950 with test_block_error_bin_tx_0_16p upper. Lane 1 error bin registers start at 1.2951 with test_block_error_bin_tx_1_0 lower. The transmit direction error bin counter registers span from 1.2900 to 1.3307, the receive direction registers span from 1.3308 to 1.3715.

To:

Table 450212u shows the assignment of bits for the lane 0 PMA test block error bin 0 to bin 3 registers in the transmit direction. Lane 0 error bin registers end at 1.2950 with test_block_error_bin_tx_0_16p upper. Lane 1 error bin registers start at 1.2951 with test_block_error_bin_tx_1_0 lower. The transmit direction error bin counter registers span from 1.2900 to 1.3307. Table 450212v shows the assignment of bits for the lane 0 PMA test block error bin 0 to bin 3 registers in the receive direction. The receive direction registers span from 1.3308 to 1.3715.

Implement with editorial licence.

[Editor's note: changed page from 18 to 126]

CI 45 SC 45.2.3.1 P128 L37 # R1-71

Brown, Matthew

Qualcomm

Comment Type E Comment Status A (B) (L)

The editor's note has served its purpose.

SuggestedRemedy

Delete editor's note.

Also, delete similar editors notes at the following locations:

page 129 line 38

page 131 line 40 and line 51

page 132 line 47

page 133 line 41

page 134 line 46

page 135 line 12 and line 50

page 136 line 45

page 137 line 12

Response Response Status C

ACCEPT.

CI 120 SC 120 P203 L # R1-4

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A (B) (CG)

On D3.1 pages 203 and 204 the running header/footer carries the wrong project designation: header "IEEE Draft P802.3df/D3.1" and footer "IEEE P802.3df 400 Gb/s and 800 Gb/s Ethernet Task Force". Adjacent dj pages (200-202, 205-209) correctly read "IEEE Draft P802.3dj/D3.1" and "IEEE P802.3dj 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet Task Force". In D3.0 both pages were correct; the D3.1 date-update edit introduced the stale "df" designation.

SuggestedRemedy

Correct the page 203 and 204 header to "IEEE Draft P802.3dj/D3.1" and footer to "IEEE P802.3dj 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet Task Force" to match all adjacent pages.

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 120F SC 120F.1 P733 L26 # R1-202

Dudek, Michael

Marvell

Comment Type TR Comment Status A (B) (CG)

Figure 120F-1 (and Figure 120G-1) show the 1.6TAUI-16 between BM-PMA's however the rest of the draft indicates that the 1.6TBASE-R SM-PMA is used.

SuggestedRemedy

Add a footnote to the 1.6TAUI-16 indications in Figures 120F-1 and 120F-2 stating that the 1.6TAUI-16 SM-PMA is used. Or probably better replace BM-PMA with PMA and add a footnote to that stating. "BM-PMA's are used for 200GAUI-2, 400GAUI-4 and 800GAUI-8, the 1.6GAUI-16 uses the SM-PMA. Consider also

Response Response Status W

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft.

In Figure 120F-1 and Figure 120G-1, change all instances of BM-PMA to PMA.

[Editor's note: changed clause/subclause/page/line from 180/180.1/463/49 to 120F/120F.1/733/26]

Cl 169 SC 169.2.10 P210 L40 # R1-169

Slavick, Jeff

Broadcom Inc

Comment Type TR Comment Status A APSU wording (CA)

800GBASE-ER1 does not use the ILT and RTS functions. APSU is the result of establishing end to end communication. ILT and RTS functions provide means to establish communication across ISLs, but are not required to do so (like in the case of ER1).

SuggestedRemedy

Update 116.2.9, 169.2.10 and 174.2.12

Autonomous path start-up (APSU) is the orderly startup of the path, which occurs when SIGNAL_OK reaches the OK status at every sublayer interface in both directions. This requires communication to be established across all ISLs within the path (see 178B.3) along with all sublayers transmit and receive paths operating as expected. PHYs and Physical layers capable of sending SIGNAL_OK in both the transmit and receive directions are able to make use of APSU.

Annex 178B defines the RTS and ILT functions that facilitate establishing the communication and the transfer of SIGNAL_OK across ISLs found within a path. These functions are supported by many PMD and AUI components that utilize various types of medium.

Details of how each PMD or AUI component supports APSU are found in the relevant clauses.

APSU is specified for use by the following PMD and AUI types:

<existing list of PMD and AUI types>

Update 178B.1 to be:

This annex defines the functions to support autonomous path startup (APSU) for Physical Layer implementations that include one or more inter-sublayer (ISLs) (see 178B.3) with data rate of 200 Gb/s.

Update 178B.2 first paragraph to be:

APSU is the coordinated, orderly initialization of all ISLs and sublayers in a path (Figure 178B.1). APSU is facilitated by the combination of the ready-to-send (RTS) function and the inter-sublayer link training (ILT) function. APSU is described in 178B.4.

Update 178B.4 to be:

APSU is not intrinsically a function, it is an externally observable behavior resulting from establishing communication through all sublayers in the path in both directions. Support for APSU is defined as follows:

• An ISL can be activated using APSU if the two interfaces and the associated sublayers (e.g. PMA, Inner FEC), implement the RTS function (see 178B.6) and the ILT function (see 178B.7), or have equivalent functions.

• A PHY can be activated using APSU if every ISL and sublayer within the PHY can be activated using APSU.

ù An xMII Extender can be activated using APSU if every ISL and sublayer within it can be activated using APSU.

ù A Physical Layer can be activated using APSU if the PHY and xMII Extender (if implemented) can be activated using APSU.

ù A path can be activated using APSU if the Physical Layer at each end can be activated using APSU.

A path that does not meet the above criteria is not defined to support APSU. An ISL, PHY, Physical Layer, or path that cannot be activated using APSU may be activated using management or other means beyond the scope of this annex.

APSU is the result of SIGNAL_OK for every sublayer interface reaching the OK status.

This occurs when:

ù Each ISL establishes communication in both directions. For PMD and AUI components using the ILT and RTS functions local_rts and remote_rts are both true.

ù Each sublayer is receiving and transmitting data of the expected format.

ù End-to-end communication between the RS sublayers is established.

Response **Response Status C**

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-140.

Cl 169 SC 169.3.2 P211 L45 # R1-214

Law, David Hewlett Packard Enterprise

Comment Type E Comment Status A (B) (CG)

Since a new Figure 169-2a is being inserted after Figure 169-2 (see page 213), suggest that '... are illustrated in Figure 169-2, Figure 169-3, Figure 185-2 ...' should read '... are illustrated in Figure 169-2, Figure 169-2a, Figure 169-3, Figure 185-2 ...'.

SuggestedRemedy

See comment

Response Response Status C

ACCEPT IN PRINCIPLE.

Update text as proposed in the comment field with editorial license.

Cl 169 SC 169.4 P216 L1 # R1-53

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A delay (B) (CG)

Table 169-4 lists 96 pause_quanta for all seven 800GBASE 4-lane PMD rows: 800GBASE-KR4, 800GBASE-CR4, 800GBASE-DR4, 800GBASE-FR4-500, 800GBASE-DR4-2, 800GBASE-FR4, and 800GBASE-LR4. The correct value is 116 pause_quanta: these rows all have 59,392 bit times and 74.24 ns. Verification: $59,392 / 512 = 116$; $116 \times 640 \text{ ps} = 74.24 \text{ ns}$ ($640 \text{ ps} = 1 \text{ pause_quantum}$ at 800 Gb/s). With 96 pause_quanta: $96 \times 640 \text{ ps} = 61.44 \text{ ns}$, inconsistent with 74.24 ns. The individual PMD clause delay tables (178.6, 179.6, 180.4.1, 181.4.1, 182.4.1) all correctly state 116 pause_quanta for the same entries.

SuggestedRemedy

In Table 169-4, change 96 pause_quanta to 116 pause_quanta for all seven 800GBASE 4-lane PMD rows: 800GBASE-KR4, CR4, DR4, FR4-500, DR4-2, FR4, and LR4.

Response Response Status W

ACCEPT IN PRINCIPLE.

Note that comment R1-54 corrects the delay in 183.4.1.

Implement the suggested remedy with editorial license.

Cl 169 SC 169.8 P221 L50 # R1-310

Opsasnick, Eugene

Broadcom Inc.

Comment Type E Comment Status A (B) (CG)

The editing instruction says to change 169.6, but is located in section 169.8

SuggestedRemedy

Change "169.6" to 169.8" in the first line of 169.8.

Response Response Status C

ACCEPT.

Cl 169 SC 169.8 P221 L53 # R1-311

Opsasnick, Eugene

Broadcom Inc.

Comment Type E Comment Status A (B) (CG)

The newly inserted text in this paragraph is not underlined.

SuggestedRemedy

Underline the inserted text around "and Clause 176 through Clause 187".

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 171 SC 171.2 P232 L29 # R1-43
 Bruckman, Leon NVIDIA
Comment Type TR Comment Status A (B) (L)
 Nomenclature error: the text refers to 'DTE 1.6XS' (missing the letter 'T'). The correct standardized sublayer name is 'DTE 1.6TXS' throughout all other uses in this clause and document.
SuggestedRemedy
 Change 'DTE 1.6XS' to 'DTE 1.6TXS'.
Response Response Status W
 ACCEPT IN PRINCIPLE.
 Implement the suggested remedy where applicable throughout the draft. This includes Figure 171-3a.

Cl 171 SC 171.6.1 P239 L # R1-5
 Hajduczenia, Marek RG Nets
Comment Type E Comment Status R (B) (L)
 The where-clause for tx_am_sf was botched in the D3.0->D3.1 edit - it seems that the "where" statement is missing and the newly inserted text should be part of the "tx_am_sf" variable definition, and not as a self standing text
SuggestedRemedy
 Fix per comment + the same issue in 171.6.2
Response Response Status C
 REJECT.
 These changes were made in response to comments I-399 and I-400 against D3.0, to improve readability, as recorded in the following report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf.
 Adding "where" to the beginning of the sentence following the variable assignment is not necessary and does not improve the accuracy or clarity of the draft.

Cl 171 SC 171.7 P241 L26 # R1-44
 Bruckman, Leon NVIDIA
Comment Type TR Comment Status A (B) (L)
 Incorrect figure insertion instruction: the amendment text says 'Insert new Figure 171-3a after Figure 173-3' but Figure 171-3a logically belongs in Clause 171 and should be inserted after Figure 171-3, not after Figure 173-3 (which is in Clause 173, page 273).
SuggestedRemedy
 Change 'Insert new Figure 171-3a after Figure 173-3' to 'Insert new Figure 171-3a after Figure 171-3'.
Response Response Status W
 ACCEPT.

Cl 173 SC 173.4.2 P273 L1 # R1-215
 Law, David Hewlett Packard Enterprise
Comment Type E Comment Status A (B) (L)
 The editing instructions for the figure in subclause 173.4.2 '8:32 PMA' say 'Replace Figure 17303 with the following', and the title of Figure 17303 is '8:32 PMA functional block diagram'. On review of the published IEEE Std 802.3-2024, I believe that Figure 17303 is '32:8 PMA functional block diagram', and it is Figure 17304, which is part of subclause 173.4.2 '8:32 PMA', that is '8:32 PMA functional block diagram'.
SuggestedRemedy
 Suggest that 'Replace Figure 17303 with the following:' should read 'Replace Figure 17304 with the following' and that 'Figure 1730308:32 PMA functional block diagram' should read 'Figure 1730408:32 PMA functional block diagram'.

Response Response Status C
 ACCEPT.
Cl 173 SC 173.4.2 P1273 L1 # R1-222
 Law, David Hewlett Packard Enterprise
Comment Type T Comment Status R (withdrawn) (L)
 The editing instructions for the figure in subclause 173.4.2 '8:32 PMA' say 'Replace Figure 17303 with the following', and the title of Figure 17303 is '8:32 PMA functional block diagram'. On review of the published IEEE Std 802.3-2024, Figure 17303 is '32:8 PMA functional block diagram', and it is Figure 17304, which is part of subclause 173.4.2 '8:32 PMA', that is '8:32 PMA functional block diagram'.
SuggestedRemedy
 Suggest that 'Replace Figure 17303 with the following:' should read 'Replace Figure 17304 with the following' and that 'Figure 1730308:32 PMA functional block diagram' should read 'Figure 1730408:32 PMA functional block diagram'.

Response Response Status Z
 REJECT.
 This comment was WITHDRAWN by the commenter.

Cl 174 SC 174.1.4 P275 L35 # R1-39

Bruckman, Leon

NVIDIA

Comment Type T Comment Status A (B) (CG)

Tables cited in reversed order. The text reads 'Table 174-3 and Table 174-2 specify the correlation between PHY types and clauses.' Per standard document convention, tables are cited in ascending numerical order. Table 174-2 (electrical PHY types) appears first in the document, followed by Table 174-3 (optical PHY types).

SuggestedRemedy

Change 'Table 174-3 and Table 174-2' to 'Table 174-2 and Table 174-3'.

Response Response Status C

ACCEPT.

Cl 174 SC 174.2.5 P277 L37 # R1-40

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A (B) (CG)

Wrong annex reference for the 1.6TAUI-16 C2M. The text states 'The 1.6TAUI-16 C2M is specified in Annex 176D,' but Annex 176D covers 200GAUI-1 C2M, 400GAUI-2 C2M, 800GAUI-4 C2M, and 1.6TAUI-8 C2M -- the 8-lane variant, not the 16-lane. The immediately following line (line 41) also references Annex 176D for 1.6TAUI-8 C2M (correct), making the duplication obvious. The 1.6TAUI-16 C2M is consistently referenced as Annex 120G throughout the document (TOC, line 3649, lines 17530, 33782, 38873).

SuggestedRemedy

Change 'The 1.6TAUI-16 C2M is specified in Annex 176D.' to 'The 1.6TAUI-16 C2M is specified in Annex 120G.'

Response Response Status W

ACCEPT.

Cl 174 SC 174.2.13 P279 L7 # R1-67

Bruckman, Leon

NVIDIA

Comment Type E Comment Status R (Bp) (CG)

Although in the base standard in similar sections titles the word "Degrade" is capitalized, it seems not necessary.

SuggestedRemedy

Change: "FEC Degrade" to "FEC degrade"

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 174 SC 174.4 P283 L35 # R1-41

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A (B) (CG)

Table 174-4 AUI row: Name '1.6TAUI-4 C2C or C2M' -- the interface 1.6TAUI-4 does not exist; the standard defines 1.6TAUI-8 and 1.6TAUI-16.

SuggestedRemedy

Replace the row with: Sublayer='1.6TAUI-8 C2C or C2M component', bit times=73 728, pause_quanta=144, ns=46.08. Notes column unchanged.

Response Response Status W

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 174 SC 174.5 P287 L28 # R1-42

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A skew (B) (CG)

Table 174-6 footnote e states '1 UI equals 8.31543 ps at PMD lane signaling rate of 113.4375 GBd.' The correct value is 8.81543 ps (1 / 113.4375e9 = 8.81543e-12 s). The table body confirms the error: SP3=68 UI and SP4=386 UI are consistent only with 8.81543 ps (0.6 ns / 8.81543 ps = 68.1; 3.4 ns / 8.81543 ps = 385.7), not with 8.31543 ps (which gives 72 and 409). The same '8.31543' typo appears in Table 116-6 (doc line 13668) and Table 169-6 (doc line 16352).

SuggestedRemedy

In Table 174-6 footnote e, change '8.31543 ps' to '8.81543 ps'. Apply the same correction to the identical footnote in Table 116-6 and Table 169-6.

Response Response Status W

ACCEPT IN PRINCIPLE.

In Table 174-6 footnote e, change '8.31543 ps' to '8.81543 ps'.

Apply the same correction in Table 116-9 and Table 169-6.

Implement with editorial license.

Cl 174A SC 174A.9.6 P753 L1 # R1-300

Dawe, Piers J G

NVIDIA

Comment Type ER Comment Status A (B) (CG)

D3.0 comment 421 (174A.9 is not as user-friendly as it should be): I think it has been noted before that the order of subclauses here does not represent their contents. 174A.9.5, Error mask test method using PMA measurements, says "If this test fails, then the performance may be further verified using the method in 174A.9.6 or 174A.9.7." 174A.9.7, BLER method for a single lane using PMA measurements, says "ratio.If this test fails, then the performance may be further verified using the method in 174A.9.6." 174A.9.6, BLER method for all lanes using PMA measurements, doesn't: it's the end of the line.

SuggestedRemedy

Swap 174A.9.6 and 174A.9.7.

Insert a space after "ratio."

Improve the subclause titles to match their contents and relate to each other:

174A.9.5--Error mask test method *for each lane* using PMA measurements

174A.9.7 to be 6--BLER method for *each* lane using PMA measurements

174A.9.6 to be 7--BLER method for all lanes using PMA measurements

or

174A.9.5--*Lane-by-lane* error mask test method using PMA measurements

174A.9.7 to be 6--*Lane-by-lane* BLER method for using PMA measurements

174A.9.6 to be 7--BLER method for all lanes using PMA measurements

Response Response Status W

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the changes proposed in the suggested remedy are improvements to the draft.

On page 753, line 27 add a space between "ratio." and "If".

Change the title of 174A.9.5 to "Error mask test method for each lane using PMA measurements"

Change the title of 174A.9.7 to "BLER method for each lane using PMA measurements"

Swap the order of 174A.9.6 and 174A.9.7.

Implement with editorial license.

Cl 175 SC 175.1.4 P289 L23 # R1-61

Bruckman, Leon

NVIDIA

Comment Type E Comment Status A (B) (L)

Typo: 'The lower interface of the PCS connects to a PMA or Inner FEC though the service interface below the PCS.' -- 'though' should be 'through'. The immediately preceding sentence correctly uses 'through'.

SuggestedRemedy

Change 'though the service interface' to 'through the service interface'.

Response Response Status C

ACCEPT.

Cl 175 SC 175.2.4.6 P294 L31 # R1-62

Bruckman, Leon

NVIDIA

Comment Type E Comment Status A (Bp) (L)

Typo: 'the PRBS9 pad at the end of the alignment maker group are ignored on receive.' -- 'maker' should be 'marker'.

SuggestedRemedy

Change 'alignment maker group' to 'alignment marker group'.

Response Response Status C

ACCEPT.

CI 176 SC 176.4.1 P325 L15 # R1-250

Law, David

Hewlett Packard Enterprise

Comment Type T Comment Status A (Bp) (L)

Figure 176.2 '200GBASE-R 8:1, 400GBASE-R 16:2, 800GBASE-R 32:4, 1.6TBASE-R 16:8 PMAs functional block diagram' and Figure 176.12 '200GBASE-R 1:8, 400GBASE-R 2:16, 800GBASE-R 4:32, 1.6TBASE-R 8:16 PMAs functional block diagram' show a SIL block in both the IS_SIGNAL.request and IS_SIGNAL.indication primitive paths, and Figure '176.9.1:8 symbol-pair demultiplexer functional block diagram' shows an output to the 'SIL'.

There, however, doesn't appear to be any reference to 'SIL' or 'Signal indication logic' in Clause 176. I believe, however, the operation of the SIL on the IS_SIGNAL.indication path is described in the penultimate paragraph of subclause 176.2 'PMA service interface' and the associated Table 176.5, and the operation of the SIL on the IS_SIGNAL.request path is described in the penultimate paragraph of subclause 176.3 'Service interface below PMA', and associated Table 176-6.

Suggested Remedy

[1] In subclause 176.2 'PMA service interface', change the text 'For the m:n and n:m PMAs the SIGNAL_OK parameter at the client interface is set according to Table 176.5.' to read 'For the m:n and n:m PMAs the SIGNAL_OK parameter at the client interface is set by the signal indication logic (SIL) according to Table 176.5.'.

[2] In subclause 176.3 'Service interface below PMA', change the text 'For m:n and n:m PMAs, the SIGNAL_OK parameter at the interface below the PMA is set according to Table 176.6.' to read 'For m:n and n:m PMAs, the SIGNAL_OK parameter at the interface below the PMA is set by the SIL according to Table 176.6.'.

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However the comment correctly points out that SIL is used in the block diagram but is not defined in the text, so the suggested remedy is an improvement to the draft. A similar change needs to be made in Clause 177 and Clause 184, for the same reason.

Clause 176

In subclause 176.2, page 321, line 53 change the following sentence:

"For the m:n and n:m PMAs the SIGNAL_OK parameter at the client interface is set according to Table 176-5"

to:

"For the m:n and n:m PMAs the SIGNAL_OK parameter at the client interface is set by the signal indication logic (SIL) according to Table 176-5"

In subclause 176.3, page 323, line 11 change the following sentence:

"For m:n and n:m PMAs, the SIGNAL_OK parameter at the interface below the PMA is set according to Table 176-6."

to:

"For m:n and n:m PMAs, the SIGNAL_OK parameter at the interface below the PMA is set by the SIL according to Table 176-6."

Clause 177

In subclause 177.2, page 358, line 26 change the following sentence:

"The SIGNAL_OK parameter takes one of four values according to Table 177-1."

to"

"The SIGNAL_OK parameter is set to one of four values by the signal indication logic (SIL) according to Table 177-1."

In subclause 177.3, page 359, line 2 change the following sentence:

"The SIGNAL_OK parameter takes one of four values according to Table 177-2."

to"

"The SIGNAL_OK parameter is set to one of four values by the SIL according to Table 177-2."

Clause 184

In subclause 184.3, page 598, line 51 change the following sentence:

"The SIGNAL_OK parameter takes one of four values according to Table 184-1."

to"

"The SIGNAL_OK parameter is set to one of four values by the signal indication logic (SIL) according to Table 184-1."

In subclause 184.4, page 599, line 28 change the following sentence:

"The SIGNAL_OK parameter takes one of four values according to Table 184-2."

to"

"The SIGNAL_OK parameter is set to one of four values by the SIL according to Table 184-2."

[Editor's note: also applies to clauses 176, 177, 184]

CI 176 SC 176.5.2 P342 L # R1-131

de Koos, Andras

Microchip Technology Inc

Comment Type TR Comment Status A time sync (L)

The PMA needs clarification for what Rx and Tx mean
For example, an 8:1 PMA is identical to a 1:8 PMA, just with the Rx/Tx labels reversed.
Whether it is an 8:1 PMA or a 1:8 PMA depends what it is attached to in the PHY stack, not the function it performs.
But this presents problems, especially for the management objects.
Two interpretations are possible:
A: 'RX' management objects always connect to the RX for an 8:1 PMA, and to the RX of the 1:8 PMA (And Tx PMA always to Tx registers). That seems strange, since it means that one MDIO register would connect to either path through the PMA, depending on how this PMA is attached with in the PHY stack.
B: 'RX' management objects for the 8:1 PMA connect to the 'TX' of the 1:8 PMA, and vice versa. Each path through the PMA (whatever title Rx/Tx label) is connected to a single set of management variables that would not change depending on how the PMA is connected in the PHY stack. This makes much more sense (to me, at least!)

This absolutely needs to be clarified, especially for the PMA_delay status variables for TimeSync. There, the 'Rx' and 'Tx' delays are calculated differently. The delays would be reported incorrectly if approach A were to be used!

SuggestedRemedy

In sections 176.5.2, change the sentence to:
The transmit function of the n:m PMAs is identical to the receive function of the m:n PMAs (see 176.4.3), and uses the 'RX' PMA Management variables.

(and a similar update for section 176.5.3)

Response Response Status C

ACCEPT IN PRINCIPLE.
The PMA management variables in 176.11 are used by all PMAs (m:n, n:m and n:n). A management variable defined for the transmit direction is used by all PMAs for the transmit direction. For example, the variable PMA_delay_ns_TX_max is used to store the transmit path data delay incurred in the PMA; regardless of whether this is a m:n, n:m or n:n PMA. Similarly, the management variables for the receive direction are used by all the PMAs in the receive direction.

The CRG reviewed the following related contribution:
https://www.ieee802.org/3/dj/public/26_07/dekoos_3dj_01_2607.pdf

A change in the definition the path data delays in 176.10 is needed for the n:m PMAs.

In 176.10, remove the 2nd sentence of the 3rd paragraph.

In 176.10, change the 3rd sentence of the 3rd paragraph

From:

"The transmit path data delay is reported as the delay incurred by the first bit of the PCS

alignment marker of the PCS lane with the largest delay through the PMA"

To:

"The transmit path data delay for the m:n and n:n PMAs are reported as the delay incurred by the first bit of the PCS alignment marker of the PCS lane with the largest delay through the PMA. The transmit path data delay for the n:m PMA is reported as the delay incurred by the first bit of the PCS alignment marker of the PCS lane with the smallest delay through the PMA."

And change the 4th sentence of the 3rd paragraph

From:

"The receive path data delay is reported as the delay incurred by the first bit of the PCS alignment marker of the PCS lane with the smallest delay through the PMA."

To:

"The receive path data delay for the m:n and n:n PMAs are reported as the delay incurred by the first bit of the PCS alignment marker of the PCS lane with the smallest delay through the PMA. The receive path data delay for the n:m PMA is reported as the delay incurred by the first bit of the PCS alignment marker of the PCS lane with the largest delay through the PMA."

Implement with editorial license.

CI 176 SC 176.6 P342 L 1 # R1-45

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A (B) (L)

Section heading '176.6' reads: '200GBASE-R 1:1, 400GBASE-R 2:2, 800GBASE-R 4:4, 1.6TBASE-R 8:8, 1.6TBASE 16:16 PMAs'. The last entry is '1.6TBASE 16:16 PMAs' -- the '-' R' suffix is missing from the PHY type name. All other uses in the document correctly use '1.6TBASE-R 16:16 PMA' (e.g., Table 176-3 footnote a, Table 176-4 heading).

SuggestedRemedy

Change '1.6TBASE 16:16 PMAs' to '1.6TBASE-R 16:16 PMAs' in the section heading.

Response Response Status W

ACCEPT.

Cl 176	SC 176.6	P343	L36	# R1-46
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	(B) (L)
Figure 176-13 legend reads 'n = 1 (200GBASE-R), 2 (400GBASE-R), 4 (800GBASE-R), or 8 (1.6TBASE-R)'. This omits n = 16 for the 1.6TBASE-R 16:16 PMA, even though the figure caption and section heading explicitly include the 16:16 PMA.				
SuggestedRemedy				
Change the legend to: 'n = 1 (200GBASE-R), 2 (400GBASE-R), 4 (800GBASE-R), 8 (1.6TBASE-R 8:8 PMA), or 16 (1.6TBASE-R 16:16 PMA)'.				
Response	Response Status W			
ACCEPT IN PRINCIPLE.				
Figure 176-13 applies to the 1:1, 2:2, 4:4, 8:8 and 16:16 PMAs as noted in the Figure title. In the legend at the bottom of the figure, the value for the variable n is define for each type of PMA except the 16:16. The value of n for the 16:16 PMA needs to be added to this legend.				
Change the text in the legend of Figure 176-13 from:				
"n = 1 (200GBASE-R), 2 (400GBASE-R), 4 (800GBASE-R), or 8 (1.6TBASE-R)"				
to:				
"n = 1 (200GBASE-R 1:1 PMA), 2 (400GBASE-R 2:2 PMA), 4 (800GBASE-R 4:4 PMA), 8 (1.6TBASE-R 8:8 PMA), or 16 (1.6TBASE-R 16:16 PMA)"				
Implement with editorial licence.				

Cl 176C	SC 176C.5	P802	L 14	# R1-70
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	delay (B) (E)
Table 176C-1 footnote b states 'One pause_quantum is equal to 2.56 ns for 200GAUI-1, 1.28 ns for 400GAUI-2, 640 ps for 800GAUI-4, and 256 ps for 1.6TAUI-8.' The value '256 ps for 1.6TAUI-8' is wrong. For 1.6TBASE-R, 1 pause_quantum = 512 bit times x 0.625 ps = 320 ps. The 1.6TAUI-8 row shows 144 pause_quanta / 46.08 ns: 144 x 320 ps = 46.08 ns (correct); 144 x 256 ps = 36.86 ns (does not match 46.08 ns). Table 176D-1 footnote b contains the identical error (p. 824).				
SuggestedRemedy				
In footnote b of Table 176C-1 (and identically in Table 176D-1), change '256 ps for 1.6TAUI-8' to '320 ps for 1.6TAUI-8'.				
Response	Response Status W			
ACCEPT IN PRINCIPLE.				
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.				
However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy with editorial license.				

Cl 176C	SC 176C.6.4.4	P808	L53	# R1-200
Dudek, Michael		Marvell		
Comment Type	T	Comment Status	A	BLER extrapolation (CB)
Measuring the very low probabilities in this table is not possible in a reasonable test time and also is not necessary as they are much lower than the required Frame loss ratio probability.				

SuggestedRemedy
Below the table add a note "In order to predict whether a receiver meets the BLER requirement in a short test time it is acceptable to have zero counts in a 10 second test for any bin which has a probability less than 1e-11.

Response	Response Status C
ACCEPT IN PRINCIPLE.	
Resolved using the response to comment R1-201.	

Cl 176C	SC 176C.6.4.5.2	P810	L38	# R1-212
Dudek, Michael		Marvell		
Comment Type	T	Comment Status	A	RX test calibration (B) (E)
"package class A"no longer exists in Table 176C-2.				

SuggestedRemedy
Delete "for package class A"

Response	Response Status C
ACCEPT.	

Cl 176C	SC 176C.6.4.5.2	P810	L38	# R1-27
Hajduczenia, Marek		RG Nets		
Comment Type	T	Comment Status	A	RX test calibration (B) (E)
The jitter-adjustment sentence was changed from "Table 176C-2 for package A" (D3.0) to "Table 176C-2 for package class A" (D3.1). The qualifier does not resolve: Table 176C-2 (p803) lists single JH4u/JHRMS limits (0.118/0.023 UI) with no class A/class B differentiation, so "for package class A" references a per-class entry that does not exist in that table.				

SuggestedRemedy
Change "as close as practical to their limits in Table 176C-2 for package class A" to "as close as practical to their limits in Table 176C-2", dropping the per-class qualifier that has no corresponding distinction in Table 176C-2.

Response	Response Status C
ACCEPT IN PRINCIPLE.	
Resolved using the response to comment R1-212.	

Cl 176D	SC 176D.4	P825	L 12	# R1-216
Law, David		Hewlett Packard Enterprise		
Comment Type	T	Comment Status	A	(B) (E)
Figure 176D03 illustrates PMA:IS_UNITDATA_0:k.request as an arrow output from the top of the k:n PMA input to the bottom of the C2M Component (module side) above. This doesn't seem correct, as IS_UNITDATA_0:k.request is the transmit path and should therefore be output by the C2M component (module side) and input to the k:n PMA (see Figure 176-2).				
SuggestedRemedy				
Reverse the direction of the PMA:IS_UNITDATA_0:k.request between the C2M Component (module side) and the k:n PMA.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.				
However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy.				

Cl	176D	SC	176D.8.3.4	P	836	L	45	#	R1-243
Rysin, Alexander				NVIDIA					
Comment Type	TR	Comment Status				A	(B) (E)		
176D.8.3.4 cites the wrong equation numbers for two of the three modal return loss mask requirements. The subclause currently reads:									
"The common-mode to common-mode return loss specification is satisfied by either meeting the requirement of Equation (179015) or having ERL_CC value higher than the specification in Table 176D09."									
"The common-mode to differential-mode return loss specification is satisfied by either meeting the requirement of Equation (179016) or having ERL_DC higher than the value in Table 176D09."									
Common-mode to common-mode (CC) return loss mask (RLcc) is defined in Eq. 179014 and common-mode to differential-mode (DC) return loss mask (RLdc) is defined in Eq. 179015.									
SuggestedRemedy									
In 176D.8.3.4, first sentence, change "Equation (179015)" to "Equation (179014)."									
In 176D.8.3.4, second sentence, change "Equation (179016)" to "Equation (179015)."									
Response		Response Status							
ACCEPT.									

Cl	176D	SC	176D.8.3.4	P	836	L	45	#	R1-28
Hajduczenia, Marek				RG Nets					
Comment Type	TR			Comment Status	A				
				(B) (E)					
The new body text of 176D.8.3.4 cites the wrong RL mask equations, off by one. It says common-mode-to-common-mode is satisfied by Equation (179-15) and common-mode-to-differential-mode by Equation (179-16). However Equation (179-14) is RLcc and Equation (179-15) is RLdc; Equation (179-16) is not the mask used here. Table 176D-9 (p837) and the parallel Clause 179 text (p436) both correctly pair ERLCC with Equation (179-14) and ERLDC with Equation (179-15), so the body contradicts its own table.									
SuggestedRemedy									
In the 176D.8.3.4 body, change "Equation (179-15)" to "Equation (179-14)" for the ERLCC sentence, and "Equation (179-16)" to "Equation (179-15)" for the ERLDC sentence, to match Table 176D-9 and the parallel Clause 179 text.									
Response				Response Status					
ACCEPT.				W					

CI **176D** SC **176D.8.11** P**839** L**30** # **R1-201**

Dudek, Michael

Marvell

Comment Type **T** Comment Status **A** BLER extrapolation (CB)

Measuring the very low probabilities in this table is not possible in a reasonable test time and also is not necessary as they are much lower than the required Frame loss ratio probability.

SuggestedRemedy

Below the table add a note "In order to predict whether a receiver meets the BLER requirement in a short test time it is acceptable to have zero counts in a 10 second test for any bin which has a required probability less than 1e-11.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

The following contributions were reviewed by the CRG:

https://www.ieee802.org/3/dj/public/26_07/dudek_3dj_01_2607.pdf

https://www.ieee802.org/3/dj/public/26_07/ran_3dj_04a_2607.pdf

Based on on straw polls TF-1 and TF-2 there is consensus to adopt the proposed changes in ran_3dj_04a_2607.

Implement the proposed changes on slide 15 of ran_3dj_04a_2607 with editorial license.

Straw poll TF-1 (pick one) directional

I support adding guidance for shortened test time for BLER receiver tests for C2M and C2C AUIs.

Y: 36

N: 10

Straw poll TF-2 (pick one) directional

I support adopting the proposal in:

A: ran_3dj_04a_2607 slide 15

B: dudek_3dj_01_2607 slide 6

A: 32 B: 9

CI **176D** SC **176D.8.12.1** P**841** L**9** # **R1-315**

Ran, Adeo

Cisco Systems, Inc.

Comment Type **E** Comment Status **A**

The representation of the MCB and HCB in figures 176D-8a and 176D-8b is backwards. It should be similar to that of figure 176D-7b, where the MCB has a receptacle and the HCB plugs into it.

SuggestedRemedy

Flip the receptacle/plug shapes horizontally in figures 176D-8a and 176D-8b.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy with editorial license.

CI **176D** SC **176D.8.12.2** P**842** L**30** # **R1-241**

Rysin, Alexander

NVIDIA

Comment Type **ER** Comment Status **A** (B) (E)

The word "noise" appears twice in succession:

"For a given noise RMS noise voltage s_ns measured at the Tx test reference, the spectral density of the noise source N_s(f) used for calculation of COM may be calculated using Equation (179018)."

SuggestedRemedy

Change:

"For a given noise RMS noise voltage s_ns measured at the Tx test reference..."

to:

"For a given RMS noise voltage s_ns measured at the Tx test reference..."

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy.

CI 176D	SC 176D-3	P829	L5	# R1-240
Rysin, Alexander		NVIDIA		
Comment Type	TR	Comment Status	A	R_peak (E)
The current value R_peak of module output in AUI-C2M is set too high. The current value cannot be met in simulation even for slightest deviation from the COM reference model. Prior work (mi_3dj_02a_2507) also showed that the current value may be unreachable. Data from recent interoperability events (e.g. calvin_3dj_01a_2601) show that most tested modules cannot meet the R_peak and v_f specifications together for the same TX preset.				
SuggestedRemedy				
Change module output R_peak to 0.44 to provide some margin for reasonable deviations from the COM reference model parameters.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
The CRG reviewed the contribution				
< https://www.ieee802.org/3/dj/public/26_07/rysin_3dj_01a_2607.pdf >.				
There was support for changing the minimum R_peak from 0.51 to 0.43 and to change the T_r parameter of the reference COM transmitter from 4 ps to 7 ps for module output (no change for host output).				
In Table 176D-3 (module output) change R_peak from 0.51 to 0.43.				
In Table 176D-7 (COM parameters) split T_r into two rows: host to module direction with the value 0.004 ns (as in D3.1), and module to host direction with the value 0.007 ns.				
Implement with editorial license.				

CI 177	SC 177.5.8	P370	L19	# R1-63
Bruckman, Leon		NVIDIA		
Comment Type	T	Comment Status	A	(B) (L)
Redundant word: 'The values of Q are shown in table Table 177-3.' -- 'table' appears twice (lowercase then capitalized). The corresponding transmit sentence in 177.4.2 correctly reads '...shown in Table 177-3.'				
SuggestedRemedy				
Delete the lower-case 'table': '...shown in Table 177-3.'				
Response	Response Status C			
ACCEPT.				

CI 177	SC 177.5.8	P370	L20	# R1-6
Hajduczenia, Marek		RG Nets		
Comment Type	E	Comment Status	A	(B) (L)
Missing space in newly inserted sentence: "from the120-bit block multiplexer" -- "the" is run together with "120-bit". This text is new in D3.1 and appears in 177.5.8 Convolutional deinterleaver.				
SuggestedRemedy				
Change "from the120-bit block multiplexer" to "from the 120-bit block multiplexer".				
Response	Response Status C			
ACCEPT.				

CI 177	SC 177.7.3	P374	L7	# R1-47
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	(B) (L)
Normative 'shall' statement references the wrong state diagram figure. The text reads: 'An Inner FEC Pad detection process as illustrated in the state diagram in Figure 177-13 shall be implemented...' Figure 177-13 is the Inner FEC self-synchronization state diagram. The pad detection state diagram is Figure 177-14. Implementations following this text literally would implement a second instance of the sync state diagram instead of the pad-lock process.				
SuggestedRemedy				
Replace 'Figure 177-13' with 'Figure 177-14' in the second sentence of 177.7.3.				
Response	Response Status W			
ACCEPT.				

CI 177 SC 177.11.4.1 P383 L13 # R1-49

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A (B) (L)

PICS table 177.11.4.1 (Transmit function) is missing entries for several mandatory functions. Sec. 177.4.1 states alignment marker lock 'is required for 200GBASE-R, 400GBASE-R, 800GBASE-R, and 1.6TBASE-R Inner FEC' (normative requirement). Sec. 177.6.1.2 states 'The Inner FEC shall include a PRBS31 test-pattern generator'; Sec. 177.6.1.5 uses 'shall' for the SSPRQ generator; Sec. 177.6.2.1 uses 'shall' for the PRBS31 checker; Sec. 177.6.1.1 and 177.6.2.3 contain additional 'shall' requirements for PRBS31-encoded-by-Inner-FEC modes. None of these mandatory items appear in the PICS proforma. Additionally, PICS item TF5 is labelled '8:1 PAM4 interleaving' but Sec. 177.4.6 is titled '8:1 bit-pair interleaving' -- these should match.

SuggestedRemedy

Add PICS items for: symbol demultiplexing/deskew (177.4.1, M); PRBS31 test-pattern generator (177.6.1.2, M); SSPRQ generator (177.6.1.5, M); PRBS31 checker (177.6.2.1, M); PRBS31-encoded-by-Inner-FEC TX mode (177.6.1.1, M); PRBS31-encoded-by-Inner-FEC RX mode (177.6.2.3, M). Also rename TF5 feature from '8:1 PAM4 interleaving' to '8:1 bit-pair interleaving' to match Sec. 177.4.6.

Response Response Status W

ACCEPT IN PRINCIPLE.
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.
However, the change proposed in the suggested remedy is an improvement to the draft.

In 177.11.4.1 Transmit function:
Rename '8:1 PAM4 interleaving' to '8:1 bit-pair interleaving' for TF5.
Shift numbers for TF1-TF6 by +1 to TF2-TF7.
Insert a new line at the top of the table:
TF1, Symbol demultiplexing/deskew, 177.4.1, , M;
Insert new lines at the bottom of the table:
TF8, PRBS31-encoded-by-Inner-FEC TX mode (177.6.1.1, M);
TF9, PRBS31 test-pattern generator, 177.6.1.2, , M;
TF10, SSPRQ generator, 177.6.1.5, , M;
TF11, PRBS31 checker, 177.6.2.1, , M;
In 177.11.4.2 Receive function
Insert a new line at the bottom of the table:
RF7, PRBS31-encoded-by-Inner-FEC RX mode, 177.6.2.3, , M.
Implement with editorial license.

CI 177 SC 177.11.4.4 P384 L13 # R1-66

Bruckman, Leon

NVIDIA

Comment Type T Comment Status A (B) (L)

Missing feature name for TIM1

SuggestedRemedy

Add feature name to TIM1

Response Response Status C
ACCEPT IN PRINCIPLE.
Add feature name "Delay constraints" to TIM1.

CI 177A SC 177A P849 L7 # R1-69

Bruckman, Leon

NVIDIA

Comment Type E Comment Status A (B) (L)

VPn arrows are different in the 3 annexes with test vectors 177A, 184A and 186A

SuggestedRemedy

Use the VPn arrows from Annex 184A for annexes 177A and 186A.

Response Response Status C
ACCEPT.
[Editor's note: also applies to annexes 177A, 186A]

CI 178 SC 178.1 P387 L47 # R1-48

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A (B) (E)

Table 178-4 (Relationship between clauses and PHY types defined in Clause 178) lists the associated PCS clause for 1.6TBASE-KR8 as '172--1.6TBASE-R PCS'. Clause 172 is the 800GBASE-R PCS, not the 1.6TBASE-R PCS. The 1.6TBASE-R PCS is Clause 175. This is contradicted by 178.3 in the same clause, which correctly states 'the Clause 175 PCS for 1.6TBASE-KR8'. The 800GBASE-KR4 row in the same table correctly shows '172'.

SuggestedRemedy

Change '172--1.6TBASE-R PCS' to '175--1.6TBASE-R PCS' in Table 178-4.

Response Response Status W
ACCEPT IN PRINCIPLE.
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.
However, the change proposed in the suggested remedy is an improvement to the draft.
Implement the suggested remedy.

CI 178 SC 178.6 P390 L14 # R1-51

Bruckman, Leon

NVIDIA

Comment Type TR Comment Status A delay (B) (E)

Table 178-5 (delay constraints) footnote b states '256 ps for 1.6TBASE-KR8' as the pause_quantum value. For 1.6TBASE, 1 pause_quantum = 512 bit times x 0.625 ps/bit = 320 ps, per Clause 174 Table 174-4 footnote c which explicitly states '320 ps' for 1.6TBASE. The value 256 ps corresponds to a hypothetical 2 Tb/s system and is clearly a transcription error. Verification: Table 178-4 lists 232 pause_quanta for 118,784 bit times (118,784 / 512 = 232, correct), but 232 x 256 ps = 59.4 ns not 74.24 ns; only 232 x 320 ps = 74.24 ns is consistent with the ns column.

SuggestedRemedy

In footnote b of Table 178-5, change '256 ps for 1.6TBASE-KR8' to '320 ps for 1.6TBASE-KR8'.

Response Response Status W

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy.

CI 178 SC 178.8.8 P392 L29 # R1-104

Simms, William

NVIDIA Corporation

Comment Type E Comment Status A (B) (E)

No period at end of sentence

SuggestedRemedy

add period 179.8.8.

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy.

CI 178 SC 178.9.2 P393 L # R1-105

Simms, William

NVIDIA Corporation

Comment Type T Comment Status R (withdrawn) (E)

note for me: did we mean to drop package classes

SuggestedRemedy

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 178 SC 178.9.2 P393 L25 # R1-211

Dudek, Michael

Marvell

Comment Type TR Comment Status A RLdc (B) (E)

The transmitter common-mode to differential mode return loss is specified in 178.9.2.7 but is missing from Table 178-6

SuggestedRemedy

Add Common-mode to differential-mode return loss RLdc(min) to Table 178-6. Reference 178.9.2.7 and value Equation 178-2

Response Response Status W

ACCEPT.

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl 178 SC 178.9.2 P393 L26 # R1-210

Dudek, Michael

Marvell

Comment Type T Comment Status A dv_f (E)

In Table 178-6 Difference steady-state voltage is listed as a range. However the reference clause 178.9.2.5 only requires meeting the minimum value. Also as long as the Differential peak-to-peak voltage specification is met there doesn't seem to be a reason to have a maximum difference steady-state voltage.

SuggestedRemedy

Change this just to min with the unchanged value of 0V.

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

The reference steady state voltage range should be based on the A_v and A_ne parameters of COM, which represent the minimum and maximum "launch" voltages of the reference transmitter.

Since the minimum dv_f is defined as 0 (based on A_v), and A_ne-A_v=0.481-0.385=0.096, the range in Table 178-6 (0 to 0.1) seems suitable.

The text in 178.9.2.5 should refer to the range rather than the minimum. This will also affect 176C.6.3.6 by reference.

In 178.9.2.5, change "shall meet the specification d_vf (min) in Table 178-6" to "shall be within the range specified for d_vf in Table 178-6".

Cl 178 SC 178.9.3 P397 L40 # R1-107

Simms, William

NVIDIA Corporation

Comment Type E Comment Status A dERL (B) (E)

Table 178-9 row 'Difference effective return loss, dERL (min) cites 178.9.3.6 and should be 178.9.3.7

SuggestedRemedy

change reference to 178.9.3.7

Response Response Status C

ACCEPT.

Cl 178 SC 178.9.3.1 P398 L19 # R1-106

Simms, William

NVIDIA Corporation

Comment Type E Comment Status A (B) (E)

cites specification in 178.9.2.1 but should be 178.9.2.2

SuggestedRemedy

change reference to 178.9.2.2

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy.

Cl 178 SC 178.9.3.7 P403 L39 # R1-108

Simms, William

NVIDIA Corporation

Comment Type E Comment Status A dERL (B) (E)

question if Receiver Different ERL (178.9.3.7)should reference table 178-15 as Transmitter ERL does (178.9.2.3)

SuggestedRemedy

add table 178-15 if required

Response Response Status C

ACCEPT IN PRINCIPLE.

Change "Table 178-8 and Table 178-14" to "Table 178-8, Table 178-14, and Table 178-15".

CI 178 SC 178.10 P404 L46 # R1-7

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A AC coupling (B) (E)

In Table 178-13 (Channel characteristics summary), the "Maximum AC-coupling 3 dB corner frequency" row has a blank Reference cell. In D3.0 it cited 178.10.5. The D3.1 renumbering shifted AC-coupling to 178.10.4 and other rows were updated (e.g., SCMRCH 178.10.6->178.10.5), but this row's Reference was emptied rather than updated. Every other row carries a Reference. The blank also appears in the duplicate at p812.

SuggestedRemedy

Set the Reference cell of the "Maximum AC-coupling 3 dB corner frequency" row in Table 178-13 to 178.10.4 (also at the p812 duplicate).

Response Response Status C

ACCEPT IN PRINCIPLE.

In Annex 178C there is no specification of channel AC coupling. D3.0 used the same reference 178.10.4 although it is a different channel specification. Assuming the intent is to have the same requirements as in 178, a subclause should be added.

Add a new subclause 176C.7.5 "AC-coupling" with content "The channel AC-coupling specifications are identical to those of 178.10.4".

In Table 178-13., use 178.10.4 for the reference cell of "Maximum AC-coupling 3 dB corner frequency".

In Table 176C-8, use 176C.7.5 for the reference cell of "Maximum AC-coupling 3 dB corner frequency".

Implement with editorial license.

[Editor's note: changed clause/subclause from 0/0 to 178/178.10]

CI 178 SC 178.10 P404 L48 # R1-109

Simms, William

NVIDIA Corporation

Comment Type E Comment Status R (withdrawn) (E)

Note for me: check justification to lower SCMR from 20dB to 17dB. I see Rich's presentation for ERL of 17, but nothing for SCMR.

SuggestedRemedy

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 178 SC 178.10.1 P406 L50 # R1-110

Simms, William

NVIDIA Corporation

Comment Type E Comment Status R (withdrawn) (E)

Note no longer present "When channel scattering parameters are measured with a reference impedance other than R0, they shall be transformed per 178A.1.3 and Equation (178A04) before COM computation."

SuggestedRemedy

if note unintentionally removed, add "When channel scattering parameters are measured with a reference impedance other than R0, they shall be transformed per 178A.1.3 and Equation (178A04) before COM computation."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 178 SC 178.10.3.3 P409 L47 # R1-8

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A Modal ERL (B) (E)

In the "where" list, RLcd(f) cites sCD11(f) as "defined in (176D.8.3)", but 176D.8.3 is the parent subclause "Return loss specifications". sCD11(f) is defined in the child subclause 176D.8.3.3 "Mixed-mode reflection coefficients". The parallel Clause 179 reference on p445 and sibling definitions (RLcc/RLdc, p437/p438) correctly cite 176D.8.3.3.

SuggestedRemedy

Change "(176D.8.3)" to "(176D.8.3.3)" to point to the precise defining subclause, matching the parallel Clause 179 reference on p445.

Response Response Status C

ACCEPT.

Cl 178 SC 178.14.3 P413 L26 # R1-147

Healey, Adam Broadcom Inc.

Comment Type E Comment Status A PICS (B) (E)

There is no obvious reason to have AUI200G and AUIL200G as separate items. Also, per 21.6.2, the notation O.<n> means "at least one of the group of options labeled by the same numeral <n> is required." It is more appropriate to use O/<n> which means "one and only one of the group of options labeled by the same numeral <n> is required." The PMD is either in the same package as the PCS or it isn't.

SuggestedRemedy

Consolidate items *AUI200G and AUIL200G in a single item, "AUI2C" with Feature "AUI-n C2C" and Value/Comment "PMD is separated from the PCS by an AUI-C2C specified in Annex 120B, Annex 120D, Annex 120F, or Annex 176C". Change the status for item PHY and new item AUI2C to "PMD:O/3".

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

Cl 178A SC 178A.1.3 P853 L14 # R1-76

Brown, Matthew Qualcomm

Comment Type E Comment Status A (B) (E)

This editor's note has served its purpose.

SuggestedRemedy

Delete editor's note.

Response Response Status C

ACCEPT.

Cl 178B SC 178B P874 L1 # R1-140

Huber, Thomas Nokia

Comment Type TR Comment Status A coherent APSU (CA)

Annex 178B should include a subclause describing an RTS mechanism for use with coherent PMDs. Shoehorning LR1 into the IMDD mechanism and describing ER1 as tunneling the SIGNAL_OK status of the AUI across the PMD is not sufficient for supporting APSU with coherent PMDs.

SuggestedRemedy

Add new subclause 178B.6 "RTS function for coherent ISLs", with subclauses 178B.6.1 "RTS function for coherent interfaces" and 178B.6.2 "State diagram". A detailed presentation covering this and other related issues will be provided.

Response Response Status C

ACCEPT IN PRINCIPLE.

Two related contributions were considered at the Clause 178B ad hoc meeting on June 25 as follows:

https://www.ieee802.org/3/dj/public/adhoc/178b/26_0625/huber_178b_01a_260625.pdf
https://www.ieee802.org/3/dj/public/adhoc/178b/26_0625/huber_178b_02_260625.pdf

At the same ad hoc meeting two related straw polls were taking as recorded in the following report:

https://www.ieee802.org/3/dj/public/adhoc/178b/26_0625/polls_178b_260625.pdf

The results of the straw poll from the June 25 ad hoc indicated support by the attendees of the ad hoc meeting for the direction and proposed changes in huber_178b_01a_260625 and huber_178b_02_260625.pdf, respectively.

The following related contributions were reviewed by the CRG:

https://www.ieee802.org/3/dj/public/26_07/huber_3dj_01a_2607.pdf
https://www.ieee802.org/3/dj/public/26_07/huber_3dj_02a_2607.pdf
https://www.ieee802.org/3/dj/public/26_07/slavick_3dj_01_2607.pdf

Based on Straw Poll TF-8 there was consensus to adopt huber_3dj_01a_2607 however the CRG requested a few improvements.

The contribution huber_3dj_02a_2607 is huber_3dj_01a_2607 updated to incorporate the feedback from the CRG.

Implement the proposed changes on slides 5 to 28 in huber_3dj_02a_2607.

Update subclause 178B.9 to indicate that this subclause is relevant only to PAM4.

Implement with editorial license.

Straw Poll TF-8 (directional)

I support adopting the changes proposed in huber_3dj_01a_2607.

Y: 53

N: 20

Cl 178B SC 178B.2 P874 L28 # R1-141

Huber, Thomas

Nokia

Comment Type TR Comment Status A coherent APSU (CA)

The last two sentences of the second paragraph and the second sentence of the third paragraph are specific to IMDD ISLs. If the coherent RTS FSM is adopted, they will need to be made generic.

SuggestedRemedy

A detailed presentation covering this and other related issues will be provided.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-140.

Cl 178B SC 178B.4 P876 L3 # R1-142

Huber, Thomas

Nokia

Comment Type TR Comment Status A coherent APSU (CA)

Several cross-references are specific to IMDD ISLs. If the coherent RTS FSM is adopted, they will need to be made generic.

SuggestedRemedy

A detailed presentation covering this and other related issues will be provided.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-140.

Cl 178B SC 178B.5 P876 L33 # R1-143

Huber, Thomas

Nokia

Comment Type TR Comment Status A coherent APSU (CA)

178B.5 through 178B.8 are all specific to IMDD PHYs. If the coherent RTS FSM is adopted, it will be better to group those 4 together into one subclause.

SuggestedRemedy

Add a new 178B.5 header titled "RTS and ILT functions for IMDD ISLs". Make current clauses 178B.5 to 178B.8 level-3 headings below this, clarifying that they are specific to IMDD interfaces. A detailed presentation covering this and other related issues will be provided.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-140.

Cl 178B SC 178B.5 P876 L38 # R1-177

Slavick, Jeff

Broadcom Inc

Comment Type T Comment Status A (Bp) (CA)

This list of what the ILT functions are made of, not a pick which ones you want list.

SuggestedRemedy

Delete ", and" from the first item in the dashed list.

Response Response Status C

ACCEPT.

Cl 178B SC 178B.5 P876 L42 # R1-260

Law, David

Hewlett Packard Enterprise

Comment Type T Comment Status A (Bp) (CA)

Subclause 178B.5 'RTS and ILT functions within an interface' says 'The RTS function uses inputs from the adjacent interface (via the service interface), its peer interface (via the ILT function), and its per-interface ILT function to permit the ILT function to transition to DATA mode.'

My understanding is that the inputs from the adjacent interface (via the service interface) is the SIGNAL_OK parameter of the IS_SIGNAL.request primitive from the interface above the PMA to the adjacent interface below the PMA, and the SIGNAL_OK parameter of the IS_SIGNAL.indication primitive from the interface below the PMA to the adjacent interface above the PMA. In other words, this condition is communicated 'downward' via IS_SIGNAL.request and communicated 'upward' via IS_SIGNAL.indication.

Isn't it possible, however, for the Signal indication logic (SIL) in a sublayer in the path between adjacent interfaces to modify the SIGNAL_OK parameter of the IS_SIGNAL primitive communicated between adjacent interfaces, for example the Clause 176 Physical Medium Attachment (PMA) sublayer with RS-FEC symbol multiplexing (see Figure 176012 and Tables 17605 and 17606) and Clause 177 Inner Forward Error Correction Sublayer (see Figure 17702).

As a result, isn't the SIL in the sublayers between adjacent interfaces, if present, also part of APSU and used as an input to enable the ILT function to transition to DATA mode?

SuggestedRemedy

In subclause 178B.5, change the text 'The RTS function uses inputs from the adjacent interface (via the service interface) ...' to read 'The RTS function uses inputs from the adjacent interface, including any SIL between (via the service interface) ...'.

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl **178B** SC **178B.5** P**877** L**6** # **R1-217**

Law, David Hewlett Packard Enterprise

Comment Type **TR** Comment Status **A** Sublayer interfaces (CA) (Bp)

Figure 178B 'RTS and ILT functions within interfaces' shows the SIGNAL_OK parameter of inst:IS_SIGNAL.request and the tx_symbol parameter of inst:IS_UNITDATA_i.request primitives as inputs to the RTS function and transmit function, respectively, and the SIGNAL_OK parameter of inst:IS_SIGNAL.indication and the rx_symbol parameter of inst:IS_UNITDATA_i.indication as outputs from the RTS function and receive function, respectively.

The definition for the adjacent_signal_ok variable in subclause 178B.8.2.1 includes the note 'For an interface below a PMA or Inner FEC (AUI component or PMD), SIGNAL_OK is received via the IS_SIGNAL.request primitive of the service interface above the interface. For an interface above a PMA (AUI component), the SIGNAL_OK is received via the IS_SIGNAL.indication primitive of the service interface below the interface.'

Subclause 176C.4 'Service interfaces' says 'For an AUI component above the AUI channel, the SIGNAL_OK parameter of the PMA:IS_SIGNAL.indication primitive corresponds to the variable rts_status of the RTS function, as defined in 178B.8.2.1.' and 'For an AUI component below the AUI channel, the SIGNAL_OK parameter of the PMA:IS_SIGNAL.request primitive corresponds to the variable rts_status of the RTS function, as defined in 178B.8.2.1.'

Figure 176C.4 shows PMA:IS_UNITDATA_0:k.request as an input to the interface below the PMA, the C2C component (host side), and PMA:IS_UNITDATA_0:k.request as an output from the interface above the PMA, the C2C component (module side).

As a result, I believe that the figure only illustrates an interface below a PMA.

SuggestedRemedy

Change the title of Figure 178B.3 'RTS and ILT functions within interfaces' to read 'RTS and ILT functions within an interface below a PMA (AUI component or PMD)' and add a new Figure titled 'RTS and ILT functions within an interface above a PMA (AUI component)' with the PAM service interface primitives mirrored.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Instead of adding a new figure, Figure 178B-3 can use the same convention as Figure 178B-4.

In Figure 178B-3 change "SIGNAL_OK parameter of inst:IS_SIGNAL.request" and "SIGNAL_OK parameter of inst:IS_SIGNAL.indication" to "SIGNAL_OK" and add the same notes as in Figure 178B-4.

Cl **178B** SC **178B.5** P**878** L**14** # **R1-219**

Law, David Hewlett Packard Enterprise

Comment Type **T** Comment Status **A** (B) (CA)

Subclause 178B.8.2.1 'Variables' defines adjacent_signal_ok as an 'Enumerated variable derived from the value of the SIGNAL_OK parameter on the service interface.'. The SIGNAL_OK parameter is shown as an input to the RTS Function, and the adjacent_signal_ok variable is used by the ILT function (see Figure 178B.10b Training control state diagram, part 2). It, therefore, appears that the adjacent_signal_ok variable is derived from the SIGNAL_OK parameter by the RTS Function, and then passed to the ILT function.

SuggestedRemedy

Add an arrow labelled 'adjacent_signal_ok' from the RTS Function box to the ILT function box.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy with editorial license.

Cl **178B** SC **178B.6** P**878** L**46** # **R1-291**

Dawe, Piers J G NVIDIA

Comment Type **TR** Comment Status **R** Signal detect (CA)

D3.0 comment 433 and others: signal detect should gate RTS. RTS should not move to DATA mode if no light is coming in, whether or not a retimer has locked its clock to something.

SuggestedRemedy

Add text that for an optical PMD or PMD/PMA, local_rts should not be set to true if signal detect indicates that there is no optical input.

Response Response Status **W**

REJECT.

The resolution to comments R1-289 and R1-290 determined that signal detect would not be explicitly specified as a recommended input to the ILT and RTS functions.

There is no consensus to make the proposed changes.

Cl 178B SC 178B.7 P879 L41 # R1-293

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R Signal detect (CA)

This says "Recovery from the ISL_FAIL state in the training control state diagram (Figure 178B.10b) requires management to assert mr_restart_training or reset; the timing of this action is implementation dependent and beyond the scope of this standard" which sounds permanent or at least aggravating.

SuggestedRemedy

I wonder if this should say that if the link was up and signal_detect transitions from OK to not, the state diagram should be restarted, or whether it should stay down until the return of the optical signal causes something else to restart it.

Response Response Status W

REJECT.

The resolution to comments R1-289 and R1-290 determined that signal detect would not be explicitly specified as a recommended input to the ILT and RTS functions.

There is no consensus to make the proposed changes.

Cl 178B SC 178B.7.2 P880 L1 # R1-292

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R Signal detect (CA)

D3.0 comment 433 and 436: a receiving PMD or PMA should not claim all is well if no light is coming in.

SuggestedRemedy

Add text here and possibly in 178B.8.3 saying that if the lane-by-lane signal detect is 0, per-lane ISL should (recommended) not move to mission mode or stay there (although it may transmit onto the line).

Response Response Status W

REJECT.

The resolution to comments R1-289 and R1-290 determined that signal detect would not be explicitly specified as a recommended input to the ILT and RTS functions.

There is no consensus to make the proposed changes.

Cl 178B SC 178B.7.2 P880 L14 # R1-179

Slavick, Jeff

Broadcom Inc

Comment Type T Comment Status A (B) (CA)

We can be more specific on what "exit the transmit disable state" so it clears it's a variable change not a state diagram state change.

SuggestedRemedy

Add "(tx_disable = false)" after "transmit disable state".

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

Cl 178B SC 178B.7.8 P889 L51 # R1-180

Slavick, Jeff

Broadcom Inc

Comment Type T Comment Status A (B) (CA)

The TRAIN_LOCAL state in is Figure 178B-10a but you list both in this paragraph but only 10a in the next.

SuggestedRemedy

In the 2nd paragraph of 178B.7.8 remove the "and Figure 178B-10b".
In the 4th paragraph of 178B.7.8 add "and Figure 178B-10b" after Figure 178B-10a

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

Cl 178B SC 178B.7.8.4 P891 L26 # R1-29

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A (B) (CA)

In Figure 178B-12, two coef_sts assignments read "COEFFICIENT AT LIMIT AND QUALIZATION LIMIT" (missing leading E in EQUALIZATION). This does not match the enumerated value defined in 178B.9 (p897): "COEFFICIENT AT LIMIT AND EQUALIZATION LIMIT". The correct spelling renders fine elsewhere on the same page (e.g., "NO EQUALIZATION").

SuggestedRemedy

In both occurrences on p891, change "QUALIZATION" to "EQUALIZATION" so each assignment reads "coef_sts = COEFFICIENT AT LIMIT AND EQUALIZATION LIMIT".

Response Response Status C

ACCEPT.

Cl **178B** SC **178B.7.8.4** P**891** L**26** # **R1-181**

Slavick, Jeff Broadcom Inc

Comment Type **ER** Comment Status **A** (B) (CA)

Missing an E in EQUALIZATION

SuggestedRemedy

Change QUALIZATION to EQUALIZATION on lines 26 and 33.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-29.

Cl **178B** SC **178B.8.2.1** P**893** L**39** # **R1-221**

Law, David Hewlett Packard Enterprise

Comment Type **T** Comment Status **A** (Bp) (CA)

Suggest that it should be clarified that the adjacent_signal_ok variable is derived from the SIGNAL_OK parameter of the input IS_SIGNAL primitive on the service interface.

SuggestedRemedy

- [1] Change the text '... the SIGNAL_OK parameter on the service interface.' to read '... the SIGNAL_OK parameter of the input IS_SIGNAL primitive on the service interface.'
- [2] Change the NOTE to read 'NOTE: For an interface below a PMA or Inner FEC (AUI component or PMD), the adjacent_signal_ok variable is derived from the SIGNAL_OK parameter of the IS_SIGNAL.request primitive of the service interface above the interface. For an interface above a PMA (AUI component), the adjacent_signal_ok variable is derived from the SIGNAL_OK parameter of the IS_SIGNAL.indication primitive of the service interface below the interface.'

Response Response Status **C**

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl **178B** SC **178B.8.2.1** P**893** L**42** # **R1-144**

Huber, Thomas Nokia

Comment Type **TR** Comment Status **A** Sublayer interfaces (CA) (Bp)

The NOTE related to the variable adjacent_signal_ok seems not correct. It says:
 "NOTE: For an interface below a PMA or Inner FEC (AUI component or PMD), SIGNAL_OK is received via the IS_SIGNAL.request primitive of the service interface above the interface. For an interface above a PMA (AUI component), the SIGNAL_OK is received via the IS_SIGNAL.indication primitive of the service interface below the interface". Service interfaces are defined at the top of the sublayer - so for an interface below a PMA or Inner FEC, SIGNAL_OK is received via IS_SIGNAL.request for that layer that is below the PMA or Inner FEC. The primitive for the layer above (i.e., the PMA or Inner FEC) would be at the top of that layer above. In other words, if you want to communicate something down the stack, you use the layer below's service interface to do it.

SuggestedRemedy

Change the first sentence of the note to read as follows:

NOTE - For an interface below a PMA or Inner FEC (AUI component or PMD), SIGNAL_OK is received via the IS_SIGNAL.request primitive of the service interface of the AUI component or PMD.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-221.

Cl **178B** SC **178B.8.2.1** P**893** L**53** # **R1-218**

Law, David

Hewlett Packard Enterprise

Comment Type **TR** Comment Status **A** Sublayer interfaces (CA) (B)

Subclause 178B.6 'RTS function' says that 'The state of the rts_status variable is provided by the interface to the adjacent interface via the service interface SIGNAL_OK parameter.'. Subclause 176D.4 'Service interfaces' says that 'For an AUI component above the AUI channel, the SIGNAL_OK parameter of the PMA:IS_SIGNAL.indication primitive corresponds to the variable rts_status of the RTS function ...' and that 'For an AUI component below the AUI channel, the SIGNAL_OK parameter of the PMA:IS_SIGNAL.request primitive corresponds to the variable rts_status of the RTS function ...'.

As a result, I believe that the rts_status variable maps to the SIGNAL_OK parameter of the IS_SIGNAL.indication primitive if the interface is below the PMA, and to the SIGNAL_OK parameter of the IS_SIGNAL.request primitive if the interface is above the PMA.

SuggestedRemedy

[1] Add the following text to the rts_status variable definition:

The rts_status maps to the SIGNAL_OK parameter of the output IS_SIGNAL primitive on the service interface.

[2] Based on the note provided for the adjacent_signal_ok variable, which is mapped from the SIGNAL_OK parameter, add the following note to the rts_status variable:

NOTE: For an interface below a PMA or Inner FEC (AUI component or PMD), rts_status maps to the SIGNAL_OK parameter of the IS_SIGNAL.indication primitive of the service interface above the interface. For an interface above a PMA (AUI component), rts_status maps to the SIGNAL_OK parameter of the IS_SIGNAL.request primitive of the service interface below the interface.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy with editorial license.

Cl **178B** SC **178B.8.2.1** P**894** L**7** # **R1-1**

Marris, Arthur

Self

Comment Type **TR** Comment Status **A** Variables (CA)

"SQ" and "LP" are poor identifiers for mr_adj_los_mode enumerations as they could be interpreted as "square" and "link partner"

SuggestedRemedy

Rename "SQ" to "SQUELCH" and "LP" to "LOCAL_PATTERN" in 178B.8.2.1, Figure 178B.10b, and Table 45.133e.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

Rename "SQ" to "SQUELCH" and "LP" to "LOCAL_PATTERN" in 178B.8.2.1, Figure 178B.10b, and Table 45-133e.

In 178B.8.2.1 in the definition of mr_adj_los_mode delete "(squelch)" and "(local pattern)".

Implement with editorial license.

Cl **178B** SC **178B.8.2.1** P**894** L**17** # **R1-176**

Slavick, Jeff

Broadcom Inc

Comment Type **ER** Comment Status **A** (Bp) (CA)

"the APSU" does not make sense.

SuggestedRemedy

Remove the word "the"

Response Response Status **C**

ACCEPT.

CI 178B SC 178B.8.2.4 P896 L1 # R1-316

Ran, Adeo Cisco Systems, Inc.

Comment Type E Comment Status A

Following up on unsatisfied comment I-206.

The RTS update state diagram (Figure 179B-9) has two states with no assignments. These can be merged into other states.

Previous proposals in
<https://www.ieee802.org/3/dj/public/26_05/mascitto_3dj_01_2605.pdf> and
<https://www.ieee802.org/3/dj/public/26_03/slavick_3dj_03_2603.pdf> have been considered, but were not fully equivalent. This suggested remedy is a slight modification to previous proposal mascitto_01, fixing an issue to make it fully equivalent, in case the CRG wants to go in this direction. Since it is fully equivalent, the comment is editorial.

The suggested remedy includes the textual diagram representation. A graphical representation will be provided in a separate contribution.

SuggestedRemedy

[*] --> START: (reset + mr_restart + !adjacent_intf_rx_ready)

START: local_rts ← false; USE_TX_CLOCK(local)

START --> SWITCH_CLOCK: ((isl_ready + !mr_training_enable) * uses_recovered_clock)

START --> FORWARD_RTS: ((isl_ready + !mr_training_enable) * !uses_recovered_clock)

SWITCH_CLOCK: USE_TX_CLOCK(recovered); start forward_rts_timer

SWITCH_CLOCK --> FORWARD_RTS: ((isl_ready + !mr_training_enable) *
forward_rts_timer_done)

FORWARD_RTS: local_rts ← true

Response Response Status C

ACCEPT IN PRINCIPLE.

Slide 3 of the following contribution was reviewed by the
CRG:https://www.ieee802.org/3/dj/public/26_07/ran_3dj_02_2607.pdf

Implement the changes shown on slide 3 of ran_3dj_02_2607 with editorial license.

CI 178B SC 178B.8.3 P896 L40 # R1-182

Slavick, Jeff Broadcom Inc

Comment Type TR Comment Status A (B) (CA)

We've gone from a bit awkward to really awkward.

D3.0 was:

An interface using the E1 format implements one instance of each of the training control, the training frame lock and the coefficient update state diagrams, and their associated variables, functions, counters and timers defined in this subclause, independently for each lane.

D3.1 is :

An interface using the E1 format implements one instance per lane of each of the training control, the training frame lock and the coefficient update state diagrams, and their associated variables, functions, counters and timers defined in this subclause.

SuggestedRemedy

make it:

Each lane of an interface using the E1 format implements one instance of each of the training control, the training frame lock and the coefficient update state diagrams, and their associated variables, functions, counters and timers defined in this subclause.

Response Response Status W

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

CI 178B SC 178B.8.3 P896 L44 # R1-183

Slavick, Jeff Broadcom Inc

Comment Type TR Comment Status A (B) (CA)

We've gone from a bit awkward to really awkward.

D3.0 was:

An interface using the O1 format implements one instance of each of the training control and the training frame lock state diagrams, and their associated variables, functions, counters and timers defined in this subclause, independently for each lane.

D3.1 is :

An interface using the O1 format implements one instance per lane of each of the training control and the training frame lock state diagrams, and their associated variables, functions, counters and timers defined in this subclause..

SuggestedRemedy

make it:

Each lane of an interface using the O1 format implements one instance of each of the training control and the training frame lock state diagrams, and their associated variables, functions, counters and timers defined in this subclause.

Response Response Status W

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

CI **178B** SC **178B.8.3.1** P**897** L**26** # **R1-246**
 Osorio, Luz Nokia
 Comment Type **ER** Comment Status **R** Variables (CA) (B)
 Add a reference to the INITIALIZE definition. Use the same format as the rest of the document.
 "or the value INITIALIZE."
 SuggestedRemedy
 "or the INITIALIZE setting in table 179-8."
 Response Response Status **W**
 REJECT.
 ic_req holds the enumeration not the TX FIR actual value.
 Its value is a set of enumerations which is all the enumerations that ic_sel can take on plus INITIALIZE. The existing definition is correct as written.

CI **178B** SC **178B.8.3.1** P**898** L**6** # **R1-185**
 Slavick, Jeff Broadcom Inc
 Comment Type **TR** Comment Status **A** Variables (CA)
 With the addition of the recovery states (branches out of PATH_READY) the you now have local_rx_ready being used when mr_training_enable = true and training=false. Only when both mr_training_enable = true and training=true can local_rx_ready be dependent upon having optimized the remote transmitter.
 SuggestedRemedy
 Change "mr_training_enable" to "training" in the definition of local_rx_ready.
 Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 Resolved using the response to comment R1-245.

CI **178B** SC **178B.8.3.1** P**898** L**7** # **R1-245**
 Osorio, Luz Nokia
 Comment Type **TR** Comment Status **A** Variables (CA)
 Since the recovery after failure of local_rx_ready does not mandate training takes place (sending ILT frames), it is not possible for the receiver to optimize the remote transmit by requesting adjustments from the local receiver.
 Change mr_training_enable to training in the definition
 SuggestedRemedy
 "that the remote transmit has been optimized if mr_training_enable training is true, that the local receive equalizers have been optimized, and that no further adjustments are required for normal data transmission. The specific conditions for"
 Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 Change
 "if mr_training_enable is true"
 To
 "if training = true"
 Implement with editorial license.

CI **178B** SC **178B.8.3.1** P**898** L**7** # **R1-186**
 Slavick, Jeff Broadcom Inc
 Comment Type **E** Comment Status **A** (B) (CA)
 Missing space
 SuggestedRemedy
 Insert a space at the start of the sentence that begins with "The specific conditions"
 Response Response Status **C**
 ACCEPT.

Cl **178B** SC **178B.8.3.1** P**898** L**8** # **R1-244**

Osorio, Luz

Nokia

Comment Type **ER** Comment Status **A** (B) (CA)

Missing space after period in "[...]" that the remote transmit has been optimized if mr_training_enable is true, that the local receive equalizers have been optimized, and that no further adjustments are required for normal data transmission. The specific conditions for"

SuggestedRemedy

"that the remote transmit has been optimized if mr_training_enable is true, that the local receive equalizers have been optimized, and that no further adjustments are required for normal data transmission. The specific conditions for"

Response Response Status **W**

ACCEPT.

Cl **178B** SC **178B.8.3.1** P**898** L**9** # **R1-314**

Ran, Adeo

Cisco Systems, Inc.

Comment Type **T** Comment Status **A**

The current text can be read as requiring deassertion of local_rx_ready only at 100 ms or later. It should be clarified that implementations may deassert local_rx_ready earlier, but deassertion is mandatory under the stated conditions.

SuggestedRemedy

Replace the text from "The specific conditions for setting this variable to true are implementation dependent" through the end of the local_rx_ready definition with the following:
"The specific conditions for setting this variable to true or false are implementation dependent; however, this variable shall be false when either of the following applies: (a) an input signal corresponding to tx_disable = true in the peer is received for 100 ms (the minimum duration of the quiet_timer) or longer; (b) the training control state diagram (Figure 178B-10a) is in RECOVERY state."

Response Response Status **C**

ACCEPT IN PRINCIPLE.

Implement suggested remedy with editorial license.

Cl **178B** SC **178B.8.3.1** P**898** L**11** # **R1-188**

Slavick, Jeff

Broadcom Inc

Comment Type **TR** Comment Status **A** State diagrams (CA)

Stating that variable takes on a particular value in a given state diagram state means its value should be set by the state diagram.

In Figure 136-7 local_rx_ready is a variable set in the state diagram and depends upon a local_trained variable.

We should take on this same format for 178B-10

SuggestedRemedy

Rename local_rx_ready to local_trained and remove the last sentence of the current definition.

Create a new definition for local_rx_ready with a definition of "Boolean variable that indicates the state of the receiver. The value of local_rx_ready is set by the training control state diagram (Figure 178B-10a and Figure-10b).

In Figure 178B-10a and 10b replace all current instances of local_rx_ready with local_trained

In Figure 178B-10a add "local_rx_ready = false" to the INITIALIZE and RECOVERY states.

In Figure 178B-10b add "local_rx_ready = false" to the LOSS_OF_SIGNAL state.

In Figure 178B-10a add "local_rx_ready <= true" to TRAIN_REMOTE and RX_READY states.

In Figure 178B-10b add "local_rx_ready <= true" to PATH_UP state.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

The following contribution was reviewed by the CRG:

https://www.ieee802.org/3/dj/public/26_07/ran_3dj_05a_2607.pdf

Implement the changes on slides 7, 8, and 9 with editorial license.

Cl **178B** SC **178B.8.3.1** P**898** L**11** # **R1-220**

Law, David Hewlett Packard Enterprise

Comment Type **T** Comment Status **A** State diagrams (CA)

The definition of the local_rx_ready variable in subclause 178B.8.3.1 'Variables' says 'When in the RECOVERY state (Figure 178Bû10a) this variable is false.'. The use of descriptive language ("is") rather than normative language ("shall") makes this a statement of behaviour rather than a requirement. The RECOVERY state of Figure 178Bû10a, however, does not set the local_rx_ready variable to any value.

SuggestedRemedy

Either:

[1] Add the action 'local_rx_ready <= false' to the RECOVERY state of Figure 178Bû10a state diagram

or

[2] Change the text 'When in the RECOVERY state (Figure 178Bû10a) this variable is false.' to read 'It shall be set to false on entry to the RECOVERY state (Figure 178Bû10a)'.

Response Response Status **C**

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-188.

Cl **178B** SC **178B.8.3.3** P**900** L**9** # **R1-145**

Huber, Thomas Nokia

Comment Type **E** Comment Status **A** (B) (CA)

timers should be in alphabetical order

SuggestedRemedy

Move quiet_timer after propagation_timer

Response Response Status **C**

ACCEPT.

Cl **178B** SC **178B.8.3.3** P**902** L**10** # **R1-30**

Hajduczenia, Marek RG Nets

Comment Type **E** Comment Status **A** (B) (CA)

The state box in Figure 178B-10b is labeled "WAIT_DATAPTH", a misspelling of "WAIT_DATAPATH". This is a newly added state in the D3.1 state-diagram rework; the correct spelling appears nowhere in D3.1, so the state name is unresolvable/inconsistent.

SuggestedRemedy

Change the state name in Figure 178B-10b from "WAIT_DATAPTH" to "WAIT_DATAPATH".

Response Response Status **C**

ACCEPT.

[Editor's note: Changed clause/subclause from 0/0 to 178B/178B.8.3.3]

Cl **178B** SC **178B.8.3.5** P**901** L**4** # **R1-247**

Osorio, Luz Nokia

Comment Type **ER** Comment Status **R** (B) (CA)

The starting state uses the name INITIALIZE which could be mistaken with the INITIALIZE coefficient setting. Suggest using START

SuggestedRemedy

Change INITIALIZE to START

Response Response Status **Z**

REJECT.

This comment was WITHDRAWN by the commenter.

CI 178B SC 178B.8.3.5 P901 L26 # R1-187

Slavick, Jeff Broadcom Inc

Comment Type TR Comment Status A State diagrams (CA)

In Figure 136-7 we require that remote_tp_mode be not PAM2 before moving to TRAIN_REMOTE.
In 178B.x.x.x we state that transmitting non-PAM2 is required but there is no normative statement to that effect in that paragraph.

SuggestedRemedy

Add "(remote_tp_mode != PAM2)" as a criteria for the transition from TRAIN_LOCAL to TRAIN_REMOTE in Figure 178B-10a

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy and add new variable as follows:
remote_tp_mode Enumerated variable that corresponds to the "modulation and precoding status" bits in the status field of received training frames. It may be assigned one of the following values: pam2, pam4, pam4 with precoding."

Implement with editorial license.

CI 178B SC 178B.8.3.5 P902 L1 # R1-167

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type TR Comment Status A State diagrams (CA)

Not all implementations may be able to support all the recovery methods shown in Figure 178B-10b. The FAIL state must be supported by all implementations (default).

SuggestedRemedy

Specify an implementation dependent variable that indicates the recovery modes supported as described in osorio_178b_03_260625.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-248

CI 178B SC 178B.8.3.5 P902 L11 # R1-213

Law, David Hewlett Packard Enterprise

Comment Type E Comment Status A (B) (CA)

Subclause 178B.8.1 'State diagram conventions' says 'The notation used in the state diagrams follows the conventions of 21.5.'. Table 2101 'State diagram operators' in subclause 21.5 defines the use of the [left arrow] character as the 'Assignment operator'.

SuggestedRemedy

Change the two instances of the use of the characters '<=' as the assignment operator in the WAIT_DATAPATH state in Figure 178B010b 'Training control state diagram, part 2' to use the [left arrow] character.

Response Response Status C

ACCEPT.

CI 178B SC 178B.8.3.5 P902 L27 # R1-189

Slavick, Jeff Broadcom Inc

Comment Type TR Comment Status A State diagrams (CA)

Not all implementations may be capable of supporting all recover modes.

SuggestedRemedy

Implement the suggested updates in "Indication of support for recovery states" presentation to the June 25, 2026 178B ad-hoc

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-248

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl **178B** SC **178B.8.3.5** P**902** L**40** # **R1-248**
 Osorio, Luz Nokia
 Comment Type **TR** Comment Status **A** State diagrams (CA)
 add recovery options and ability variables as proposed in osorio_178b_03_260625
 An updated proposal presentation will be provided if there are changes required after the June 25th ad-hoc
 SuggestedRemedy
 Implement changes per the presentation proposal
 Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 The following contribution was reviewed by participants at the IEEE 802.3dj Clause 178B ad hoc meeting on June 25:
https://www.ieee802.org/3/dj/public/adhoc/178b/26_0625/osorio_178b_01a_260625.pdf
 A related straw poll taken at the meeting indicated unanimous support as recorded in the following report:
https://www.ieee802.org/3/dj/public/adhoc/178b/26_0625/polls_178b_260625.pdf
 Implement, with editorial license, the proposed changes on slides 5 through 10 of the osorio_178B_01a_260625.

Cl **178B** SC **178B.10** P**906** L**7** # **R1-249**
 Osorio, Luz Nokia
 Comment Type **TR** Comment Status **A** Management (CA)
 Add management variables proposed in osorio_178b_03_260625
 An updated proposal presentation will be provided if there are changes required after the June 25th ad-hoc
 SuggestedRemedy
 Implement changes per the presentation proposal
 Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 Resolved using the response to comment R1-248.

Cl **178B** SC **178B.10** P**906** L**12** # **R1-170**
 Slavick, Jeff Broadcom Inc
 Comment Type **T** Comment Status **A** (B) (CA)
 In Table 178B-6 we list both PMA_reset and PMD_reset which are mapped to the same MDIO register bit and these are then used by the "reset" variable.
 SuggestedRemedy
 In Table 178B-6 update PMA_reset Control variable name to "reset (PMA_reset, PMD_reset)" and remove the PMD_reset row.
 Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.
 Implement the suggested remedy with editorial license.
 In Table 178B-6 update PMA_reset Control variable name to "reset" and remove the PMD_reset row.
 Implement with editorial license.

Cl **178B** SC **178B.10** P**906** L**34** # **R1-172**
 Slavick, Jeff Broadcom Inc
 Comment Type **E** Comment Status **A** (B) (CA)
 Group all the mr variables together
 SuggestedRemedy
 Move mr_adj_los_mode to be after mr_training_enable in Table 178B-6
 Response Response Status **C**
 ACCEPT.

Cl **178B** SC **178B.10** P**907** L**22** # **R1-168**
 Slavick, Jeff Broadcom Inc
 Comment Type **TR** Comment Status **A** (B) (CA)
 remote_tf_lock is derived from the LP status frames not the LD status frames.
 SuggestedRemedy
 Change MDIO reference from 45.2.1.167 to 45.2.1.163 for remote_tf_lock in Table 178B-7.
 Response Response Status **W**
 ACCEPT.

CI **178B** SC **178B.11** P**909** L**25** # **R1-37**

Bruckman, Leon

NVIDIA

Comment Type **TR** Comment Status **A** (B) (CA)

Sec. 178B.9 (Training retimers) contains a normative 'shall': 'The local clock shall meet the signaling rate range specified for the interface' (p. 904, line 4). There is no corresponding PICS entry in the Annex 178B PICS proforma.

SuggestedRemedy

Add a new PICS entry (e.g., RT1) in Sec. 178B.11 for Sec. 178B.9, Status M, confirming the local clock signaling rate requirement.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy with editorial license.

CI **178B** SC **178B.11.4** P**909** L**47** # **R1-38**

Bruckman, Leon

NVIDIA

Comment Type **TR** Comment Status **A** (B) (CA)

Sec. 178B.10 states 'If the MDIO interface is not implemented, an alternate mechanism to access management variables shall be provided' (p. 905, line 37). The Sec. 178B.11.4 Management PICS table has only M1 for MDIO mapping (MD:M) with no entry for alternate access. Sec. 184.12 addresses this correctly with a dedicated '!MD:M' item; Annex 178B has no equivalent.

SuggestedRemedy

Add a new PICS item M2 to Sec. 178B.11.4 with Status '!MD:M', 'Alternate access to management variables when MDIO not implemented,' referencing Sec. 178B.10.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy with editorial license.

CI **179** SC **179.1** P**419** L**17** # **R1-50**

Bruckman, Leon

NVIDIA

Comment Type **TR** Comment Status **A** (B) (E)

Table 179-4 (Relationship between clauses and PHY types defined in Clause 179) lists the associated PCS clause for 1.6TBASE-CR8 as '172--1.6TBASE-R PCS'. Clause 172 is the 800GBASE-R PCS. The 1.6TBASE-R PCS is Clause 175. This is contradicted by 179.3, which correctly states 'the Clause 175 PCS for 1.6TBASE-CR8'. The 800GBASE-CR4 row in the same table correctly shows '172'.

SuggestedRemedy

Change '172--1.6TBASE-R PCS' to '175--1.6TBASE-R PCS' in Table 179-4.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy.

CI **179** SC **179.6** P**422** L**14** # **R1-52**

Bruckman, Leon

NVIDIA

Comment Type **TR** Comment Status **A** delay (B) (E)

Table 179-5 (delay constraints) footnote b states '256 ps for 1.6TBASE-CR8' as the pause_quantum value. For 1.6TBASE, 1 pause_quantum = 320 ps (512 x 0.625 ps), per Clause 174 Table 174-4 footnote c. The value 256 ps is wrong: 232 x 256 ps = 59.4 ns, not 74.24 ns as shown in the table's ns column; 232 x 320 ps = 74.24 ns is consistent. The same '256 ps' error occurs in Table 178-4 footnote b (p. 389, line 14) for 1.6TBASE-KR8.

SuggestedRemedy

In footnote b of Table 179-5, change '256 ps for 1.6TBASE-CR8' to '320 ps for 1.6TBASE-CR8'.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft.

Implement the suggested remedy.

CI 179 SC 179.8.9 P426 L31 # R1-2

Marris, Arthur

Self

Comment Type TR Comment Status A (B) (E)

Comment 409 against draft 3.0 capitalized "local_pattern". This was the correct thing to do as tx_mode enumerated values are in upper case in the base standard.

SuggestedRemedy

In Clauses 179, 180, 181, 182, and 183 capitalize the enumerated values for tx_mode.

Also on page 432 change "(i.e., coef_req is δHoldö)" to "(i.e., coef_req is δHOLDö)"

Capitalize ic_req values in Table 17908 and Table 17909

Response Response Status W

ACCEPT.

[Editor's note: also applies to clauses 179, 180, 181, 182, 183]

CI 179 SC 179.8.9 P426 L31 # R1-9

Hajduczenia, Marek

RG Nets

Comment Type T Comment Status A (B) (CA)

The tx_mode enumerated values were renamed to uppercase (TRAINING, LOCAL_PATTERN, FILL, DATA) in the definition at 178B.8.3.1 (p899), but five PMD body-text references still use the old lowercase value: "tx_mode = local_pattern" on p426, p470, p509, p541, and p571. Additionally, the newly added FILL entry in the definition list (p899) lacks a terminating period ("FILL: transmit either training frames or LOCAL_PATTERN"), unlike the sibling TRAINING/LOCAL_PATTERN/DATA entries.

SuggestedRemedy

Update the five body-text references to "tx_mode = LOCAL_PATTERN" (p426, p470, p509, p541, p571) to match the renamed enumeration, and add a terminating period to the FILL definition on p899.

Response Response Status C

ACCEPT IN PRINCIPLE.

Add a period at the end of the tx_mode/FILL value definition in 178B.8.3.1.

The capitalization is resolved by the response to comment R1-2.

[Editor's note: also applies to annex 178B]

CI 179 SC 179.8.9 P426 L31 # R1-178

Slavick, Jeff

Broadcom Inc

Comment Type TR Comment Status A (B) (E)

We're using upper case for enumerations.

SuggestedRemedy

In 179.8.9, 180.5.12, 181.5.12, 182.5.12, 183.5.12, 185.5.11 change local_pattern to LOCAL_PATTERN.

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-2.

CI 179 SC 179.9.4.2.1 P429 L23 # R1-152

Ghiasi, Ali

Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R CRU (CG)

The CRU definition is inadequate and will introduce linear pulse fit measurement error!

See also unsatisfied comment 264 D3.0.

SuggestedRemedy

Suggest to following improvement:

The CRU has a corner frequency of 4 MHz and a slope of 20 dB/decade up to at least 40 MHz. The CRU maximum jitter transfer peak is 0.3 dB from 40 kHz to 40 MHz.

See ghiasi_3dj_01_2607

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 179 SC 179.9.4.2.3 P431 L22 # R1-184

Slavick, Jeff Broadcom Inc

Comment Type ER Comment Status A (B) (E)

We have updated the enumerations to be upper case so we should use upper case in Table 179-8 too

SuggestedRemedy

Change initialize to INITIALIZE and preset to PRESET in Table 179-8, Table 179-9 and their footnotes

In 178.9.3.4.3 change preset 1 to PRESET 1

In 178.9.3.6 change preset 1 to PRESET 1 in the NOTE (twice)

In 179.8.9 change preset 1 to PRESET 1 and preset 6 to PRESET 6

In 179.9.4.2.1 change "preset 1" to PRESET 1 (removing the "s")

In 179.9.4.2.2 change preset 1 to PRESET 1

In 179.9.5.3.3 change preset 1 to PRESET 1 in step b)

In 179.9.5.3.5 change preset 1 to PRESET 1

In 179.9.5.5.5 change preset 1 to PRESET 1 in the NOTE (twice)

In 176C.6.4.5.3 change preset 1 to PRESET 1 in step 3

In 176C.6.4.7 change preset 1 to PRESET 1 in the NOTE (twice)

In 176D.8.7 change preset 1 to PRESET 1

In 176D.8.12.2 change preset 1 to PRESET 1 in step c)

In 176D.8.12.4 change preset 1 to PRESET 1

In 176D.8.14 change preset 1 to PRESET 1 in the NOTE (twice)

Response Response Status W

ACCEPT.

[Editor's note: also applies to clauses 178, 179, 176C, 176D]

CI 179 SC 179.9.4.8.2 P436 L35 # R1-10

Hajduczenia, Marek RG Nets

Comment Type E Comment Status A (B) (E)

Stray doubled closing parenthesis in the ERL substitution bullets. In 179.9.4.8.2 (p436): "the transmitter sDC22(f))." and in 179.11.3.2 (p448): "the channel sCD11(f))." each have only one opening paren, so the second ")" is stray. Adjacent bullets in the same lists correctly use a single paren (e.g., sCD11(f). / sCD22(f).). The defect also recurs at the parallel s-parameter bullets on p836 and p923.

SuggestedRemedy

Remove the extra closing parenthesis in each affected bullet (e.g., "sDC22(f).", "sCD11(f).") on p436 and p448, and apply the same fix to the parallel occurrences on p836 and p923.

Response Response Status C

ACCEPT IN PRINCIPLE.

remove the stray closing parenthesis in "(f))" in the following locations:

179.9.4.8.2

179.11.3.2

176D.8.3.2

178B.8.3.1

[Editor's note: also applies to clauses 179, 176D, 178B]

CI 179 SC 179.9.5.3.3 P441 L45 # R1-242

Rysin, Alexander NVIDIA

Comment Type E Comment Status A (B) (E)

The word "noise" appears twice in succession:

"For a given noise RMS noise voltage s_ns measured at the Tx test reference, the spectral density of the noise source N_s(f) used for calculation of COM may be calculated using Equation (179018)."

SuggestedRemedy

Change:

"For a given noise RMS noise voltage s_ns measured at the Tx test reference..."

to:

"For a given RMS noise voltage s_ns measured at the Tx test reference..."

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy.

Cl 179 SC 179.11.2 P448 L7 # R1-11

Hajduczenia, Marek

RG Nets

Comment Type TR Comment Status A Fitted IL (E)

A new normative shall-requirement for cable assembly fitted insertion loss (added per comment I-168) computes the value using the 93A.3 procedure with $f_{min} = 0.01$ GHz, $f_{max} = 67$ GHz, and $Df = 0.01$ GHz. An accompanying Editor's note states the I-168 resolution did not provide these parameters and the editor inferred them from 178A.1.3. The inferred values are unconfirmed: 178A.1.3 specifies only a measurement sweep range (≥ 10 MHz to ≥ 67 GHz), not 93A.3 fitting parameters, and a related fitted-IL computation on p921 uses $f_{min} = 0.05$ GHz, conflicting with the 0.01 GHz used here.

SuggestedRemedy

WG to confirm the intended $f_{min}/f_{max}/Df$ for the 93A.3 fitted insertion loss calculation in 179.11.2 and either ratify or correct the inferred values (note the f_{min} discrepancy vs. 0.05 GHz on p921), then resolve and remove the Editor's note.

Response Response Status W

ACCEPT IN PRINCIPLE.

The frequency step Δf is required for the calculation of the fitted insertion loss in 93A.3. For cable assemblies, the D3.1 values (10 MHz for both f_{min} and Δf , per the recommendation in 178A.1.3) enable capturing ripple in the ILdd due to reflections between the ends of the cable. A step of 50 MHz might not capture the ripple correctly and interfere with the fitting calculation. Therefore, the D3.1 values in 179.11.2 are justifiable. However, as the comment notes, the mated test fixtures FOM_ILD specification in 179B.4.1 includes a fitted insertion loss too, and provides only f_{min} (0.05 GHz). For completeness, the frequency step Δf should also be specified. A step of 0.01 GHz would be consistent with the other clauses and with previous specifications, such as Table 162-20.

In the final paragraph of 179B.4.1, add "with $\Delta f = 0.01$ GHz" at the end of the first sentence.

Cl 179 SC 179.11.2 P448 L7 # R1-75

Brown, Matthew

Qualcomm

Comment Type E Comment Status A (B) (E)

This editor's note has served its purpose.

SuggestedRemedy

Delete editor's note.

Response Response Status C

ACCEPT.

Cl 179 SC 179.11.3.2 P448 L45 # R1-199

Dudek, Michael

Marvell

Comment Type TR Comment Status A (B) (E)

The cable assembly S parameters should be used for these tests not those for the channel. Channel isn't defined here

SuggestedRemedy

Change "channel" to "cable assembly" in 4 places

Response Response Status W

ACCEPT.

Cl 179 SC 179.15.3 P459 L3 # R1-146

Healey, Adam

Broadcom Inc.

Comment Type E Comment Status A (B) (E)

Item AUIAPSU states a requirement for the AUI which is already covered in 176C.6.3.. It does not belong in this table.

SuggestedRemedy

Remove item AUIAPSU. Consolidate items *AUI200G and AUIL200G in a single item, "AUIC2C" with Feature "AUI-n C2C" and Value/Comment "PMD is separated from the PCS by an AUI-C2C specified in Annex 120B, Annex 120D, Annex 120F, or Annex 176C".

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the change proposed in the suggested remedy is an improvement to the draft. Implement the suggested remedy with editorial license.

CI **179B** SC **179B.3.1** P**918** L**29** # **R1-150**

Heck, Howard

TE Connectivity

Comment Type **TR** Comment Status **A** reference connector (E)

There may be multiple configurations of MDI connectors (SMT right angle 1x1, Stacked SMT right angle 2x1, Cabled host, Vertical SMT, etc) which can introduce different high speed performance variabilities. It is requested that this clause reference recommended specific configurations to minimize variations between MCB and Cable Assembly suppliers.

SuggestedRemedy

Add text: A single port 1x1 SMT right angle MDI connector or 1x1 Cabled host MDI connector is normative for the MCB specifications. Other connector configurations are informative.

Response

Response Status **C**

ACCEPT IN PRINCIPLE.

The CRG has reviewed slides 9-11 of the contribution

<https://www.ieee802.org/3/dj/public/26_07/ran_3dj_01a_2607.pdf>.

Implement the suggested changes on slide 11 of ran_3dj_01a_2607 with editorial license.

CI **179B** SC **179B.4.1** P**920** L**2** # **R1-149**

Heck, Howard

TE Connectivity

Comment Type **TR** Comment Status **R** reference IL (E)

MTF insertion loss specification and the method for accounting for differences between the fitted insertion loss of an actual test fixture and the reference insertion loss remains an open from D3.0.

SuggestedRemedy

Change page 920, line 9 to ILdd(f) is the fitted insertion loss in dB at frequency f

Change page 918 line 1 and page 918, line 28 and to
The effects of differences between the fitted insertion loss of an actual test fixture and the reference insertion loss are to be accounted for in the measurements.

Add text describing the de-embed/re-embed method for account for the differences between MTF actual and reference insertion loss that will be provided in a planned supporting presentation to be given at the Plenary.

Response

Response Status **Z**

REJECT.

This comment was WITHDRAWN by the commenter.

CI **179B** SC **179B.5.4** P**929** L**32** # **R1-31**

Hajduczenia, Marek

RG Nets

Comment Type **TR** Comment Status **A** (E)

After the 179B.4 restructuring, two PICS items cite the wrong subclause. TF5 (Mated test fixtures common-mode to common-mode return loss, Per Equation (179B-6)) cites 179B.4.4, which is now "Mated test fixtures integrated crosstalk noise" (p927). TF6 (Per Equation (179B-7), common-mode to differential-mode return loss) cites 179B.4.3, which is now the common-mode to differential-mode insertion loss subclause (Equation (179B-8)). Both return-loss requirements and Equations (179B-6)/(179B-7) reside in 179B.4.2.2 "Mated test fixtures modal return loss specifications" (p923). The equation numbers were updated but the subclause references were not.

SuggestedRemedy

In Table 179B-5, change the Subclause cell of both TF5 and TF6 to 179B.4.2.2 (the modal return loss specifications subclause containing Equations (179B-6) and (179B-7)).

Response

Response Status **W**

ACCEPT IN PRINCIPLE.

The CRG reviewed slides 3-7 of the contribution

<https://www.ieee802.org/3/dj/public/26_07/ran_3dj_01a_2607.pdf>

Implement the proposed changes on slide 7 of ran_3dj_01a_2607 with editorial license.

[Editor's note: changed clause subclause from 0/0 to 179B/179B.5.4]

CI **179D** SC **179D.1.1** P**941** L**34** # **R1-32**

Hajduczenia, Marek

RG Nets

Comment Type **E** Comment Status **A** (B) (E)

In the newly added clause referencing Table 179D-5, the PMD type is misspelled "1.6TBAESE-CR8" (letters transposed) instead of "1.6TBASE-CR8". The correct spelling appears elsewhere on the same page.

SuggestedRemedy

Change "Table 179D-5 for 1.6TBAESE-CR8" to "Table 179D-5 for 1.6TBASE-CR8".

Response

Response Status **C**

ACCEPT.

Cl 180 SC 180.1 P461 L10 # R1-64
 Bruckman, Leon NVIDIA
Comment Type E Comment Status A (B) (O)
 Grammatical error: 'These PMDs types provide point-to-point...' -- 'PMDs' combined with 'types' is incorrect. Clause 183.1 correctly reads 'These PMD types provide...' The same error appears in Clause 182.1 (p. 530, line 9).
SuggestedRemedy
 Change 'These PMDs types provide' to 'These PMD types provide' in 180.1 and 182.1.
Response Response Status C
 ACCEPT.

Cl 180 SC 180.1 P462 L6 # R1-271
 Dawe, Piers J G NVIDIA
Comment Type ER Comment Status R PHY tables (B) (CG)
 D3.0 comment 417 which was misunderstood: Table 180-1 and 2, Physical Layer clauses associated with... are clones of each other, and they don't fit on a single page. Each really has three columns. The status (optional, required or conditional) is the same in each. But the presentation obscures the very high level of commonality. Also it is inaccurate: there is no "Clause 117--400 Gb/s RS" for example; Clause 117 is for both 200 Gb/s and 400 Gb/s operation. Really, we are indicating what is relevant in the clause, not which clause we mean, so it should be in a separate column from the clause number.
SuggestedRemedy
 Present these two tables as a single table with four columns: Clause number, 200GBASE-DR1 (with the abbreviated titles from Table 180-1), 400GBASE-DR2 (with the abbreviated titles from Table 180-2), Status.
 Combine footnotes c and e.
 Follow Table 167-2, not Table 122-1 which inefficiently interleaves rows from two tables.
 Make similar improvements in other clauses

Response Response Status W
 REJECT.
 This comment is re-submission of comments I-417 and I-418 against D3.0. The responses to the comments may be found in the following report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_id.pdf
 This new comment points out a different format than considered in the response to those comments.
 Nevertheless, the separate tables as currently in the draft provide clear definition for each of the PMD types defined in the clause without creating a rather dense table combining all four PHY types into a single table. Note also that for many sublayer types multiple rows will be required, e.g., for PCS 119 for 200G/400G, 172 for 800G, 175 for 1.6T. This is also helpful on concise wording in the footnotes. Clause number and "abbreviated" titles are always combined in the same column for previous PMD clauses with rare exceptions; separating the two into separate columns would not be inappropriate but at the same time is not necessary.

Cl 180 SC 180.1 P462 L45 # R1-273
 Dawe, Piers J G NVIDIA
Comment Type ER Comment Status R editorial (Bp) (CG)
 These tables of what makes up a particular Physical Layer are not so useful for teaching beginners because they leave out the elephant in the room: the PMD clause. Beginners don't read everything all at once: they have to start somewhere. I know we have omitted this before.
SuggestedRemedy
 Add a row:
 | 180 | 200GBASE-DR1 PMD | 400GBASE-DR2 PMD | Required |
 So the table can stand by itself.
 And similarly for other similar tables.

Response Response Status C
 REJECT.
 This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.
 The title of this table says "Table 18001uPhysical Layer clauses associated with the 200GBASE-DR1 PMD", which hints clause 180 and 200GASE-DR1 is required when considering this table. Therefore no need to add a row to point that out. The preceding text and the table title state that the table includes additional sublayers. Furthermore, these tables are consistent with multiple previous generations of PMDs.

Cl 180 SC 180.1 P463 L6 # R1-272
 Dawe, Piers J G NVIDIA
Comment Type ER Comment Status R PHY tables (B) (CG)
 D3.0 comment 418 which was misunderstood: Table 180-3 and 4, Physical Layer clauses associated with... are not quite clones of each other, and they don't fit on a single page. Each really has three columns. The status (optional, required or conditional) is the same in each. But the presentation obscures the very high level of commonality and makes it hard to spot the differences (PCS and PMA). Also it is inaccurate: there is no "120F--800GAUI-8 C2C" for example; Annex 120F is for many MAC rates. Really, we are indicating what is relevant in the clause, not which clause we mean, so it should be in a separate column from the clause number.

SuggestedRemedy
 Present these two tables as a single table with four columns: Clause number, 800GBASE-DR4 (with the abbreviated ~titles from Table 180-3), 1.6TBASE-DR8 (with the abbreviated titles from Table 180-4), Status. There will be one more row than in Table 180-3.
 Combine footnote d.
 Follow Table 167-2, not Table 122-1 which inefficiently interleaves rows from two tables.

Response Response Status W
 REJECT.
 Resolved using the response to comment R1-271.

CI **180** SC **180.2** P**464** L**48** # **R1-274**

Dawe, Piers J G

NVIDIA

Comment Type **TR** Comment Status **R** BLER (CB)

D3.0 comment 419: 174A, particularly Table 174A-1, lays out the standard error allocations for the sublayers in a PHY. This paragraph is applicable to pluggable modules where the decisions are made in the module, errors can be counted at its PMA, and some error budget is reserved for the following AUIs in the receive path. But it is not correct for RTL, LPO, NPO and CPO where there is no C2M AUI. For some of these, a different BER added should be used, and for others, the PCS method at line 53 is more relevant.

Suggested Remedy

Either:

Change the first sentence to:

With a compliant input signal, a PMD receiver is expected to meet the block error ratio (BLER) of 1.45×10^{-11} (see 174A.6), measured at the PMA adjacent to the PMD using the method described in 174A.9, with the appropriate BER added. For a receiver in a module with an AUI C2M, BER added is 6.4×10^{-5} . For a receiver where the PMA adjacent to the PMD is the final PMA in the receiver (e.g. in a host ASIC), BER added is 3.2×10^{-5} . For a receiver where the PMA adjacent to the PMD is followed by an AUI C2C then the final PMA in the receiver, BER added is 5×10^{-5} .

Or,

Add this information in 180.9.14 (see another comment) and refer to it here.

Similarly in other optical clauses.

Response Response Status **U**

REJECT.

This comment is a resubmission of comment I-419 against Draft 3.0 recorded in the following report.

https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_id.pdf

The approved response to I-419 is repeated here:

<start>

REJECT.

The comment points out that some implementations may not align with sublayer partitioning as specified by IEEE 802.3.

How such a device determines its compliance is left to the implementer.

Note the last paragraph of 180.2 describes the behavior the PHY is expected to meet for cases when the PMD can not be evaluated standalone. Also, 174A.9 explains how BER added is determined and 174A.13 summarizes the allocation of error ratios to each physical interface.

There is no consensus to make the proposed changes.

<end>

The error allocation provided in 180.2 accounts for the possibility of there being one or two electrical interfaces (e.g., one C2M AUI and one C2C AUI) within a PHY.

Based on the result of straw poll TF-9 there is no consensus to make the proposed changes.

Straw poll TF-9 (directional)

I support adopting the changes proposed in the suggested remedy of comment R1-274.

Yes: 22

No: 34

CI **180** SC **180.2** P**465** L**48** # **R1-275**

Dawe, Piers J G

NVIDIA

Comment Type **ER** Comment Status **R** editorial (Bp) (CB)

D3.0 comment 420 (once in the bucket): the logic of this subclause is obscured. The idea is that we have an FLR expectation, which is translated to a PHY receiver BLER expectation, which is further translated to a PMD receiver BLER expectation using the BER added method. But the second and third paragraphs in this subclause (and similar) are out of order.

Suggested Remedy

Swap the second and third paragraphs so that the logic behind these requirements is more apparent, here and in similar subclauses.

Response Response Status **U**

REJECT.

This comment is a resubmission of Draft 3.0 comment I-420 in the following report:

https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf

There was no consensus to make the proposed change.

No new substantive evidence or rationale is provided in the comment.

No new substantive information on the desired outcome is provided in the suggested remedy.

Cl 180 SC 180.3 P465 L5 # R1-302

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status A service interface (B) (O)

This language "between the PMD and the PMD client" and "receives signal status information from the client sublayer" is arcane and obfuscatory (and not consistent). "Client" is not defined, and not in the vocabulary of the analog engineers who should be reading this clause. It may be needed for the generalised "MAC client" but for each PMD type, there is only one "client" type: PMA or Inner FEC. 174.3.1 uses much more palatable language: "the next higher sublayer".

SuggestedRemedy

In this clause, change "the PMD client" to "the PMA immediately above the PMD". Change "the client sublayer" to "the PMA". Make similar changes in other clauses.

Response Response Status W

ACCEPT IN PRINCIPLE.

In 180.3 the term client appears twice. The term "client sublayer" is commonly used in the base standard to refer to the sublayer immediately above a sublayer. The term "the PMA immediately above the PMD" is a rather large mouthful to repeat especially multiple times. However, it would be helpful to properly introduce the term where it is used and be consistent in terminology. On page 465 line 5, change "PMD client" to "PMD client (PMA immediately above the PMD)"

On page 465 line 20 change "client sublayer" to "PMD client".

Apply the same changes to 181.3, 178.4, and 179.4.

Apply the same changes to 182.3 and 183.3, except replace "PMA" with "Inner FEC".

Implement with editorial license.

Cl 180 SC 180.3 P465 L20 # R1-303

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status A service interface (B) (O)

This has been changed from "the status" to "signal status information" but it is not clear which signal: it might be the "data" signal that the PMA is sending to the PMD, or the one the PMA is receiving from the PMD, or something else. Following a reference above to 174.3 led me to 174.3.1 "The IS_SIGNAL.request primitive is used to define the transfer of signal status to a sublayer from the next higher sublayer" which doesn't seem right: the primitive should transfer something, not define the transfer of something. And still, no indication of which signal. 174.3.3 leads to 116.3.3. 116.3.3.4 gets to the point.

SuggestedRemedy

Back in 180.3, say which signal, and because SIGNAL.request is such an oddball, provide a cross-reference to 116.3.3.4. Make related changes as appropriate.

Response Response Status W

ACCEPT IN PRINCIPLE.

Since the PMD:IS_SIGNAL.request primitive accompanies the PMD:IS_UNIT_DATA.request primitive, it is implied that it relates to the signal in the transmit direction through the client sublayer (a PMA sublayer in this case).

A detailed definition of the PMD:SIGNAL.request primitive is provided in 116.3.3.4. A direct reference would be help. Similarly, a reference to 116.3.3.3 would be helpful for the PMD:IS_SIGNAL.indication primitive.

On page 465 line 15 change "PMD:IS_SIGNAL.indication primitive" to

"PMD:IS_SIGNAL.indication primitive (see 116.3.3.3)".

On page 465 line 20 change "PMD:IS_SIGNAL.request primitive" to

"PMD:IS_SIGNAL.request primitive (see 116.3.3.4)".

Apply similar changes to 178.4, 179.4, 181.3, 182.3, and 183.3.

Implement with editorial license.

Cl 180 SC 180.5.1 P467 L20 # R1-203

Dudek, Michael

Marvell

Comment Type E Comment Status A wording (B) (O)

The sentence can be worded better

SuggestedRemedy

Replace "While TP2 and the patch cord for transmitter measurements and tests are shown in Figure 18302, they are not part of the medium" with "While TP2 and the patch cord used for transmitter measurements and tests are shown in Figure 18302, they are not part of the medium." Make the same change in clauses 181, 182 and 183

Response Response Status C

ACCEPT.

Cl 180 SC 180.5.1 P467 L44 # R1-289

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R signal detect (CG)

D3.0 comment 433: This shows the signal detect after the usual AND gate at the bottom of the diagram, but an unconnected SIGNAL_OK at the top of the diagram. But these are connected; the ILT/RTS functions will be asleep until signal detect reports that there is light coming in and it is worth waking up. A consideration of squelch shows that getting this right can be important. Optical signal detect protects the DSP from processing crosstalk (successfully or not) when there is no input, and it definitively keeps the receiver from getting confused if the light has disappeared and confusing those debugging the network. For these purposes it does not matter that it is slow, nor that it doesn't identify a good signal; it unambiguously identifies a too-weak or no signal. Every optical clause has had this for a long time; 187 for example still does, in the traditional way. But there might be implementations where delivering the signal detect information to the right place in RTS is impractical.

SuggestedRemedy

Show signal detect as a recommended input to "ILT/RTS functions". Several clauses.

Response Response Status U

REJECT.

This comment is a submission of comment I-436 against Draft 3.0.
Resolved using the response to comment R1-290.

Cl 180 SC 180.5.2 P468 L8 # R1-111

mi, guangan

Huawei Technologies Co., Ltd

Comment Type E Comment Status A (B) (O)

"PMD:IS_UNITDATA_0.request to PMD:IS_UNITDATA_n01.request ", the use of primitives in this subclause has been changed to xxxx primitive. Should be consistent.

SuggestedRemedy

change to "PMD:IS_UNITDATA_0.request primitive to PMD:IS_UNITDATA_n-1.request primitive"

Response Response Status C

ACCEPT.

Implement the suggested remedy.

Also, add the word "primitive" where similarly appropriate throughout the draft.
Implement with editorial license.

Cl 180 SC 180.5.4 P468 L48 # R1-290

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R signal detect (CG)

D3.0 comment 436: global signal detect should gate RTS. It would be advisable for the lane-by-lane signal detect to gate ILT training (no light = nothing to train on): see another comment.

SuggestedRemedy

Add sentence that Global_PMD_signal_detect should (recommended) be used as an input to the RTS function of 178A. Several optical clauses.

Response Response Status U

REJECT.

This comment is a restatement of comment I-436 against Draft 3.0.

This comment refers to another comment ("see another comment"), likely R1-289.

IEEE 802.3 is an interoperability specification, not an implementations specification.

Although use of the signal detect variable might be helpful in determining the state of the input signal it is not necessary to mandate or recommend the use of that variable for the purposes listed in the comment.

This clause defines Global_PMD_signal_detect only as a management variable and consequently bits in a management register. Use beyond that is not defined. The quality of a received optical signal is determined by the receiver. The means by which a receiver determines the quality of the optical signal is beyond the scope of this standard, though it might include use of the Global_PMD_signal_detect variable.

Cl 180 SC 180.6.4 P484 L34 # R1-12

Hajduczenia, Marek

RG Nets

Comment Type T Comment Status A TDECQ threshold (O)

Figure 180-11 still labels the nominal sub-eye threshold levels Pth1/Pth2/Pth3, but the surrounding text and Equations (180-4), (180-5), (180-6) were renamed in D3.1 to P0,th1/P0,th2/P0,th3. The figure (which the text says illustrates these thresholds) was not updated, mismatching the equations.

SuggestedRemedy

Update the Figure 180-11 threshold labels from Pth1/Pth2/Pth3 to P0,th1/P0,th2/P0,th3 to match Equations (180-4) through (180-6) and the text on p484.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-93.

[Editor's note: changed clause/subclause from 0/0 to 180/180.6.4]

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl **180** SC **180.7** P**470** L**46** # **R1-238**
 Maniloff, Eric Ciena Corporation
 Comment Type **E** Comment Status **A** editorial (B) (O)
 "(e.g., a 400GBASE-DR4 PMD)" Should refer to 400GBASE-DR2, as this clause is about 200G/lane PMDs
 SuggestedRemedy
 Replace 400GBASE-DR4 with 400GBASE-DR2
 Response Response Status **C**
 ACCEPT.

Cl **180** SC **180.7.1** P**471** L**33** # **R1-231**
 Maniloff, Eric Ciena Corporation
 Comment Type **TR** Comment Status **R** average power (O)
 To meet the (OMAouter)min requirement of -1 + max(TECQ,TDECQ) dBm at max TECQ/TDECQ of 3.4dB with minimum ER requires a minimum of 3.6dBm of optical average launch power. Current average launch power (max) is specified at 4dBm. The margin of 0.4dB is not practical for manufacturing, temperature, aging. A minimum of 1 dB of difference between min and max value is required for product manufacturing and across lifetime and environmental conditions for the worst TDECQ at the minimum ER specified to satisfy the (OMAouter)min requirement. Typically this difference has been 1.5 dB.
 SuggestedRemedy
 Change value in table 180-7, Average launch power, each lane(max) from "4" dBm to "5" dBm.
 Response Response Status **Z**
 REJECT.
 This comment was WITHDRAWN by the commenter.

Cl **180** SC **180.7.1** P**471** L**42** # **R1-237**
 Maniloff, Eric Ciena Corporation
 Comment Type **TR** Comment Status **R** TDECQ CEQ (O)
 Over peaking of transmitters is not effectively prevented. The overshoot limit is not effective as higher bandwidth transmitters could be over peaked and still meet an overshoot specification. Use of Ceq from the Reference Rx gives insight into whether the Tx is overpeaked. With the introduction of the DFE into the Reference Rx, the calculation of Ceq needs to be modified to account for the DFE tap.
 SuggestedRemedy
 Add Ceq_TXBW into Table 180-7 with a Minimum value of 0dB. Similar changes should be made in clauses 181, 182, 183. The definition of this parameter is: $Ceq_TXBW = 10\log_{10}(Ceq) + 10\log_{10}(OMA_{outer}) - 10\log_{10}(OMA_TDECQ) - 10\log_{10}(1+b) + 10\log_{10}(1/(1-b))$. See details in https://www.ieee802.org/3/dj/public/26_05/aloin_3dj_03_2605.pdf
 Response Response Status **W**
 REJECT.
 This is a resubmission of Draft 3.0 Comment I-223 in the following comment report: https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf
 A related contribution in the following link was presented and discussed at the 260624 ad-hoc meeting:
https://www.ieee802.org/3/dj/public/adhoc/optics/0626_OPTX/aloin_3dj_adhoc_01_260624.pdf
 The CRG reviewed the following presentation:
https://www.ieee802.org/3/dj/public/26_07/kimber_3dj_01_2607.pdf
 After CRG discussion there was no consensus to make the proposed change.

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl 180 SC 180.7.1 P471 L44 # R1-295

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ limit (O)

D3.0 comment 466: With the current histogram spacing and width, good links can be made with non-compliant TDECQ and TECQ - because it is not a useful thing to limit, as explained before. It is a penalty, which the receiver can tolerate with no specific limit, as it just one component of required sensitivity. Optimising ECQ at the expense of performance is bad. The things that matter (at 4.56e-4) are the net useful modulation amplitude, OMA-TDECQ, and the residual penalty, K = ECQ -dBo(Ceq), and the overshoot. 800GBASE-LR4 has a limit of 3.9 dB, showing what is possible.

SuggestedRemedy

If the histogram spacing and width remains as is, increase the TDECQ and TECQ limits to 3.8 dB. The net useful modulation amplitude, OMA-TDECQ, is unchanged.
Do this for all IMDD PMDs except 800GBASE-LR4.

Response Response Status U

REJECT.

This comment is a resubmission of Draft 3.0 Comment I-439 in the following comment report

https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf
No new substantive information on the desired outcome is provided in the suggested remedy.

Cl 180 SC 180.7.1 P471 L53 # R1-85

Johnson, John

Broadcom Corporation

Comment Type TR Comment Status A Tx Power Excursion (O)

Update the spec limits for Transmitter power excursion (max) to agree with the revised spec limit for Transmitter overshoot and undershoot.

SuggestedRemedy

Update the spec limit for Transmitter power excursion, each lane (max) in Table 180-7 from 2.3 dBm to 2.9 dBm.
See slides in johnson_3dj_01_0726.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation
https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf

Implement slide 5 with editorial license.

Cl 180 SC 180.7.1 P472 L8 # R1-298

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R OMA (O)

D3.0 comment 442: as line rates are increased, maintaining a particular extinction ratio becomes increasingly challenging (this will be even worse at 400G). However, MPI can get worse at low extinction ratio, when it happens (rarely - fading) although for PAM4 this is a fairly weak function. On the other hand ECQ improves with low extinction ratio (linearity), differently for different technologies but it always happens (not fading). Also, RF power and supply voltage can be reduced at lower extinction ratio, and/or modulators can be made smaller or faster. We have addressed this before, in 100GBASE-DR where we reduced the minimum from 5 dB to 3.5 dB for a 0.3 increase in OMA-TDECQ (see https://www.ieee802.org/3/cd/public/July17/dawe_3cd_01_0717.pdf), then in 100GBASE-FR1, 100GBASE-LR1 and later PMDs when we specified 3.5 dB across the board.
It is time to take the next step in this long-running secular trend.

Notice D3.0 comment 72, proposing reducing the OMA for DR by 0.6 dB, had some support. So it might be reasonable to take the simpler approach of increasing the budget by 0.2 dB by improving the sensitivity by 0.2 dB.

By the way, if an additional 0.2 dB seems large when Table 190-9 says "includes an allocation of 0.1 dB for MPI penalty" some transmitter-related MPI penalty is measured in TDECQ, and in 2017 we estimated 0.3 dB extra for going from 5 dB to 3.5 dB extinction ratio, consistent with this proposal.

We could use a budgeting formula that relates predicted MPI penalty to extinction ratio (not changing TDECQ) if the numbers were thought large enough to make it worthwhile.

SuggestedRemedy

Reduce the extinction ratio limit from 3.5 dB to 3 dB.
Similarly to what we did for 100GBASE-DR: change OMA min from -0.1, -1+max(TECQ, TDECQ) for all extinction ratios, to -0.1, -1+max(TECQ, TDECQ) for extinction ratio >= 3.5 dB
+0.1, -0.8+max(TECQ, TDECQ) for extinction ratio <3.5 dB.
In Table 180-9, Illustrative link power budget, change "Power budget (for max TDECQ)" to Power budget (for max TDECQ and 3.5 dB extinction ratio).
Apply this to DR (180) and DR-2 (182) (the other IMDD clauses have higher reflections).

Or, more simply:

Reduce the extinction ratio limit from 3.5 dB to 3 dB.
Reduce the receiver sensitivity and stressed receiver sensitivity by 0.2 dB
Increase the budget and the allocation for MPI penalty by 0.2 dB.

Apply this to clauses 180 and 182.

Response Response Status U

REJECT.

This comment is a resubmission of Draft 3.0 Comment I-442 in the following comment report

https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Clause, Subclause, page, line

Cl 180

SC 180.7.1

Page 40 of 87

2026-07-16 3:53:11 PM

EE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

No new substantive information on the desired outcome is provided in the suggested remedy

Based on the result of straw poll TF-10 there is no consensus to adopt the proposed changes.

Straw Poll TF-10 (decision)

I support adopting the first option proposed in the suggested remedy of comment R1-298.

Yes: 12

No. 44

Cl	180	SC 180.7.1	P472	L9	#	R1-299
Dawe, Piers J G			NVIDIA			
Comment Type	TR	Comment Status	R			Tx transition time (O)
D3.0 comments 443, 319-321: The transmitter transition time limit was scaled from 100G where a product receiver with FFE-based or other linear equalization was considered. Today at 200G, transmitter bandwidth relative to the line rate is more challenging, and receivers have DSP, with design learnings from copper where the signals are *much* slower, so they tolerate slow signals well. 34/2/2 ~8 ps max is no longer necessary. There may be a relationship between (bandwidth and shape) -> group delay response -> difficult to equalise and error floor, which could be investigated further. However, this is a specification; we specify outcomes, not possible causes. A signal that the ECQ equaliser finds difficult to equalise, gets a bad rating anyway. Transmitter transition time is not very accurate because it relies on the definition of OMA, which is less reliable than we hoped. The relation between bandwidth and transition time is not 1:1. A transmitter implementer with poor (bandwidth and shape) can get a pass on transition time by cranking up the emphasis, which makes the signal worse but compliant. This spec item encourages bad behaviour. The ECQ main tap max, and the overshoot limit, also catch undesirable signals but in a more reasonable, balanced way. We don't need this as well. Notice that for 800GBASE-LR4, the limit is 13 ps for a 7% faster line rate, and it's probably the same receiver DSP that tolerates this.						

SuggestedRemedy

Change 8 ps to 10 ps for all IMDD except 800GBASE-LR4, or remove the item completely and rely on well-chosen ECQ, tap weight and overshoot limits to protect us from bad signals on the far right of the ECQ map.

Response	Response Status
REJECT	U

This is a resubmission of Draft 3.0 Comment I-443 in the following comment report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf

Also, I-443 was resolved in conjunction with comments I-319, I-320, I-321, and I-322. The straw poll taken by the CRG related to this set of comments indicated a clear preference for not increasing the transition time.

This new comments provides no new substantive information to support the proposed changes.

CI 180	SC 180.7.1	P472	L 50	# R1-288
Dawe, Piers J G		NVIDIA		
Comment Type	TR	Comment Status	R	Tx overshoot (O)

27% overshoot is a lot. As it is on each side, the envelope is 156% of the OMA.

As SSPRQ contains 65,000 UI we are basing the measurement on the 6th-worst UI (roughly), which is on the edge of too few for a good measurement. If a signal's overshoot involves ISI but not on those UI, we would miss that. Another SSPRQ-like pattern could give a different result.

SuggestedRemedy

I wonder if $1e-4$ is too ambitious and we should set the spec at e.g. $5e-3$, with different limits

Response	Response Status	U
REJECT.		

The change on overshoot was adopted in D3.0. CRG reached consensus after reviewing the following supporting contribution with data shown in P5:
https://www.ieee802.org/3/di/public/26_05/rodes_3di_01b_2605.pdf

The commentor noted that in the comment the statement "6th-worst UI" should have been "2nd-worst UI" and in the suggested remedy that spec should have been "5e-4" not "5e-3".

The comment does not provide sufficient justification to support the suggested remedy.

The suggested remedy does not provide sufficient detail to implement.

CI 180	SC 180.7.2	P473	L 15	# R1-232
Maniloff, Eric		Ciena Corporation		
Comment Type	TR	Comment Status	R	average power (O)
Changing the Tx Power Max, requires a corresponding update to the Average receive power, each lane(max).				

Suggested Remedy

Change value in table 180-8, Average receive power, each lane(max) from "4" dBm to "5" dBm, and increase the damage threshold to maintain a 1dB offset from the Average receive power (max)

Response	Response Status	Z
REJECT.		

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.8.2 P476 L49 # R1-191

Lewis, David SPECIPHY / Lumentum

Comment Type TR Comment Status R channel IL (O)

The allocation of channel insertion loss to fiber cable attenuation and connection / splice loss does not account for cases where link distance is less than the maximum. For example an optical circuit switch (OCS) may be connected via short fiber cables and a number of connectors / patch panels. A presentation in support of this comment can be provided at the next Task Force meeting.

SuggestedRemedy

Change the second paragraph to: "The maximum link distance is based on an allocation of 0.25 dB total fiber cable attenuation, with the remainder of the maximum channel insertion loss specified in Table 180-12 being allocated to connection and splice loss. For example, with no discrete reflections greater than -55 dB, a total connection and splice loss allocation of 2.75 dB supports either five connections with an average insertion loss per connection of 0.5 dB, or alternatively an optical switch with insertion loss of 2 dB and one connection with insertion loss of 0.5 dB. Connections with different loss characteristics may be used provided the requirements of Table 180-10 are met."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.1 P478 L45 # R1-262

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status R TECQ nomenclature (Bp) (O)

TDECQ and TECQ have changed significantly from previous versions with the conversion from an FFE to a DFE for the reference equalizer. To avoid confusion with TDECQ and TECQ for 100G, the name should be changed to something else.

SuggestedRemedy

Change "Transmitter and Dispersion Eye Closure for PAM4 (TDECQ)" to "Transmitter and Dispersion Eye Closure for PAM4 - DFE (TDECQ-D)". Make similar changes for TECQ. Make the changes where appropriate throughout the document.

Response Response Status W

REJECT.

This change is unnecessary, in effect could result in confusion. Using a different name of the same parameter leads to confusion to reader.

DFE is part of the reference equalizer, and the reference equalizer has been evolving since TDECQ was adopted as a transmitter parameter. We used different ref. equalizers for different speeds and MMF vs SMF. TDECQ's purpose and meaning is not changed by the reference equalizer, just like implementation DSPs using different equalizer schemes are all DSPs, and all form 800GBASE-DR4 module.

CI 180 SC 180.9.2 P479 L23 # R1-151

Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R CRU (CG)

The CRU definition is inadequate and will introduce TDECQ measurement error!
See also unsatisfied comment 253 D3.0

SuggestedRemedy

Suggest to following improvement:

The CRU has a corner frequency of 4 MHz and a slope of 20 dB/decade up to at least 40 MHz. The CRU maximum jitter transfer peak is 0.3 dB from 40 kHz to 40 MHz.
See ghiasi_3dj_01_2607

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.5 P479 L53 # R1-278

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R OMA (O)

The improved OMA definition leaves some loose ends:
If the waveform is an inverted SSPRQ, does one use 7+ threes and 6+ zeros or 6+ threes and 7+ zeros?
Why 6 and 7?
The statistics of 7 vs. 7 or more could be better or worse; it depends what's in SSPRQ.
Other things being equal, excluding "or more" makes the analysis simpler (although no-one would do it by hand anyway). Similarly, does "central 2 UI" give better or worse consistency than 2.5 to 4.5 / 2 to 4? Starting at 2 is probably undesirable.
We changed the definition because a measurement on just two runs is open to error caused by ISI, but measuring with PRBS13Q still has that problem. PRBS13Q is the reason why we say 7 and 6 not 7 and 7.

SuggestedRemedy

Change the text so that:
The definition is with SSPRQ;
Runs of 7, whether zeros or threes, except when using PRBS13Q;
Users are warned that PRBS13Q (7 and 6 because that's what's available) is less accurate; in case of a conflict, SSPRQ takes precedence;
Choose "runs of" vs. "or more" and central vs. 2.5 to 4.5 according to the evidence.

Response Response Status U

REJECT.

The comment does not provide sufficient evidence that the proposed changes are warranted

The draft as it stands is correct and allows both SSPRQ and PRBS 13Q as test pattern, as what has been done in the past for IMDD optical PMDs. Both test patterns are widely adopted in the industry.

Using all patterns that are 6 zeros or more and 7 threes or more is consistent with the industry implementations, therefore it provides better consistency.

CI 180 SC 180.9.5 P480 L5 # R1-112

mi, guangan

Huawei Technologies Co., Ltd

Comment Type E Comment Status A editorial (B) (O)

"OMA_outer" needs editorial fix.

SuggestedRemedy

change outer to subscript.

Response Response Status C

ACCEPT.

CI 180 SC 180.9.6.1 P480 L45 # R1-268

Swenson, Norman

Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status R (withdrawn) (O)

The test says "TDECQ is measured with all receive lanes in operation using pattern 3 or 5 (see Table 180013)." The purpose of this is not clear. This relates to unsatisfied rejected comment I-388 from D3.0.

SuggestedRemedy

Change to: "TDECQ is measured with all receive lanes in operation using pattern 3 or 5 (see Table 180013) in order to subject the optical lane under test to possible crosstalk interference."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.6.1 P480 L45 # R1-223

Swenson, Norman

Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status A TDECQ wording (O)

The text says "TDECQ is measured with all receive lanes in operation using pattern 3 or 5 (see Table 180013)." The purpose of this is not clear. This relates to unsatisfied rejected comment I-388 from D3.0.

SuggestedRemedy

Change to: "TDECQ is measured with all receive lanes in operation using pattern 3 or 5 (see Table 180013) in order to subject the optical lane under test to possible crosstalk interference."

Response Response Status C

ACCEPT IN PRINCIPLE.

Change "TDECQ is measured with all receive lanes in operation" to
"TDECQ is measured with all receive lanes of the PMD under test in operation"

With editorial license.

Cl 180 SC 180.9.6.1 P480 L53 # R1-224

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status R TDECQ wording (O) (Bp)

The text says "For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal." The xAUI-n has two inputs: one from the channel going towards the host, and one from the host going towards the channel. I presume this is meant as the one from the host going towards the channel. That should be clearer. This relates to unsatisfied rejected comment I-390 from D3.0.

SuggestedRemedy

Change to: "For those cases where the optical lane under test is normally fed by an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 180 SC 180.9.6.1 P480 L53 # R1-113

mi, guangcan Huawei Technologies Co., Ltd

Comment Type T Comment Status R (withdrawn) (O)

this sentence talks about using the clock derived from the recovered clock from the AUI signal. The AUI interface here should be the AUI interface immediately attached to the PMA then the PMD under test, therefore can only be an AUI-C2M. AUI-C2C is not relevant here.

SuggestedRemedy

change " an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) " to " an xAUI-n chip-to-module (C2M).

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 180 SC 180.9.6.1 P480 L53 # R1-261

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status A TDECQ wording (O)

The text says "For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal." A channel fed PRBS31Q by a xAUI-n input normally transmits a PRBS31Q. Are we talking about a case where the xAUI input to the channel under test is ignored, and a pattern generator produces an SSPRQ instead that the channel under test transmits? This should be clearer. This relates to unsatisfied rejected comment I-390 from D3.0.

SuggestedRemedy

Change to: "For those cases where the optical lane under test is normally fed by an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal. The xAUI-n input signal is ignored and the optical lane under test transmits the specified test pattern instead."

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed slide 30 of

https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01b_2607.pdf

Implement the changes shown in slide 30 of issenhuth_3dj_01b_2607 with editorial license.

CI 180 SC 180.9.6.1 P480 L53 # R1-154

Ghiasi, Ali Ghiasi Quantum LLC, Marvell Semiconductor, Inc.

Comment Type TR Comment Status R TDECQ/TFT AUI (O)

802.3dj has spend last 1.5 year how to best capture jitter in TDECQ and FRx, unless xAUI-n (C2M) interface operate with jitter tolerance condition in 176D.8.14 TDECQ measurement can be too optimistic.

SuggestedRemedy

Add sentence: For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal. Suggest above text and a figure to be provided to move into a section called "Transmitter Under Test Requirements"
See ghiasi_3dj_02_2605

Response Response Status U

REJECT.

This comment is resubmission of comments I-258 in D3.0 in the following report
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf

A straw poll related to comment I-258 is shown here:

Straw poll TF-17 (directional) from May 2026
I support adoption of the proposed change (blue text) in the top paragraph on slide 9 of ghiasi_3dj_02_2605.
Yes: 10
No: 23

There was clear consensus from the May 2026 straw poll to keep the draft as is, not adding the jitter tolerance condition to the AUI interface when measuring the TDECQ of the PMD transmitter.

The following presentation was reviewed by the CRG
https://www.ieee802.org/3/dj/public/26_07/ghiasi_3dj_02_2607.pdf

No additional new information supporting the suggested remedy or the proposed implementation.

After CRG discussion there was no consensus to make any changes.

Straw poll TF-7 (directional)
I support the proposed changes on slide 9 of ghiasi_3dj_02_2607
Yes: 10
No: 17

CI 180 SC 180.9.6.1 P481 L4 # R1-226

Swenson, Norman Nokia Corporation, Norman Swenson Consulting, Point

Comment Type ER Comment Status R TDECQ wording (Bp) (O)

Section 180.9.6.1 says "The optical splitter and variable reflector are adjusted so that each transmitter is tested with the optical return loss specified in Table 18007. The state of polarization of the back reflection is adjusted to create the greatest RIN." Section 180.9.6.2 on the next page says "The channel provides back reflection with return loss specified in Table 180015. The state of polarization of the back reflection is adjusted to create the greatest RIN." This seems very redundant.

SuggestedRemedy

Eliminate "The channel provides back reflection with return loss specified in Table 180015. The state of polarization of the back reflection is adjusted to create the greatest RIN." from Section 180.9.6.2.

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.6.2 P482 L21 # R1-114

mi, guangcan Huawei Technologies Co., Ltd

Comment Type E Comment Status A TDECQ editorial (B) (O)

A series of variables are defined in this subclause. The variables z_n , y_n , and $x(n)$ are all used to describe discrete values. While $r(t)$, $z(t)$, and $eat(t)$ are used to represent continuous values dependent of t . It is better to change $x(n)$ to x_n so that it is consistent with the discrete naming style.

SuggestedRemedy

change $x(n)$ to x_n . Implement with editorial license to fix other instances.

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the following change:

change $x(n)$ to x_n , where $_n$ means n is subscript.

CI 180 SC 180.9.6.3 P482 L18 # R1-301

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status A TDECQ variable (B) (O)

The notation seems inconsistent. We have z_n and $x(n)$; both seem to be series, or an element in a series, but they are written differently.

SuggestedRemedy

Remove the unnecessary algebra and clean up the remaining notation.

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-94 and R1-114.

CI 180 SC 180.9.6.3 P482 L20 # R1-94

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ editorial (B) (O)

*** Comment submitted with the file image.png attached ***

Notation for $X(n)$ is inconsistent with z_n and y_n

SuggestedRemedy

Change $x(n)$ to x_n

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment #114.

CI 180 SC 180.9.6.3 P482 L30 # R1-296

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ editorial (Bp) (O)

This equation has z on both sides. One can do that in an assignment programming, e.g. $n=n+1$, but not in an equation.

SuggestedRemedy

This equation doesn't represent how an oscilloscope works anyway, but choose a distinct letter for each thing until we start to run out of letters.

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.6.3 P482 L30 # R1-284

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ sampling (O)

This makes out that the ECQ measurement apparatus takes T-spaced samples at ϕ , then ϕ is swept. Neither a sampling scope nor a real-time scope do that; they collect samples all across the waveform / pattern; certain samples fall in the histogram windows, others don't.

SuggestedRemedy

Delete Eq. 180-2. Delete the "sampler" on the left of Figure 180-10, or at least make it clear that it samples the whole waveform at uniform density.

Response Response Status U

REJECT.

The method is consistent itself. Implementations using non-integer number of sample/UI is upon vendor's choice.

During CRG discussion it was noted that the sampler shown is part of the reference equalizer and not intended to show the operation of the scope.

There was no consensus to make the proposed change.

CI 180 SC 180.9.6.3 P482 L32 # R1-297

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ editorial (Bp) (O)

The vector w is not a set, it is a vector or "tuple" where the order matters (but credit for putting w in bold).

SuggestedRemedy

Remove the curly brackets

Response Response Status U

REJECT.

This represents a set of elements and use of "curly brackets" is a conventional way of representing a set of elements.

CI 180 SC 180.9.6.3 P482 L50 # R1-92
 Johnson, John Broadcom Corporation
 Comment Type E Comment Status A TDECQ editorial (Bp) (O)
 Move the note after Figure 180-10 to follow Equation 180-3 where the variable "a" is first referenced.
 SuggestedRemedy
 Remove the NOTE after Figure 180-10.
 After Equation 180-3, insert the text: "where the variable a is an integer in the range 0 to 4 associated with the permitted number of feed-forward precursor taps as specified in Table 180.16."
 Response Response Status C
 ACCEPT.

CI 180 SC 180.9.6.3 P483 L13 # R1-235
 Maniloff, Eric Ciena Corporation
 Comment Type TR Comment Status R TDECQ tap limit (O)
 The maximum main tap coefficient limit $w(0)$ of 2.5 was determined for an FFE reference equalizer in the absence of a DFE. The addition of the DFE in the reference equalizer changes the magnitude of the main tap coefficient of the FFE filter. A proposed reduction of the maximum main tap value is suggested based on the relationship between the DFE tap value and the main tap coefficient evolution due to the addition of the DFE. This reduction to the max limit takes into account the DFE max value of $b(1)$ of $b_{max} = 0.33$. A support presentation will be/has been presented at the 6/24 ad-hoc.
 SuggestedRemedy
 Change value in table 180-16, Main tap coefficient limit $w(0)$ Maximum value from "2.5" to "1.9"
 Response Response Status C
 REJECT.
 This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.
 The following contribution was reviewed and discussed at the June 24 IEEE 802.3dj ad hoc meeting.
https://www.ieee802.org/3/dj/public/adhoc/optics/0626_OPTX/alloin_3dj_adhoc_01_260624.pdf
 After CRG discussion there was no consensus to make the proposed change.

CI 180 SC 180.9.6.3 P483 L13 # R1-285
 Dawe, Piers J G NVIDIA
 Comment Type TR Comment Status A TDECQ tap limit (O)
 D3.0 comment 466: transmitted SMF signals should not be over-emphasised, so the main tap should not be $< \text{approx } 1$, if there were no DFE tap. The minimum used to be 0.9 (one can think of that as 10% tolerance): it was changed to 0.8 when the DFE tap was added. However, over-emphasised signals use little DFE, so 0.8 allows an excessively over-emphasised signal. Long story short: a much more accurate criterion is main tap + feedback tap $< \text{limit}$ (in the same way as the formula for pre-post-cursor tap coefficient difference limit contains the feedback tap). Relative to the signalling rate, 200G silicon is slower than 100G or 50G silicon, and 200G receivers are designed to cope well with slow signals. Designing them to cope with badly set-up signals as well would be wasteful.
 SuggestedRemedy
 Change 0.8 to: $0.9 - b(1)$.
 Response Response Status C
 ACCEPT.

Cl 180 SC 180.9.6.3 P483 L17 # R1-236

Maniloff, Eric

Ciena Corporation

Comment Type TR Comment Status R TDECQ tap limit (O)

The minimum normalized first postcursor tap coefficient limit $w(1)/w(0)$ of -0.6 was determined for an FFE reference equalizer in the absence of a DFE. The addition of the DFE in the reference equalizer changes the magnitude of the first precursor tap coefficient of the FFE filter. A proposed increase of the minimum first postcursor tap value is suggested based on the relationship between the DFE tap value and the first postcursor tap coefficient evolution due to the addition of the DFE. This reduction to the min limit takes into account the DFE max value of $b(1)$ of $b_{max} = 0.33$. A support presentation will be/has been presented at the 6/24 ad-hoc.

SuggestedRemedy

Change value in table 180-16, Normalized feed-forward tap coefficient limits $w(i)/w(0)$, $i=1$
Minimum value from "-0.6" to "-0.27"

Response Response Status C

REJECT.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

The following related contribution was reviewed and discussed at the June 24 IEEE 802.3dj ad hoc meeting.
https://www.ieee802.org/3/dj/public/adhoc/optics/0626_OPTX/aloin_3dj_adhoc_01_260624.pdf

After CRG discussion there was no consensus to make the proposed change.

Cl 180 SC 180.9.6.3 P483 L19 # R1-281

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ tap limit (O)

D3.0 comment 468: The tap weights were created by drawing limit lines that passed a population of transmitters, some time ago. The main tap maximum and the other FFE tap limits need review, taking the DFE into account and considering what is cost-effective for receivers. In particular, the we will see less of the characteristic alternating FFE tap weights because the DFE can do that job better, so taps 3+ should be tightened up.

SuggestedRemedy

Review the tap weight limits and make changes as appropriate.

Response Response Status U

REJECT.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

The suggested remedy does not provide sufficient detail to implement.

CI 180 SC 180.9.6.3 P483 L25 # R1-282

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ tap limit (O)

D3.0 comment 468: causal vs. anticausal pulse shapes are not equivalent. This pre-post equalizer coefficient difference limit $|w(1)/w(0) - b(1) - w(-1)/w(0)|$ seeks to keep the attack and decay of the signal pulse similar. Yet the evidence from https://iee802.org/3/dj/public/25_05/chayeb_3dj_01_2505.pdf#page=8 and 9, plotted in https://iee802.org/3/ds/public/Interim_May_27-28-2026/dawe_3ds_01_260527.pdf, shows that for a real receiver, the situation is very far from symmetrical; relatively fast attack (c(-1) close to zero) and slow decay (c(+1) strongly -ve) is tolerated well (naturally), but the opposite is not.

SuggestedRemedy

Remove the absolute bars | |. If necessary, add a minimum limit, e.g. -0.55, remembering that there are acceptable signals off the bottom of the plot in dawe_3ds_01_260527.

Response Response Status U

REJECT.

This is a resubmission of Draft 3.0 Comment I-468 in the following comment report: https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf. No new substantive information on the desired outcome is provided in the suggested remedy

Note that the contribution cited in the comment (dawe_3ds_01_260527) was not presented to the IEEE 802.3dj task force or CRG, but rather was presented to another task force (802.3ds).

After CRG discussion there was no consensus to make the proposed change.

CI 180 SC 180.9.6.3 P483 L29 # R1-280

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ tap limit (O)

D3.0 comment 469: the feedback tap coefficient limit, 0.33, is too weak, and even worse than in D3.0 (the definition has changed). The low limit encourages bad transmitter setup. Copper clauses use a limit of 0.85.

SuggestedRemedy

At least restore the status quo (0.43) which would leave a good allocation for losses between TP2 and TP5. There is very little traditional frequency-dependent loss between TP2 and TP3, and a moderate amount between TP3 and TP4, so that's a good allocation for losses between TP4 and TP5. Preferably, change the limit to 0.5.

Response Response Status U

REJECT.

The comment didn't provide sufficient justification for the suggested remedy.

The suggested remedy is suggesting to change b(1) limit to 0.43 to account for allocation between losses (assuming it is referred to the passive channel) between TP2 and TP5.

After CRG discussion there was no consensus to make the proposed change.

CI 180 SC 180.9.6.4 P483 L54 # R1-204

Dudek, Michael

Marvell

Comment Type E Comment Status A TDECQ editorial (B) (O)
typo

SuggestedRemedy

Change "capture waveform" to "captured waveform"

Response Response Status C

ACCEPT.

CI 180 SC 180.9.6.4 P484 L1 # R1-115

mi, guangcan

Huawei Technologies Co., Ltd

Comment Type E Comment Status A TDECQ editorial (B) (O)
"assumed à... for Gaussian noise ?(t), ". While when ?(t) was defined as "colored Gaussian noise".

SuggestedRemedy

change to "for colored Gaussian noise ?(t), determine the equalizer"

Response Response Status C

ACCEPT.

CI 180 SC 180.9.6.4 P484 L1 # R1-287

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R TDECQ method (O)

802.3 is not a test specification (the 802.3 companion test specification was withdrawn).

Previously, and for ECQ in other clauses, we had " One way of doing this, using estimated PAM4 symbol error ratio as the figure of merit for the equalized signal, is described below." and "Alternative optimization methods such as minimum mean squared error (MMSE) may be used to determine equalizer tap weights to reduce test time, and are expected to report equal or higher values of TDECQ. These alternative methods should not be used for receiver sensitivity and stressed receiver sensitivity calibration." So it was clear that the normative value is the best value (which does not take long to find, in spite of assertions that it must). We said that a short cut that reads high may be used, where reading high does not weaken the spec. Which would have been the case even if not stated.

But this says: "For a given sampling phase ϕ_0 and assumed RMS value σ_G for Gaussian noise $\eta(t)$, determine the equalizer coefficients w and $b(1)$, subject to the limits in Table 180.16, that result in the minimum mean squared error (MMSE) between y_n and $x(n)$. Alternative optimization methods may be used to determine equalizer tap weights if they report equal or lower mean values of TDECQ." This tries to make the inaccurate high-reading over-prescriptive method normative, but allows better methods, but doesn't say when.

There are other flaws in this but the whole concept is misguided they should be removed when the damage in this draft is undone.

SuggestedRemedy

What a mess. Replace the whole of 180.9.6 with the normal stable ECQ definition, following 167.8.6 (the latest sound version, I believe) with the few known relevant modifications for SMF vs. MMF and the addition of the DFE.

Response Response Status U

REJECT.

Straw poll TF-4 (directional):

I support the replacement of 180.9.6 (TDECQ definition with ECQ definition) as proposed in comment R1-287.

Yes: 16

No: 28

Abstain: 21

The suggested remedy does not provide sufficient detail to implement. Based on straw poll TF-4 there was not consensus to make the proposed change.

CI 180 SC 180.9.6.4 P484 L3 # R1-227

Swenson, Norman

Nokia Corporation,Norman Swenson Consulting,Point

Comment Type TR Comment Status A TDECQ optimization (O)

The text says "Alternative optimization methods may be used to determine equalizer tap weights if they report equal or lower mean values of TDECQ." If alternative methods are proposed they should be spelled out. Having an unspecified test methodology can result in different measurements by different parties, resulting in interoperability issues.

SuggestedRemedy

Delete the sentence unless specific alternative methods are proposed and specified.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P484 L4 # R1-95

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type TR Comment Status A TDECQ optimization (O)

*** Comment submitted with the file image.png attached ***

Current TDECQ reference equalizer optimization method is ambiguous and leaves the method open to interpretation causing measurement repeatability issues.

SuggestedRemedy

Change the following sentence:

"Alternative optimization methods may be used to determine equalizer tap weights if they report equal or lower mean values of TDECQ."

to:

"Alternative optimization methods may be used to determine equalizer tap weights if they report comparable TDECQ values."

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L4 # R1-205

Dudek, Michael Marvell

Comment Type TR Comment Status A TDECQ optimization (O)

If a DUT is tested with an optimization method that results in a lower TDECQ it can have a lower OMA. There is an interoperability issue if the Rx stressed sensitivity is calibrated with the MMSE algorithm and would have had a lower TDECQ if optimized using this other method.

SuggestedRemedy

Change "equal or lower" to "equal or similar"

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L6 # R1-103

El-Chayeb, Ahmad Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ calculation (O)

The TDECQ calculation method described in clause 180.9.6.4 page 484 line 6 through page 487 line 24 is confusing to the reader. A clear TDECQ calculation method helps measurement accuracy and repeatability across different test and measurement vendors.

SuggestedRemedy

Re-write the TDECQ calculation method described in clause 180.9.6.4 starting on page 484 line 6 through page 487 line 24 to be clearer to the reader to ensure measurement repeatability across test and measurement vendors. A supporting presentation with alternative text will be provided.

Response Response Status W

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation
https://www.ieee802.org/3/dj/public/26_07/chayeb_3dj_01_2607.pdf
 slides 21-29 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01a_2607.pdf
 and slide 27 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01c_2607.pdf

Implement slides 22, 23, 24, 25, 26, 28, and 29 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01a_2607.pdf.

Implement slide 27 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01c_2607.pdf.

Implement with editorial license.

Cl 180 SC 180.9.6.4 P484 L6 # R1-96

El-Chayeb, Ahmad Keysight Technologies Inc

Comment Type TR Comment Status A TDECQ eye diagram (O)

Description of eye diagram creation is confusing and does not match the figure referenced.

SuggestedRemedy

TDECQ Measurement does not require an eye diagram. Remove the text below from the draft (lines 6 to 11).

Remove:
 "An eye diagram is formed from the equalized captured waveform, y_n , as follows. With the coefficients fixed at the determined values, sweep the sampling phase ϕ from $\phi_0 \pm T/2$ to $\phi_0 + T/2$. For each phase ϕ , plot the scatter plot of y_n . An example eye diagram is shown in Figure 180.11. The feedback symbol $x(n)$ changes at phase $\phi = \phi_0 \pm T/2$.
 NOTE: The colored Gaussian noise $\eta(t)$ added to $r(t)$ in Figure 180.10 is not reflected in the eye diagram. It is accounted for separately when estimating the SER in the analysis block."

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L7 # R1-116

mi, guangcan Huawei Technologies Co., Ltd

Comment Type E Comment Status A TDECQ editorial (B) (O)

" $\phi_0 \pm T/2$ to $\phi_0 + T/2$ ", the division sign and the number "2" should be straight up

SuggestedRemedy

change the division sign and the number to straight up. Also the case in line 9 on the same page.

Response Response Status C

ACCEPT.

Cl 180 SC 180.9.6.4 P484 L17 # R1-97

El-Chayeb, Ahmad Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ eye diagram (O)

The histogram windows in Figure 180-11 are shown at non-ideal location.

SuggestedRemedy

Center the two histogram windows in the middle of the eye.

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L18 # R1-263

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status R TDECQ eye diagram (O)

Figure 180-11 looks like a continuous time eye diagram, because interpolation has been done between the sample points. A sampled eye diagram consistent with the text would be more appropriate.

SuggestedRemedy

Replace the eye diagram with a similar one that does not interpolate between the discrete sample points.

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 180 SC 180.9.6.4 P484 L21 # R1-93

Johnson, John Broadcom Corporation

Comment Type TR Comment Status A TDECQ threshold (O)

The threshold parameter equations shown in Figure 180-11 are the same as Equations 180-4/5/6, but those equations define the nominal thresholds, which are distinguished by a subscript "0", while the parameters in the figure don't have it.

SuggestedRemedy

Add the subscript "0" to Pth1, Pth2 and Pth3 in Figure 180-11. Because Equations 180-4/5/6 are nearby in the text, it's redundant to show the equations on the figure, and they should be deleted.

Response Response Status C

ACCEPT IN PRINCIPLE.

The resolution of R1-103 will update figure 180-11.

Implement the suggested remedy with editorial license.

Cl 180 SC 180.9.6.4 P484 L21 # R1-309

Dawe, Piers J G NVIDIA

Comment Type TR Comment Status A TDECQ threshold (O)

As it says in page 486 line 52, the actual thresholds, Pth1, Pth2, and Pth3, are adjusted. So they are *not* $P_{th3} = P_{ave} + OMA_TDECQ/3$ as alleged. See D3.0 comment 474.

SuggestedRemedy

Correct Figure 180-11. Either use Pth3 and so on, correctly positioned wrt the eye but not misstated as $P_{ave} + OMA_TDECQ/3$, or use $P_0, th_3 = P_{ave} + OMA_TDECQ/3$, but move the horizontal lines to those levels

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved with the response to R1-93.

CI 180 SC 180.9.6.4 P484 L36 # R1-198

Dudek, Michael Marvell

Comment Type T Comment Status A TDECQ histogram (O)

The histograms should be centered 0.05UI before and after the sampling phase. Not just "nominally".

SuggestedRemedy

Delete "nominally"

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P484 L37 # R1-264

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type TR Comment Status R TDECQ histogram (O)

The text states "Two vertical histograms are measured through the eye diagram, nominally centered 0.05 UI before and after sampling phase ?0." and later "The precise time position of the pair of histogram windows is adjusted to minimize TDECQ while keeping the histogram windows spaced 0.1 UI apart." Since we are searching over \phi_0 to find the optimal sampling phase \phi, there is no need for this language. Also, we need one sample per symbol in each histogram.

SuggestedRemedy

Delete the word nominally from the first sentence and add additional qualifiers to give: "Two vertical histograms are measured through the eye diagram, centered 0.05 UI before and after sampling phase ?0, interpolating the eye diagram if necessary. Each histogram captures one sample for each symbol in the test pattern for each captured repetition of the test pattern." Eliminate the second sentence beginning "The precise time position..."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 180 SC 180.9.6.4 P484 L41 # R1-267

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type TR Comment Status R TDECQ histogram (O)

The current text states: "Each histogram window has a width of 0.04 UI. Each histogram window has outer height boundaries which are set beyond the extremes of the eye diagram (so that no further samples would be captured by increasing the vertical separation of the height boundaries)." It is important that all of the symbols in the test pattern are represented in the histogram, and all of the symbols in the pattern should be represented by the same number of samples so that no symbols are overrepresented or underrepresented. For these reasons, the concept of a window should be eliminated, and the text should state that the histograms should capture a sample for each symbol in the test pattern for one or more repetitions of the test pattern, interpolating if necessary.

SuggestedRemedy

Delete the first sentence in the paragraph and replace the paragraph with the following: "Each histogram has outer height boundaries which are set beyond the extremes of the eye diagram (so that no further samples would be captured by increasing the vertical separation of the height boundaries). Each symbol in the test pattern is represented equitably in each histogram, with interpolation of the eye diagram samples if necessary."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 180 SC 180.9.6.4 P484 L45 # R1-98

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ editorial (O)

Nominal sub-eye thresholds has a 0 subscript that does not show up anywhere else in the clause. The 0 subscript should be removed.

SuggestedRemedy

Change the following text:

"The nominal sub-eye threshold levels P0,th1, P0,th2, and P0,th3, are determined from the OMATDECQ and the average optical power of the eye diagram (Pave) as defined in Equation (18004), Equation (18005), and Equation (18006), and illustrated in Figure 180011."

to:

"The nominal sub-eye threshold levels Pth1, Pth2, and Pth3, are determined from the OMATDECQ and the average optical power of the eye diagram (Pave) as defined in Equation (18004), Equation (18005), and Equation (18006), and illustrated in Figure 180011."

Response Response Status W

ACCEPT IN PRINCIPLE.

The nominal sub-eye threshold levels are changed to include the subscript of 0 per comment 474 of D3.0 in the following comment report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L50 # R1-99

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ editorial (O)

Nominal sub-eye threshold P0,th1 in equation 180-4 has a 0 subscript that does not show up anywhere else in the clause. The 0 subscript should be removed.

SuggestedRemedy

Change "P0,th1" to "Pth1"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P484 L53 # R1-100

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ editorial (O)

Nominal sub-eye threshold P0,th2 in equation 180-4 has a 0 subscript that does not show up anywhere else in the clause. The 0 subscript should be removed.

SuggestedRemedy

Change "P0,th2" to "Pth2"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P485 L2 # R1-101

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type ER Comment Status A TDECQ editorial (O)

Nominal sub-eye threshold P0,th3 in equation 180-4 has a 0 subscript that does not show up anywhere else in the clause. The 0 subscript should be removed.

SuggestedRemedy

Change "P0,th3" to "Pth3"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl 180 SC 180.9.6.4 P485 L17 # R1-13

Hajduczenia, Marek

RG Nets

Comment Type TR Comment Status A TDECQ calculation (O)

D3.1 introduces a new unresolved Editor's note: "The variable yi in the following text is not the same as yn defined above, but rather appears to represent samples of the vertical histogram of yn...". This is an open nomenclature placeholder acknowledging the symbol usage (yi vs yn) is not finalized and should be resolved before publication.

SuggestedRemedy

Resolve the yi/yn nomenclature ambiguity (clarify or rename the histogram-sample variable so it is distinct from yn) and remove the Editor's note, or carry it explicitly as an unresolved-comment item.

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

Cl **180** SC **180.9.6.4** P**485** L**17** # **R1-228**
 Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point
 Comment Type **TR** Comment Status **A** TDECQ calculation (O)
 The use of y_i and y_n in the spec is confusing, as the note indicates.
SuggestedRemedy
 Replace occurrences of y_i on the remainder of this page and the following page with Y_i , and indicate that this is the mean power of the i^{th} histogram bin.
Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 Resolved using the response to comment R1-103.

Cl **180** SC **180.9.6.4** P**485** L**28** # **R1-229**
 Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point
 Comment Type **TR** Comment Status **A** TDECQ calculation (O)
 The method of estimating SER using a "CDF" (which is not really a CDF, because it is not monotonically nondecreasing) for y and a Gaussian approximation is unnecessarily complex and double or triple counts errors. It also excludes the Gaussian tail probabilities as described in swenson_3dj_01a_2603.pdf. This relates to unsatisfied rejected comment I-397 from D3.0.
SuggestedRemedy
 Revise calculation of SER by using Q functions (tabulated integrals of the Gaussian distribution) or with appropriate analytic approximations that can be expressed in closed form. A presentation will give details.
Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 This is a resubmission of Draft 3.0 Comment I-397 in the following comment report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf
 Resolved using the response to comment R1-103.

Cl **180** SC **180.9.6.4** P**485** L**41** # **R1-230**
 Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point
 Comment Type **TR** Comment Status **A** TDECQ calculation (O)
 The CDF(y) method of calculating SER assumes that the eye is open to begin with before noise is added. But with a TDECQ of 3.4 dB, there may be some points that cross the nearest threshold without added noise. This relates to unsatisfied rejected comment I-415 from D3.0.
SuggestedRemedy
 Since the transmitted symbol sequence is known and synchronized with the received waveform, the probability of symbol error can be computed for a particular y given x using a Q function, even when y is in the wrong decision region. Use this method to compute the SER. A presentation will be given with details.
Response Response Status **C**
 ACCEPT IN PRINCIPLE.
 This is a resubmission of Draft 3.0 Comment I-415 in the following comment report:
https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf
 Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P486 L52 # R1-276

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status A TDECQ threshold (O)

D3.0 comment 477 and the changed definition of OMAouter: the threshold adjust range (relaxed from 0% to 1% in 802.3cd/D3.3, April 2018) dates from the early years of ECQ when we didn't have much experience with PAM4 and believed that OMA was accurately measured and representative (which could have been more true then at 27 GBd than it is now at 106 and 113 GBd). 1% is too restrictive and a cause of the disappointing scatter that we see in TDECQ measurements. It's aggressive double jeopardy; ECQ penalises unequal eyes correctly and proportionately without this rule. This rule relies on an "accurate" determination of OMA, which as we have discovered is easier said than done, and the real receiver doesn't know or care about it. It is based on a belief that the thresholds ought to be at 1/6, 1/2, 5/6 which isn't true, even in textbooks, for band-limited PAM4 and for some noise mechanisms. P802.3db/D1.2 (100GBASE-SR1 family) adopted 2% in August 2021 and I have not heard any complaints. Transmitter and receiver implementers are reticent about revealing the limits of their abilities, but I hear that transmitter implementers do not want to spend time and money tweaking for something that doesn't matter and is sometimes counter-productive, while real receivers (same silicon for SMF and MMF) are pretty tolerant and find their own thresholds well enough.

It would be possible to write a more reasonable rule if there were a defined threat that ECQ doesn't already address.

SuggestedRemedy

Change +/-1% to +/-2%

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment is a resubmission of Draft 3.0 Comment I-477 in the following comment report

https://www.ieee802.org/3/dj/comments/D3p0/8023dj_D3p0_comments_final_clause.pdf

Implement suggested remedy with editorial license.

CI 180 SC 180.9.6.4 P486 L53 # R1-102

El-Chayeb, Ahmad

Keysight Technologies Inc

Comment Type TR Comment Status A TDECQ optimization (O)

Current language seems to allow a SER target greater than the target PAM4 SER. This would essentially change the spec line of SER up by 1%.

SuggestedRemedy

Change the following text:

"When the SER is estimated, the actual thresholds, Pth1, Pth2, and Pth3, are adjusted by up to 1% of OMATDECQ to minimize max(SERL, SERR). If max(SERL, SERR) is not within 1% of the target PAM4 SER of 4.56×10^{-4} , the process is iterated by changing sG and reoptimizing the equalizer tap coefficients. TDECQ is calculated using the optimized sG that results in max(SERL, SERR) falling within 1% of the target PAM4 SER."

to:

"When the SER is estimated, the actual thresholds, Pth1, Pth2, and Pth3, are adjusted by up to 1% of OMATDECQ to minimize max(SERL, SERR). If max(SERL, SERR) is not within 1% less than the target PAM4 SER of 4.56×10^{-4} , the process is iterated by changing sG and reoptimizing the equalizer tap coefficients. TDECQ is calculated using the optimized sG that results in max(SERL, SERR) falling within 1% less than the target PAM4 SER."

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P486 L54 # R1-117

mi, guangcan Huawei Technologies Co., Ltd

Comment Type T Comment Status A TDECQ optimization (O)

In the calculation of TDECQ, requiring the calculated max(SER_L, and SER_R) against a target SER range, "within 1% of the target PAM4", leads to non-optimal result and leaves ambiguity in the result. The calculation is numerical with arbitrarily adding noise of RMS sigma_G. It is very possible to approximate the target SER as much as the precision settings allowed. while 1% of the target PAM4 SER is 4.8e-6, a significant SER number by itself, allowing such a big range may lead to variation of the TDECQ calculation result.

SuggestedRemedy

change to ". If max(SERL, SERR) is greater than the target PAM4 SER of 4.56×10^{-4} , the process is iterated by decreasing sG and reoptimizing the equalizer tap coefficients. If max(SERL, SERR) is lower than the target PAM4 SER of 4.56×10^{-4} , the process is iterated by increasing sG and reoptimizing the equalizer tap coefficients." and "TDECQ is calculated using the optimized sG that results in max(SERL, SERR) meeting the target PAM4 SER."

the work "meeting" can be changed to approximating.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P487 L2 # R1-239

Maniloff, Eric Ciena Corporation

Comment Type TR Comment Status A TDECQ optimization (O)

The 1% of the target PAM4 SER is too precise. This would require an accuracy within +/- 0.005 dB in TDECQ and may prevent convergence.

SuggestedRemedy

Remove this criterion or replace it with a target of convergence within +/-0.05dB of TDECQ

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-103.

CI 180 SC 180.9.6.4 P487 L24 # R1-265

Swenson, Norman Nokia Corporation,Norman Swenson Consulting,Point

Comment Type ER Comment Status A TDECQ optimization (O)

We should explicitly state the search range for ϕ_0 .

SuggestedRemedy

Change "TDECQ is the minimum value of TDECQ(ϕ_0) calculated over the range of ϕ_0 ." to "TDECQ is the minimum value of TDECQ(ϕ_0) calculated over the range of ϕ_0 , $0 \leq \phi_0 < T$ ".

Response Response Status C

ACCEPT IN PRINCIPLE.

Change "TDECQ is the minimum value of TDECQ(ϕ_0) calculated over the range of ϕ_0 ." to "TDECQ is the minimum value of TDECQ(ϕ_0) calculated over the range of ϕ_0 , $0 \leq \phi_0 < T$ ".

With editorial license.

CI 180 SC 180.9.8 P485 L45 # R1-207

Dudek, Michael Marvell

Comment Type E Comment Status A TFT editorial (Bp) (O)

It would read better if the pass/fail criterion where moved to the test description rather than being in the histogram description.

SuggestedRemedy

Move "A probable failure is indicated by either of the following:
 • exceeding the TFSEH mask defined in Table 180.17
 • a value greater than 0 in counter test_block_error_bin_i_k (see 174A.9.2), for k greater than 8" from page 489 line 45 to page 487 line 46

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl **180** SC **180.9.8** P**487** L**32** # **R1-307**
Dawe, Piers J G NVIDIA
Comment Type **TR** Comment Status **R** TFT (O)
The TFT section has a blow-by-blow recipe but no introductory statement of what it is trying to do.
SuggestedRemedy
If the TFT section is kept,
Add a sentence explaining what it is for;
Address the relation between the numbers in this section and the other clauses and annexes that have very different numbers;
Adjust the numbers in this section so they relate reasonably to the rest of the draft.
Response Response Status **U**
REJECT.

The suggested remedy does not provide sufficient detail to implement.

Cl **180** SC **180.9.8** P**487** L**33** # **R1-159**
Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.
Comment Type **TR** Comment Status **R** TFT (O)
TFT test no longer defines any conditions for transmitter under test that existed in D3.0 such as xAUI-n input operating and there is counter propagating traffic
SuggestedRemedy
Add following sentence in 180.9.8 - A block diagram showing the FRx is shown in Figure 180.12 with transmitter under test meeting the requirements in XYZ.
In 180.9.6 create a new section called "Transmitter Under Test Requirements"
For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 180.1 through Table 180.4), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal. Will provide the figure. See ghiasi_3dj_02_2609
Response Response Status **Z**
REJECT.

This comment was WITHDRAWN by the commenter.

Cl **180** SC **180.9.8** P**487** L**34** # **R1-313**
Dawe, Piers J G NVIDIA
Comment Type **T** Comment Status **A** TFT (O)
It is not clear to me how AUI errors are accounted for (see Table 174A-1). Normally in 802.3 "a transmitter" means the transmitted signal, impaired by everything behind it including host jitter, power supply noise, AUI errors and so on. Yet I wonder if this is intended as a module test. From Figure 180-12 it seems there are no receive side AUI errors.
SuggestedRemedy
If the TFT section is to be kept, clarify whether the mask is applicable with or without AUI errors in the transmitter. By reference to 174A, explain what to do in the other scenario.
Response Response Status **C**
ACCEPT IN PRINCIPLE.

The Hmax values provided in the draft were calculated with a BERadd of 0.

The CRG reviewed slide 5 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01_2607.pdf

After CRG discussion there was agreement to specify that BERadd equals 0 in the second sentence of 180.9.8.2 with editorial license.

[Editor's note: changed page from 34 to 487]

CI **180** SC **180.9.8** P**487** L**34** # **R1-132**

Huber, Thomas

Nokia

Comment Type **ER** Comment Status **A** TFT nomenclature (O)

As was noted in comment I-121 against D3.0, FRx is a confusing abbreviation to use for "functional receiver", as FR is already used to identify single fiber pair 2 km CWDM PMDs. The abbreviation is used only four times in this clause and subclause 180.9.8.1, and defined again in 180.9.8.1. Since it is used infrequently, it can be deleted without causing any issues in terms of readability of the text.

SuggestedRemedy

In the first paragraph, replace:

The transmitter functional test uses a functional receiver (FRx) defined in 180.9.8.1 to measure a transmitter functional symbol error histogram (TFSEH) defined in 180.9.8.2. A block diagram showing the FRx is shown in Figure 180.12. The transmitter under test is connected to the FRx by a test fiber which meets the requirements in 180.9.6.2.

with:

The transmitter functional test uses a functional receiver defined in 180.9.8.1 to measure a transmitter functional symbol error histogram (TFSEH) defined in 180.9.8.2. A block diagram of the test configuration is shown in Figure 180-12. The transmitter under test is connected to the functional receiver by a test fiber which meets the requirements in 180.9.6.2.

In the second paragraph, replace "FRx" with "functional receiver".

In figure 180-12, delete "(FRx)"

Delete "(FRx)" in the heading of subclause 180.9.8.1, and replace "FRx" with "functional receiver" in the first sentence of that subclause.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

The comment points out a potential issue of the abbreviation "FRx" being confused with a general multi-lane PHY, e.g., 200GBASE-FR1, 400GBASE-FR2, etc. If that is a shared concern then either the abbreviation "FRx" should be deleted or changed to another form that is sufficiently unique.

On page 521 line 5, page 552 line 33, page 585 line 12, change "functional receiver" to functional receiver (FnRx)"

On page 585 line 19, on 521 line 11, page 552 line 39, and in Figure 180-12, change "functional receiver (FRx)" to "FnRx"

On page 489 line 41, change "functional receiver" to "FnRx"

Implement with editorial license.

CI **180** SC **180.9.8.1** P**488** L**26** # **R1-317**

Ran, Adeo

Cisco Systems, Inc.

Comment Type **T** Comment Status **A**

Following up on unsatisfied comment #I-25. As the response indicates, there is an issue to be considered.

"The VOA level is set to the value given by Equation (180–17), in which the first and second terms normalize differences in test fibers and optical receivers, respectively, so that symbol error counts are repeatable across different conditions"

The response claims that receiver differences with respect to jitter tracking are addressed by setting the attenuation based on the functional receiver's jitter tolerance. However, the calibration procedure uses the RS, not SRS, and RS does not include a jitter stress. Even if SRS was used, jitter tolerance is a function of jitter profile. not a single number, and cannot be converted into a single attenuation value; the jitter applied in SRS test is a single frequency plus specific aggressors, and does not represent the jitter profile of the transmitter under test. One functional receiver might have no margin at one point of the stressed test (and thus have no extra sensitivity) but still have margin with respect to the jitter profile of the transmitter under test; another functional receiver may behave differently and have no margin.

The problem of using a specific receiver as a test equipment is fundamental and cannot be circumvented. Users of the standard would expect to get the same results with any functional receiver (as the quoted sentence explicitly suggests), but it is not a realistic expectation.

It is possible that the same problem exists with different test fibers as well.

The least we can do is clarify the situation rather than make false promises.

The alternative, which should be considered, is to make the test method informative.

SuggestedRemedy

Change the quoted sentence to read: "The VOA level is set to the value given by Equation (180–17)".

Add the following NOTE at the end 180.9.8.1:

"NOTE---The intent of the first and second terms of Equation (180–17) is to calibrate differences in test fibers and optical receivers, respectively, to reduce variability across different test conditions. Test results can still vary when significantly different fibers and receivers are used".

Response Response Status **C**

ACCEPT IN PRINCIPLE.

The second part of the referenced sentence is helpful but not essential, i.e., informative. An informative note, as proposed in the suggested remedy, is a good way to separate normative text from the informative. However, the concern addressed by the second sentence proposed in the suggested remedy is sufficiently addressed by the response to comment R1.

77.

Implement the following with editorial license...

Change the quoted sentence to read: "The VOA level is set to the value given by Equation (180–17)".

Add the following NOTE at the end 180.9.8.1:

"NOTE---The intent of the first and second terms of Equation (180–17) is to calibrate differences in test fibers and optical receivers, respectively, to reduce variability across different test conditions."

Cl 180	SC 180.9.8.1	P488	L33	# R1-121
mi, guangan		Huawei Technologies Co., Ltd		
Comment Type	ER	Comment Status	A	TFT editorial (B) (O)
"OMAouter", the outer should be subscript				
SuggestedRemedy				
change outer to subscript.				
Response	Response Status		W	
ACCEPT.				

<i>Cl</i> 180	<i>SC</i> 180.9.8.1	<i>P</i> 488	<i>L</i> 37	# R1-77
Brown, Matthew		Qualcomm		
<i>Comment Type</i>	TR	<i>Comment Status</i>	A	<i>TFT equations (O)</i>

Comment I-25 against Draft 3.0 pointed out that the penalty attributed to a transmitter based on transmitter jitter may be optimistic if the functional receiver has a clock tracking bandwidth much higher than the assumed minimum of 4 MHz. However, the functional receiver calibration as defined in Draft 3.1 induces a penalty for receivers with better worst case tracking bandwidth. This ensures that a false pass does not occur. However, it does impose an unnecessary penalty upon a transmitter with negligible low-frequency jitter. The test as defined ensures no false passes but unfortunately might false fail a transmitter with very good low-frequency but otherwise near-marginal performance. An informative note in this regard would be helpful.

SuggestedRemedy

Add the following (or similar) informative note near the definition of ORx_TECQ_correction variable as follows:

"NOTE--The correction factor given by ORx_TECQ_correction might be moderately pessimistic depending upon the relative characteristics of the device used as the reference receiver and the transmitter under test. A failure of the TFT accurately identifies a transmitter that is not generally interoperable. However, the test might falsely fail a generally interoperable transmitter."

<i>Response</i>	<i>Response Status</i>	C
ACCEPT IN PRINCIPLE.		

The CRG reviewed the following presentation

https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01_2607.pdf

Implement slide 4 with editorial license.

<i>Cl</i> 180	<i>SC</i> 180.9.8.1	<i>P</i> 488	<i>L</i> 40	# R1-305
Dawe, Piers J G		NVIDIA		
<i>Comment Type</i>	TR	<i>Comment Status</i>	A	<i>TFT wording (O) (B)</i>

This says that Test_margin equals 1.5 dB, which decreases the required ORx operating BER to 2.4e5. But, 1.5 dB and 2.4e5 are not related, and there is no *required* ORx operating BER, although an ORx operating BER for calculating the bin mask is chosen in 180.9.8.2. It doesn't "equal" 1.5 dB as the output of some formula or measurement; it was chosen.

SuggestedRemedy

If the TFT section is kept, change to just "It is set to 1.5 dB."

<i>Response</i>	<i>Response Status</i>	W
ACCEPT IN PRINCIPLE.		
Resolved using the response to comment R1-118.		

Ξ E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl 180	SC 180.9.8.1	P488	L40	# R1-118
mi, guangcan		Huawei Technologies Co., Ltd		
Comment Type	T	Comment Status	A	TFT wording (O) (Bp)
"It equals 1.5 dB, which decreases the required ORx operating BER to 2.4 × 10 ⁻⁵ " This sentence seems to say, test margin 1.5dB = operating BER of Orx is 2.4e-5. while the intention I believe is the BER target assuming random error can be tightened to 2.4e-5.				
SuggestedRemedy				
change to "It equals to 1.5dB. The BER target assuming random error is changed to 2.4e-5. The transmitter functional symbol error mask is calculated using this BER target."				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
The CRG reviewed slides 18 and 19 of https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01_2607.pdf				
Implement the changes in slide 19 with editorial license.				

Cl 180	SC 180.9.8.1	P 488	L 46	# R1-120
mi, guangcan		Huawei Technologies Co., Ltd		
Comment Type	ER	Comment Status	A	TFT editorial (B) (O)
"TDECQ max" needs editorial fix				
SuggestedRemedy				
change to "TDECQ (max)"				
Response	Response Status		W	
ACCEPT.				

Cl	180	SC	180.9.8.1	P	488	L	48	#	R1-119
mi, guangcan				Huawei Technologies Co., Ltd					
Comment Type		TR	Comment Status		A	TFT (O)			
this reference of test fiber to Table 180-9 (illustrative link budget) is wrong. Test fiber									
SuggestedRemedy									
delete "specified in Table 180-9". The test fiber and VOA approximates the compliance optical channel specified in Table 180-15 was already stated at the beginning of this section. There is no need to repeat here anyway.									
Response		Response Status C							
ACCEPT IN PRINCIPLE.									
The CRG reviewed slide 8 of https://www.ieee802.org/3/dj/public/26_07/cole_3dj_01a_2607.pdf									
Implement the following change: change the original text in L48, P488									
"is the transmitter under test power budget except instead of the TDECQ max value, uses the TDECQ value measured over the test fiber ***specified in Table 180-9****, and is given by Equation (180-19)"									
to									
"is the transmitter under test power budget specified in Table 180-9 except instead of the max TDECQ value, uses the TDECQ value measured over the test fiber, and is given by Equation (180-19)"									
This resolution overrides the wording changes adopted to the response to comment R1-120									

3E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl **180** SC **180.9.8.1** P**488** L**50** # **R1-283**

Dawe, Piers J G

NVIDIA

Comment Type **TR** Comment Status **R** TFT wording (O)

This says "the best estimate of the actual MPI and DGD penalties of the test fiber", yet the fibre alone doesn't make much MPI. The reflections between transmitter and connectors, and receiver and connectors, are more important. It is not clear which reflections are taken as baseline and which are attributed to the fibre, particularly as both transmitter and receiver reflectances are not necessarily at the spec limits. MPI varies with polarisation yet there is no polarisation control in Figure 180-12.

SuggestedRemedy

If the TFT section is kept;
if MPI penalty variations are important, explain how the MPI penalty is to be estimated and add polarisation control. If MPI penalty variations are not so important or not feasible to estimate with confidence, use a fixed allocation

Response Response Status **U**

REJECT.

The CRG reviewed slides 6 and 7 of
https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01_2607.pdf
and
slides 26 and 27 of
https://www.ieee802.org/3/dj/public/26_07/cole_3dj_01a_2607.pdf

After discussion there was no consensus to make related changes to the draft.

Cl **180** SC **180.9.8.1** P**489** L**19** # **R1-306**

Dawe, Piers J G

NVIDIA

Comment Type **TR** Comment Status **R** TFT (O)

This says that an example estimate of the transmitter under test actual TDECQ value over the test fiber is the measured DUT_TECQ value plus the best estimate of the actual transmitter under test CD penalty over the test fiber. Clearly this is wrong. The best estimate of TDECQ is TDECQ. We know that CD penalty is not exactly the same as TDECQ-TECQ.

SuggestedRemedy

If the TFT section is kept, decide which is meant and use it.

Response Response Status **U**

REJECT.

The suggested remedy does not provide sufficient detail to implement.

The referenced text in the draft was discussed and there was agreement that the text was helpful to the reader.

Cl **180** SC **180.9.8.1** P**489** L**27** # **R1-122**

mi, guangcan

Huawei Technologies Co., Ltd

Comment Type **ER** Comment Status **A** TFT editorial (B) (O)

" OMA (max) spec " needs editorial fix

SuggestedRemedy

change to OMA_outer (max)". Where outer is subscript.

Response Response Status **W**

ACCEPT.

Cl **180** SC **180.9.8.1** P**489** L**30** # **R1-123**

mi, guangcan

Huawei Technologies Co., Ltd

Comment Type **ER** Comment Status **A** TFT editorial (B) (O)

this sentencet "is the actual ORx receiver sensitivity OMA at the TECQ measured for the transmitter under test" needs editorial fix.

SuggestedRemedy

change to "is the Orx receiver sensitivity OMAouter at the measured TECQ of the transmitter under test"

Response Response Status **W**

ACCEPT.

3E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

CI **180** SC **180.9.8.2** P**487** L # **R1-312**

Rubin, Amir

NVIDIA

Comment Type **T** Comment Status **R** TFT hmax (O)

The STD shouldn't aim for Hmax(k) assuming uncorrelated BER of 2.4e-5 for k in the range 1 to 8. This target uncorrelated BER is much lower than the required uncorrelated BER in Table 174A-1 for the PMD-to-PMD segment. Further, failing tested units having Hmax(k) higher than 0 for all k in the range 9 to 16 regardless of the test duration is too aggressive, and for long enough tests will fail tested units that should pass.

SuggestedRemedy

Change the STD to aim for Hmax(k) for k = 2 to 16 assuming uncorrelated BER of 2.28e-4 as required in the PMD-to-PMD. Note: k=1 doesn't fit here, since higher Hmax(1) may result from lower uncorrelated BER.

So the recommended change in table is:

Hmax(2) = 0.223
Hmax(3) = 0.092
Hmax(4) = 0.0284
Hmax(5) = 7e-3
Hmax(6) = 1.44e-3
Hmax(7) = 2.5e-4
Hmax(8) = 3.88e-5
Hmax(9) = 5.28e-6
Hmax(10) = 6.46e-7
Hmax(11) = 7.16e-8
Hmax(12) = 7.27e-9
Hmax(13) = 6.86e-10
Hmax(14) = 5.89e-11
Hmax(15) = 4.76e-12
Hmax(16) = 3.87e-13

Clarification: the Hmax values consider only the PMD-to-PMD uncorrelated BER and ignore the contribution of the remaining parts of the channel defined for the test as shown in Figure 180-12. It is therefore assumed that the test margin of 1.5dB will compensate for these remaining parts.

Response Response Status **C**

REJECT.

The test margin is set to 1.5dB to keep the input signal to the functional receiver at a higher SNR so that the receiver impairment is negligible. The average BER is expected to much lower than the BER allocated to the PMD receiver. This lower average BER was set to 2.4e-5 with consensus from the task force, as shown in straw poll conducted during work group ballot with respect to Draft 2.0 comment #392 in the following comment report: https://www.ieee802.org/3/dj/comments/D2p0/8023dj_D2p0_comments_final_clause_v2.pdf

The CRG did not agree with the rationale provided by the comment and there was no consensus to make the proposed changes.

CI **180** SC **180.9.8.2** P**489** L**33** # **R1-196**

Dudek, Michael

Marvell

Comment Type **E** Comment Status **A** TFT editorial (Bp) (O)

The name is "transmitter functional symbol error histogram on line 36 and on page 487 line 34

SuggestedRemedy

Change "test" to "transmitter"

Response Response Status **C**

ACCEPT.

CI **180** SC **180.9.8.2** P**489** L**34** # **R1-80**

Issenhuth, Tom

Huawei Technologies Co., Ltd, Issenhuth Consulting,

Comment Type **E** Comment Status **A** TFT editorial (Bp) (O)

The subclause heading is incorrect as "Test functional symbol error histogram (TFSEH)"

SuggestedRemedy

Change the subclause heading from
"Test functional symbol error histogram (TFSEH)"
to
"Transmitter functional symbol error histogram (TFSEH)"

Response Response Status **C**

ACCEPT.

CI **180** SC **180.9.8.2** P**489** L**37** # **R1-124**

mi, guangan

Huawei Technologies Co., Ltd

Comment Type **ER** Comment Status **A** TFT editorial (Bp) (O)

"using ORx operating BER = 2.4 × 10⁻⁵" should not use the "=" sign here.

SuggestedRemedy

change to "using ORx operating BER of 2.4e⁻⁵"

Response Response Status **C**

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-118.

Ξ E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl	180	SC	180.9.8.2	P	489	L	37	#	R1-308
			Dawe, Piers J G		NVIDIA				
Comment Type	TR	Comment Status	A	TFT operating BER (O)					
This says "This operating BER is ORx_RxS BER decreased by ...". Yet there is no sensitivity BER. Sensitivity is defined by BLER.									
SuggestedRemedy									
If the TFT section is kept, clear up the logic.									
Response				Response Status	C				
ACCEPT IN PRINCIPLE.									
Resolved using the response to comment R1-118.									

Cl	180	SC	180.9.8.2	P	489	L	38	#	R1-125
			mi, guangcan		Huawei Technologies Co., Ltd				
Comment Type	TR	Comment Status	A	TFT operating BER (O)					
"This operating BER is ORx_RxS BER decreased by amount corresponding to Test_margin increase of ORx_RxS_OMA." again, seems to claim that the ORx with input OMA = ORx_RxS_OMA + test margin, will operate at 2.4e-5. this would be a requirement on the ORx. What if an ORx shows lower BER than 2.4e-5 at this input OMA?									
SuggestedRemedy									
need clarify. Will discuss with the TFSEH team before July meeting to come in with clarification, or withdraw.									
Response				Response Status	C				
ACCEPT IN PRINCIPLE.									
Resolved using the response to comment R1-118.									

Cl	180	SC	180.9.8.2	P	489	L	42	#	R1-14
			Hajduczenia, Marek		RG Nets				
Comment Type	ER	Comment Status	A	TFT editorial (B) (O)					
In subclause 180.9.8.2 (Test functional symbol error histogram), the TFSEH is said to be "measured using a functional receiver as defined in 180.9.8.1 using the method defined in 180.9.8.2" -- the subclause references itself as its own measurement method. This self-reference was introduced by the D3.0->D3.1 renumbering (D3.0 referenced 180.9.9.1 and 180.9.9.2). The actual measurement method is 174A.9.3, as cited by the parent subclause 180.9.8.									
SuggestedRemedy									
Change "using the method defined in 180.9.8.2" to "using the method defined in 174A.9.3" to point to the actual measurement method (consistent with the parent 180.9.8).									
Response				Response Status	W				
ACCEPT.									

Cl	180	SC	180.9.8.2	P	489	L	42	#	R1-126
			mi, guangcan		Huawei Technologies Co., Ltd				
Comment Type	ER	Comment Status	A	TFT editorial (B) (O)					
"using the test pattern as given in Table 180.14." no need of "as":									
SuggestedRemedy									
delete as									
Response				Response Status	W				
ACCEPT.									

Cl	180	SC	180.9.8.2	P	489	L	43	#	R1-206
			Dudek, Michael		Marvell				
Comment Type	E	Comment Status	A	TFT editorial (B) (O)					
Referring to 180.9.8.2 in 180.9.8.2 is a circular reference.									
SuggestedRemedy									
Delete "using the method in 180.9.8.2"									
Response				Response Status	C				
ACCEPT IN PRINCIPLE.									
Resolved using the response to comment R1-14.									

Cl	180	SC	180.9.8.2	P	489	L	43	#	R1-286
Dawe, Piers J G				NVIDIA					
Comment Type		TR	Comment Status		R	TFT (O)			
"Minimum measurement time is 60 seconds", "a value greater than 0 in counter".									
1. 802.3 is not a test specification. There was an 802.3 test specification; it was a separate document which was withdrawn a long time ago. We specify observable behaviours at interfaces.									
2. With not maximum measurement time and no indication of a statistical criterion, this is down to chance. It's not a specification.									
I said I would TR such material.									
SuggestedRemedy									
If the TFT section is kept, define a proper, observable, repeatable criterion, for bins 9+. No limit for some of them would be proper.									
Response		Response Status		U					
REJECT.									
The suggested remedy does not provide sufficient detail to implement.									

Cl	180	SC	180.9.9	P	487	L	40	#	R1-158
Ghiasi, Ali				Ghiasi Quantum LLC,Marvell Semiconductor, Inc.					
Comment Type		TR	Comment Status		A	TDECQ/TFT AUI (O)			
802.3dj has spend last 1.5 year to best capture jitter in TDECQ and FRx, unless xAUI-n (C2M) interface operate with jitter tolerance condition in 176D.8.14 TDECQ measurement can be too optimistic see unsatisfied D3.0 comment 262									
SuggestedRemedy									
Add sentence: For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the tes pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal. Will provide the figure. See ghiasi_3dj_02_2609									
Response		Response Status W							
ACCEPT IN PRINCIPLE.									
The CRG reviewed the following presentation https://www.ieee802.org/3/dj/public/26_07/ghiasi_3dj_02_2607.pdf									
The commentor clarified that the intent of this comment was to insert text that was omitted in D3.1.									
Insert "For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 180-1 through Table 180-4), the clock for the transmitter output is recovered from the xAUI-n input signal." in 180.9.8 with editorial license.									

Ξ E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

Cl 180 SC 180.9.9 P490 L31 # R1-83

Johnson, John

Broadcom Corporation

Comment Type TR Comment Status A overshoot (Bp) (O)

Update 180.9.9, Transmitter overshoot and undershoot, to reference by exception the revised definitions of P0, P3 and OMAouter specified in 180.9.5, rather than the references to 122.8.4 given in 140.7.7.

SuggestedRemedy

Replace "Transmitter overshoot and undershoot are calculated using the methods in 140.7.7, with the exception that the hit ratio used to determine Pmax and Pmin is $1 \hat{1} 10\hat{0}4$." with "Transmitter overshoot and undershoot are calculated using the methods in 140.7.7, with the exceptions that P0, P3 and OMAouter are measured using the methods in 180.9.5 and the hit ratio used to determine Pmax and Pmin is $1 \hat{1} 10\hat{0}4$." Apply to the same sentence in 181.9.9, 182.9.9 and 183.9.9, with editorial license.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation

https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf

Implement slide 6 with editorial license.

Cl 180 SC 180.9.9 P490 L33 # R1-127

mi, guangcan

Huawei Technologies Co., Ltd

Comment Type ER Comment Status A overshoot editorial (B) (O)

P_max and P_min, max and min should be straight up

SuggestedRemedy

change max and min to straight up.

Response Response Status W

ACCEPT.

Cl 180 SC 180.9.10 P490 L43 # R1-84

Johnson, John

Broadcom Corporation

Comment Type TR Comment Status A power excursion (Bp) (O)

Update 180.9.10, Transmitter power excursion, to reflect the $1e-4$ hit ratio introduced in 180.9.9 for Transmitter overshoot and undershoot.

SuggestedRemedy

Replace "Transmitter power excursion is defined in 140.7.8." with "Transmitter power excursion is defined in 140.7.8, with the exception that the hit ratio used to determine Pmax and Pmin is $1 \hat{1} 10\hat{0}4$." Apply to the same sentence in 181.9.10, 182.9.10 and 183.9.10, with editorial license.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation

https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf

Implement slide 7 with editorial license.

CI 180 SC 180.9.14 P491 L45 # R1-195

Dudek, Michael

Marvell

Comment Type T Comment Status A Rx Sensitivity (O)

This comment is in support of comment i-493 against D3.0. For a multilane system what matters is the Frame Loss Ratio of the interface which is a function of the performance of all the lanes. It is not necessary for all the lanes to meet the specification here. Also it is not possible to test the performance of the individual lanes when using scrambled idle (pattern 5). Also the test method called out in 174A.9.6 (allowed by page 492 line 2) specifically allows is a multilane test.

SuggestedRemedy

Delete "of each lane" here and for stressed sensitivity on page 493 line 37. Make the equivalent changes in clauses 181, 182, and 183 (note that the change is not needed in 181.9.15 or 183.9.15 as they already do not say "of each lane". If the committee decision is that all lanes must meet the specification individually then pattern 5 needs to be removed as a test pattern option, 174A.9.6 needs to be deleted and exceptions need to be made to 174A.9.5 and 174A.9.7 which allow the use of 174A.9.6.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed slides 8-16 of https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01a_2607.pdf

Per straw poll TF-6 and following discussion the CRG decision was to implement the following changes with editorial license.

delete pattern 5 from test pattern specified for receiver sensitivity and Stressed receiver sensitivity in Table 180-13.

delete reference of 174A.9.6 from the receiver sensitivity methodology.

Delete the note that begins with "When measuring receiver sensitivity of a complete PHY at the PCS.."

Add an exception in 180.9.14 and 180.9.15 that, when testing using 174A.9.5 or 174A.9.7, the compliance may not be further verified using 174A.9.6.

Similarly update clauses 181, 182, and 183.

Straw poll TF-6 (directional)

I support the changes proposed in slides 14-16 of [issenhuth_3dj_01a_2607](https://www.ieee802.org/3/dj/public/26_07/issenhuth_3dj_01a_2607.pdf)

Yes: 13

No: 10

CI 180 SC 180.9.14 P491 L45 # R1-294

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R Rx Sensitivity (O)

D3.0 comment 493: as is explained at length in 174A.9 (referenced at line 53), receiver sensitivity is a property of the lanes together ("interface BER" concept, transferred to BLER), not each lane separately. We ensure that each transmit lane is good enough individually, and each channel lane individually. We test a receiver by applying the same optical power on each lane, but the receiver can share its error allocation across its lanes how it chooses, just as it can allocate errors to LSB and MSB, or I and Q, within a lane, as it chooses. This is what happens naturally if the errors are checked at the PCS, and it's fine: we put symbol distribution in the FEC to prepare for it. 174A.9.5 says "If this test passes for each lane, then the PHY or xMII Extender will meet the expected codeword error ratio. If this test fails, then the performance may be further verified using the method in 174A.9.6 or 174A.9.7." Those who wish to qualify a multilane receiver (e.g., 800GBASE-DR4) as multiple single-lane PMDs (e.g., 4 x 200GBASE-DR1) will continue to do so; those who don't like 174A.9.6 (evaluate the performance of all physical lanes in an AUI component or PMD) because they don't want to convolve the per-lane BLER histograms together, can continue to use 174A.9.5 or 174A.9.7 (evaluate the performance of each physical lane in an AUI component or PMD).

SuggestedRemedy

Here, remove "of each lane" from line 45 (because the sensitivity of a single lane in a multi-lane PMD is not a spec item) and add "on each lane" on page 492 line 1 after "OMAAouter" because this really is the OMA to be applied to each victim lane. Similarly in 180.9.15 for SRS. Similarly in other IMDD clauses.

I hope that the electrical clauses and AIs don't have this defect to be corrected.

Response Response Status U

REJECT.

Based on the resolution of R1-195 there was no consensus to make the proposed change.

CI 180 SC 180.9.14 P491 L49 # R1-128

mi, guangan

Huawei Technologies Co., Ltd

Comment Type TR Comment Status A Rx Sensitivity (O)

"The TECQ of the conformance test signal is measured according to 180.9.6, except that the test fiber is not used." is repeating what is said in 180.9.7, defining TECQ.

SuggestedRemedy

change to "The TECQ of the conformance test signal is measured according to 180.9.7"

Response Response Status C

ACCEPT.

Cl 180	SC 180.9.14	P492	L2	# R1-129
mi, guangcan		Huawei Technologies Co., Ltd		
Comment Type	TR	Comment Status	A	Rx Sensitivity (O)
174A.9.6 is PMA method of all lanes in PMD. While to measure the receiver sensitivity, the lowest OMAouter value, of any one lane, using all lane method can not isolate the contribution to the error histogram of the lane under test from all other lanes. the all lanes PMA method can not be easily adopted to measure the receiver sensitivity.				
SuggestedRemedy				
delete 174A.9.6, also in stressed receiver sensitivity.				
Response		Response Status C		
ACCEPT IN PRINCIPLE.				
Resolved using the response to R1-195.				

Cl	180	SC	180.9.14	P	492	L	5	#	R1-277
Dawe, Piers J G				NVIDIA					
Comment Type		TR	Comment Status		A	Rx Sensitivity (O)			
The correct BERadded is a normative fact; it should not be in an informative NOTE. Table 180-19 provides BERadded for only one scenario, while there are several (D3.0 comment 419). We should do a thorough job in the table, and either refer to it in 180.2 which is explaining why the spec is like it is, or repeat the info there.									
SuggestedRemedy									
At line 2, change "measured at the PMA using the test method in either 174A.9.5, 174A.9.6, or 174A.9.7" to "measured at the PMA using the test method in either 174A.9.5, 174A.9.6, or 174A.9.7, or at the PCS using the method of 174A.11". Delete the first informative note. Change "The error mask Hmax(k) to be used in the method of 174A.9.5 is provided in Table 180-20." to "The error mask Hmax(k) to be used in the method of 174A.9.5 for a detachable PMD/PMA is provided in Table 180-20." In Table 180-19, provide each BERadded as a separate row or sub-row: Errors counted at a detachable PMA that could be followed by AUI C2M and AUI C2C 6.4e-5 Errors counted at a detachable PMD/PMA that is followed by an AUI C2C 4 x 10^-5 Errors counted at a non-detachable PMA in the host 3.2 x 10^-5 Errors counted at the PCS in the host 3.2 x 10^-5 In 180.2, after "BERadded equal to 6.4 x 10^-5.", add "See Table 180-19 for other scenarios." Similarly in other optical clauses.									
Response		Response Status		U					
ACCEPT IN PRINCIPLE.									
Several of the suggested remedy points were solved in the response to comment R1-195 however after CRG discussion there was no consensus to adopt the other proposed changes in the suggested remedy that were not addressed by the resolution of R1-195.									

Cl 180 SC 180.9.14 P492 L6 # R1-130

mi, guangan Huawei Technologies Co., Ltd

Comment Type TR Comment Status A Rx Sensitivity (O)

this note says "When measuring receiver sensitivity of a complete PHY at the PCS", the receiver sensitivity is not a characteristic of a complete phy, it is defined as the characteristic of the PMD, and is defined as per lane characteristic. A complete phy using a pmd compliant to CL180 needs to satisfy the error ratio requirement as defined in 180.2. however, extending the phy requirement to receiver sensitivity definition and method is wrong and un-necessary.

SuggestedRemedy

delete the note.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-195

Cl 180 SC 180.9.14 P492 L8 # R1-279

Dawe, Piers J G NVIDIA

Comment Type TR Comment Status R Rx Sensitivity (O)

This NOTE contains quantitative technical information relating to compliance. It is not "informative"

SuggestedRemedy

Promote it to regular text.

Response Response Status C

REJECT.

The note provides a way to enable test with reasonable time-frame for manufacturing, but falls short of guaranteeing that the BLER target where the more formal test method is used. It is therefore provided as an informative note rather than normative.

Cl 180 SC 180.9.14 P492 L11 # R1-15

Hajduczenia, Marek RG Nets

Comment Type E Comment Status A RXS editorial (Bp) (O)

The new slope-check NOTES in 180.9.14 (p492) and 180.9.15 (p494) have unbalanced parentheses: "(log10(Hm(k)) - log10(Hm(4))) / (k - 4)" should be less than "(log10(Hm(4)) - log10(Hm(2))) / 2". Each leading "(" is never closed, making the scope of the division ambiguous. The document's own equivalent text on p398 and p439 uses correctly bracketed numerator differences. The same defect recurs on p494, p523, p554, p555, and p587.

SuggestedRemedy

Group the numerator differences against the divisors so the expressions read ((log10(Hm(k)) - log10(Hm(4)))) / (k - 4)) and ((log10(Hm(4)) - log10(Hm(2)))) / 2), consistent with p398/p439. Apply to all occurrences (p492, p494, p523, p554, p555, p587).

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 180 SC 180.9.14 P492 L20 # R1-79

Issenhuth, Tom Huawei Technologies Co., Ltd,Issenhuth Consulting,

Comment Type T Comment Status A Rx Sensitivity (O)

In offline consensus building there was agreement to add a new exception to Table 180-18.

SuggestedRemedy

In Table 180-18 add a line for "Average launch power of each non test signal lane (max)" with a value of -15dBm. A supporting presentation will be provided

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed slides 21 and 22 of the following presentation:
https://www.ieee802.org/3/dj/public/26_07/cole_3dj_01a_2607.pdf

A straw poll was taken

Straw poll TF-5 (decision)
I support closing comment R1-79 as per the suggested remedy with editorial license.
Yes: 16
No: 7

The consensus of the CRG was to accept the suggested remedy.
Implement the suggested remedy with editorial license.

CI 180 SC 180.9.14 P492 L30 # R1-208

Dudek, Michael

Marvell

Comment Type TR Comment Status R LRS (O)

The receiver sensitivity test is intended to ensure inter-operability with worst case transmitters that have the given TDECQ/TECQ. It is expected that a low extinction ratio will be more worst case for a receiver than a high extinction ratio, therefore the min extinction ratio should be used for the LRS test.

SuggestedRemedy

Change the min extinction ratio to 3.5dB and the max to 4.5dB in table 180-18.

Response Response Status U

REJECT.

The CRG reviewed slides 21-23 of https://www.ieee802.org/3/dj/public/26_07/cole_3dj_01a_2607.pdf

The ER specification of LRS compliance signal is correctly set.
The idea of LRS is to create a very good Tx signal as low stress to find out the receiver's best performance (OMA-TDECQ) so shifting ER to lower side is not low stress.

CI 180 SC 180.9.14 P493 L11 # R1-148

Healey, Adam

Broadcom Inc.

Comment Type E Comment Status A RXS editorial (B) (O)

Per IEEE 802.3 editorial guidelines
<http://www.ieee802.org/3/WG_tools/editorial/requirements/words.html#numbers>, "Do not include any separators in the digits to the right of the decimal point or include any trailing zeros in tables or in text."

SuggestedRemedy

Remove trailing zeros from values in Table 180-20 as necessary. Similarly in Table 178-11, Table 179-14, and Table 185-16.

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

CI 180 SC 180.9.15 P493 L45 # R1-16

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A SRS editorial (B) (O)

The newly inserted bullet reads "greater than 200MHz" with no space between value and unit, contrary to IEEE/SI style (the draft uses "4 MHz", "27 MHz" elsewhere). The same defect repeats in the parallel inserted bullets on p523, p555, and p587.

SuggestedRemedy

Change "greater than 200MHz." to "greater than 200 MHz." on p493, and apply the same fix to the identical bullets on p523, p555, and p587.

Response Response Status C

ACCEPT.

CI 180 SC 180.9.15 P493 L45 # R1-304

Dawe, Piers J G

NVIDIA

Comment Type TR Comment Status R SRS interference (O)

The frequency of the sinusoidal interferer was changed from minimum 100 MHz to minimum 200 MHz, but it's still a single frequency. The sampling scope aliases it to some other frequency and the software FFE will amplify it according to the apparent frequency, not the real one, causing error.

SuggestedRemedy

1. Change from a sine tone to a PRBS generator (not synchronous to the victim) with a high enough signalling rate that wherever the aliasing takes the apparent frequencies to (there are many real frequencies in a PRBS), the overall enhancement will be reasonably consistent. A PAM2 PRBS is more "bounded-like" than a PAM4 one as well as having a uniform spectrum.
2. Limit the amount of bounded interference.
3. In 121.8.10.1, we say "care should be taken to avoid harmonic relationships between the sinusoidal interferer, the sinusoidal jitter, the signaling rate, and the pattern repetition rate". After taking advice from scope experts: add the sampling rate of the scope to that list.

Response Response Status U

REJECT.

The following contribution was reviewed by the CRG:
https://www.ieee802.org/3/dj/public/26_07/dawe_3dj_02_2607.pdf

The contribution provided good material to understand the limitation of one aspect of the current SRS specification and provided a better understanding of the proposal.

However, the proposal is not sufficiently well developed to adopt in its current form.

There is no consensus to adopt the proposed changes.

CI 180 SC 180.12.4.1 P499 L7 # R1-251

Law, David Hewlett Packard Enterprise

Comment Type T Comment Status A PICS (Bp) (O)

PICS item F9 'Global Signal Detect function', referencing subclause 180.5.4, says 'Report to the PMD service interface the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' and item F10 'Global Signal Detect behavior', also referencing subclause 180.5.4, says SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes'.

I don't think this is correct, as the Global_PMD_signal_detect variable defined in subclause 180.5.4 'PMD global signal detect function' is only used by the MDIO registers, with the PMD:IS_SIGNAL.indication primitive generated by the RTS function.

In addition, doesn't a 200GBASE-DR1 PMD have only one lane, a 400GBASE-DR2 PMD have only two lanes, but a 1.6TBASE-DR8 PMD have eight?

SuggestedRemedy

Delete PICS item F9 and change the Value/Comment text of PICS item F10 from 'SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes' to 'Global_PMD_signal_detect is an indicator of the presence of an optical signal on all lanes, or lane 0 for a 200GBASE-DR1 PMD.

Response Response Status C

ACCEPT IN PRINCIPLE.
The comment addresses an error in the draft. However, it is sufficient to use "all lanes" for any of the PMD types since all lanes of a single-lane PHY would be the one lane, designated lane 0.
Delete PICS item F9
Change the Value/Comment text of PICS item F10 from 'SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes' to 'Global_PMD_signal_detect is an indicator of the presence of an optical signal on all lanes'.

CI 180A SC 180A.4 P947 L18 # R1-33

Hajduczenia, Marek RG Nets

Comment Type E Comment Status A (B) (O)

Two defects in rewritten D3.1 text. (1) Misplaced closing parenthesis in the 2-lane PMD sentence: "(400GBASE-DR2 or 400GBASE-DR2-2 is implemented with this optical connector)," should close after the PMD list, as in the three sibling paragraphs on the same page and the parallel paragraph on p945. (2) Grammar error: "When an single 8-lane PMD (1.6TBASE-DR8 or 1.6TBASE-DR8-2)..." should use "a".

SuggestedRemedy

(1) Move the closing parenthesis: "... (400GBASE-DR2 or 400GBASE-DR2-2) is implemented with this optical connector, ...". (2) Change "When an single 8-lane PMD" to "When a single 8-lane PMD".

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement suggested remedy with editorial license. Scrub the subclause for any additional incorrect uses of "an".

CI 181 SC 181.1 P503 L8 # R1-65

Bruckman, Leon NVIDIA

Comment Type E Comment Status A editorial (B) (O)

Two subject-verb disagreements: (1) 'The optical signals generated by this PMD type is modulated' -- plural 'signals' requires 'are modulated'. (2) 'The PMD provide point-to-point' -- singular 'PMD' requires 'provides'.

SuggestedRemedy

(1) Change 'is modulated' to 'are modulated'. (2) Change 'The PMD provide' to 'The PMD provides'.

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

CI 181 SC 181.7.1 P511 L31 # R1-86

Johnson, John Broadcom Corporation

Comment Type TR Comment Status A Tx Power Excursion (O)

Update the spec limits for Transmitter power excursion (max) to agree with the revised spec limit for Transmitter overshoot and undershoot.

SuggestedRemedy

Update the spec limit for Transmitter power excursion, each lane (max) in Table 181-5 from 2.9 dBm to 3.5 dBm.

See slides in johnson_3dj_01_0726.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation

https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf

Implement slide 5 with editorial license.

CI 181 SC 181.8.2 P516 L52 # R1-192

Lewis, David SPECIPHY / Lumentum

Comment Type T Comment Status R channel IL (O)

The allocation of channel insertion loss to fiber cable attenuation and connection / splice loss does not account for cases where link distance is less than the maximum. For example an optical circuit switch (OCS) may be connected via short fiber cables and a number of connectors / patch panels. A presentation in support of this comment can be provided at the next Task Force meeting.

SuggestedRemedy

Change the second paragraph to: "The maximum link distance is based on an allocation of 0.25 dB total fiber cable attenuation, with the remainder of the maximum channel insertion loss specified in Table 181-10 being allocated to connection and splice loss. For example, with no discrete reflections greater than -55 dB, a total connection and splice loss allocation of 3.25 dB supports either six connections with an average insertion loss per connection of 0.5 dB, or alternatively an optical switch with insertion loss of 2 dB and two connections with average insertion loss of 0.5 dB. Connections with different loss characteristics may be used provided the requirements of Table 181-8 are met."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 181 SC 181.9.6 P519 L53 # R1-155

Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R TDECQ/TFT AUI (O)

802.3dj has spend last 1.5 year how to best capture jitter in TDECQ and FRx, unless xAUI-n (C2M) interface operate with jitter tolerance condition in 176D.8.14 TDECQ measurement can be too optimistic.

SuggestedRemedy

Add sentence: For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal.

Suggest above text and a figure to be provided to move into a section called "Transmitter Under Test Requirements" Either locally create the section or just reference the section created in 180.9.6.x

See ghiasi_3dj_02_2605

Response Response Status U

REJECT.

Resolved using the response to comment R1-154.

CI 181 SC 181.9.8 P521 L3 # R1-133

Huber, Thomas Nokia

Comment Type TR Comment Status A editorial (B) (O)

The intent seems to be to refer to 180.9.8, but the text says 181.9.8 (which is this clause)..

SuggestedRemedy

Change 181.9.8 to 180.9.8.

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-17.

Cl 181 SC 181.9.8 P521 L3 # R1-17

Hajduczenia, Marek

RG Nets

Comment Type ER Comment Status A editorial (B) (O)

The sentence "The transmitter functional symbol error histogram is measured using the method defined in 181.9.8" is a circular self-reference (181.9.8 is the subclause containing the sentence). In D3.0 the corresponding text referenced 180.9.9, and the parallel D3.1 clauses 182 and 183 correctly reference 180.9.8. The D3.0->D3.1 renumbering incorrectly applied the 181 prefix.

SuggestedRemedy

Change "measured using the method defined in 181.9.8" to "measured using the method defined in 180.9.8".

Response Response Status W

ACCEPT.

Cl 181 SC 181.9.8 P521 L3 # R1-197

Dudek, Michael

Marvell

Comment Type ER Comment Status A editorial (B) (O)

This is a circular reference.

SuggestedRemedy

Change 181.9.8 to 180.9.8

Response Response Status W

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-17.

Cl 181 SC 181.9.8 P521 L11 # R1-134

Huber, Thomas

Nokia

Comment Type ER Comment Status A TFT delete FRx (O)

Defining "FRx" here adds no value since it is not used a second time.

SuggestedRemedy

Delete "(FRx)"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-132

Cl 181 SC 181.9.8 P521 L13 # R1-209

Dudek, Michael

Marvell

Comment Type TR Comment Status A TFT (O)

Table 181-13 requires minimum loss. Including the VOA in this description isn't possible.

SuggestedRemedy

Either delete "VOA and" or add "with the exception that the channel loss is not minimum".
Make the equivalent change in clauses 182 and 183.

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the second option add "with the exception that the channel loss is not minimum".

Apply this change to clause 180, 181, 182, and 183.

Implement with editorial license.

Cl 181 SC 181.9.11 P522 L1 # R1-78

Issenhuth, Tom

Huawei Technologies Co., Ltd, Issenhuth Consulting,

Comment Type T Comment Status A Extinction ratio (O)

D3.0 comment #490 made a change in 180.9.12 (180.9.11 in D3.1). The comment response did not state to make the same changes in 181/182/183

SuggestedRemedy

To keep the IMDD clauses aligned in 181.9.11, 182.9.11 and 183.9.11 Change the existing paragraph
"The extinction ratio of a PAM4 optical signal is defined as the ratio of the average optical launch power level P3, measured over ... waveforms captured at the output of the reference receiver defined in 18x.9.2."
to
"The extinction ratio of a PAM4 optical signal is defined as the ratio of the average optical launch power levels P3 and P0, as defined for OMA_outer (see 18x.9.5).
With editorial license

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 181 SC 181.9.14 P522 L52 # R1-89

Johnson, John

Broadcom Corporation

Comment Type E Comment Status A LRS wording (O) (Bp)

The sentence describing the application of the LRS TX exception table in 180.9.14 needs some additional clarification for better readability.

SuggestedRemedy

Replace "... meets the transmit characteristics given in Table 18105 except for characteristics in Table 180018 with references in 181.9." with "... meets the transmit characteristics given in Table 18105 except for the characteristics listed in Table 180018, with the references to 180.9 in that table replaced by the corresponding references in 181.9."

Response Response Status C

ACCEPT IN PRINCIPLE.

Note that comments R1-90 and R1-91 express the same concern in clauses 182 and 183, respectively.

The comment expresses concern that the references in the reference column of Table 180-18 are not wholly relevant to to Clause 181. The reference column is not strictly necessary in Table 180-18. These references are provided in transmitter characteristics table in each clause. Removing that column in Table 180-18 would resolve this concern in this comment. In Table 180-18 remove the reference column.

With editorial license.

Cl 181 SC 181.12.4.1 P528 L3 # R1-252

Law, David

Hewlett Packard Enterprise

Comment Type T Comment Status A PICS (Bp) (O)

PICS item F10 'Global Signal Detect function', referencing subclause 181.5.4, says 'Report to the PMD service interface the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' and item F11 'Global Signal Detect behavior', also referencing subclause 181.5.4, says SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes'.

I don't think this is correct, as the Global_PMD_signal_detect variable defined in subclause 181.5.4 'PMD global signal detect function' is only used by the MDIO registers, with the PMD:IS_SIGNAL.indication primitive generated by the RTS function.

SuggestedRemedy

Delete PICS item F10 and change the Value/Comment text of PICS item F11 from 'SIGNAL_DETECT is a global indicator ...' to read 'The Global_PMD_signal_detect variable is a global indicator ...'.

Response Response Status C

ACCEPT IN PRINCIPLE.

Implemented the suggested remedy with editorial license.

Cl 182 SC 182.1 P534 L35 # R1-55

Bruckman, Leon

NVIDIA

Comment Type T Comment Status A (B) (O)

Incorrect cross-reference in 182.1: 'the purpose of each 800 Gb/s PHY sublayer is summarized in 116.2.' Subclause 116.2 covers 200 Gb/s/400 Gb/s Ethernet sublayer purposes; 800 Gb/s sublayer purposes are summarized in 169.2. The analogous Clause 180.1 (closely related clause) correctly says '169.2'.

SuggestedRemedy

Change 'summarized in 116.2' to 'summarized in 169.2' in the 800 Gb/s sentence of 182.1.

Response Response Status C

ACCEPT.

Cl 182 SC 182.5.7 P540 L30 # R1-18

Hajduczenia, Marek

RG Nets

Comment Type T Comment Status A (B) (O)

The D3.1 rename PMD_global_transmit_disable -> Global_PMD_transmit_disable was applied to Clauses 179/180/181/185/187 and to the Clause 182/183 variable-mapping PICS tables (p558 Table 182-18, p589), but was missed in the Clause 182/183 body text and PICS detail rows, which still use the old name: 182.5.7 body (p540, two sentences), Clause 182 PICS item F13 (p561), 183.5.7 body (p570), and Clause 183 PICS item F14 (p592). The same variable is now named two ways within these clauses.

SuggestedRemedy

Change PMD_global_transmit_disable to Global_PMD_transmit_disable in the 182.5.7 body (p540), Clause 182 PICS F13 (p561), the 183.5.7 body (p570), and Clause 183 PICS F14 (p592), so all references match the renamed PICS table entries (p558, p589) and the other PMD clauses.

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

IEEE P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comments

Cl 182 SC 182.7.1 P542 L35 # R1-233

Maniloff, Eric Ciena Corporation

Comment Type TR Comment Status R average power (O)

To meet the (OMAouter)min requirement of -1 + max(TECQ,TDECQ) dBm at max TECQ/TDECQ of 3.4dB with minimum ER requires a minimum of 3.6dBm of optical average launch power. Current average launch power (max) is specified at 4dBm. The margin of 0.4dB is not practical for manufacturing, temperature, aging. A minimum of 1 dB of difference between min and max value is required for product manufacturing and across lifetime and environmental conditions for the worst TDECQ at the minimum ER specified to satisfy the (OMAouter)min requirement. Typically this difference has been 1.5 dB.

SuggestedRemedy

Change value in table 182-7, Average launch power, each lane(max) from "4" dBm to "5" dBm.

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 182 SC 182.7.1 P543 L8 # R1-87

Johnson, John Broadcom Corporation

Comment Type TR Comment Status A Tx Power Excursion (O)

Update the spec limits for Transmitter power excursion (max) to agree with the revised spec limit for Transmitter overshoot and undershoot.

SuggestedRemedy

Update the spec limit for Transmitter power excursion, each lane (max) in Table 182-7 from 2.3 dBm to 2.9 dBm.
See slides in johnson_3dj_01_0726.

Response Response Status C

ACCEPT IN PRINCIPLE.

The CRG reviewed the following presentation
https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf

Implement slide 5 with editorial license.

Cl 182 SC 182.7.2 P544 L16 # R1-234

Maniloff, Eric Ciena Corporation

Comment Type TR Comment Status R average power (O)

Changing the Tx Power Max, requires a corresponding update to the Average receive power, each lane(max).

SuggestedRemedy

Change value in table 182-8, Average receive power, each lane(max) from "4" dBm to "5" dBm, and increase the damage threshold to maintain a 1dB offset from the Average receive power (max)

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

Cl 182 SC 182.8.2 P547 L52 # R1-193

Lewis, David SPECIPHY / Lumentum

Comment Type T Comment Status R channel IL (O)

The allocation of channel insertion loss to fiber cable attenuation and connection / splice loss does not account for cases where link distance is less than the maximum. For example an optical circuit switch (OCS) may be connected via short fiber cables and a number of connectors / patch panels. A presentation in support of this comment can be provided at the next Task Force meeting.

SuggestedRemedy

Change the second paragraph to: "The maximum link distance is based on an allocation of 1 dB total fiber cable attenuation, with the remainder of the maximum channel insertion loss specified in Table 182-12 being allocated to connection and splice loss. For example, with no discrete reflections greater than -55 dB, a total connection and splice loss allocation of 3 dB supports either six connections with an average insertion loss per connection of 0.5 dB, or alternatively an optical switch with insertion loss of 2 dB and two connections with average insertion loss of 0.5 dB."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 182 SC 182.9.2 P550 L29 # R1-153

Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R CRU (CG)

The CRU definition is inadequate and will introduce TDECQ measurement error!
See also unsatisfied comment 252 D3.0.

SuggestedRemedy

Suggest to following improvement:
The CRU has a corner frequency of 4.27 MHz and a slope of 20 dB/decade up to at least 42.7 MHz. The CRU maximum jitter transfer peak is 0.3 dB from 4.27 kHz to 42.7 MHz.
See ghiasi_3dj_01_2607

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 182 SC 182.9.6 P551 L36 # R1-156

Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R TDECQ/TFT AUI (O)

802.3dj has spend last 1.5 year to best capture jitter in TDECQ and FRx, unless xAUI-n (C2M) interface operate with jitter tolerance condition in 176D.8.14 TDECQ measurement can be too optimistic
see unsatisfied D3.0 comment 260

SuggestedRemedy

Add sentence: For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal.
Suggest above text and a figure to be provided to move into a section called "Transmitter Under Test Requirements" Either locally create the section or just reference the section created in 180.9.6.x
See ghiasi_3dj_02_2605

Response Response Status U

REJECT.

Resolved using the response to comment R1-154.

CI 182 SC 182.9.8 P552 L39 # R1-135

Huber, Thomas Nokia

Comment Type ER Comment Status A TFT delete FRx (O)

Defining "FRx" here adds no value since it is not used a second time.

SuggestedRemedy

Delete "(FRx)"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-132

CI 182 SC 182.9.8 P552 L44 # R1-19

Hajduczenia, Marek RG Nets

Comment Type ER Comment Status A (B) (O)

In 182.9.8 (p552) and 183.9.8 (p585) the renumbered cross-references are wrong: the text cites Equation (180-5) and Equation (180-6) for Tx_DUT_power_budget and Channel_insertion_loss, but in D3.1 those are the PAM4 threshold equations Pth2=Pave and Pth3 (p484-485), unrelated to the power budget. The power-budget quantities are defined by Equation (180-18) and Equation (180-19) (p488-489). The parallel Clause 181 functional-test text on p521 correctly cites (180-18)/(180-19). The D3.0 references (180-28)/(180-29)/(180-31) were mis-renumbered.

SuggestedRemedy

In 182.9.8 (p552) and 183.9.8 (p585), change "Equation (180-5)" to "Equation (180-18)" and "Equation (180-6)" to "Equation (180-19)", matching the power-budget definitions in 180.9.8.1 and the parallel Clause 181 text on p521.

Response Response Status W

ACCEPT.

CI 182	SC 182.9.14	P554	L 18	# R1-90
Johnson, John		Broadcom Corporation		
Comment Type	E	Comment Status	A	LRS wording (O) (B)
The sentence describing the application of the LRS TX exception table in 180.9.14 needs some additional clarification for better readability.				
SuggestedRemedy				
Replace "... meets the transmit characteristics given in Table 182.9.14 except for characteristics in Table 180.9.14 with references in 182.9." with "... meets the transmit characteristics given in Table 182.9.14 except for the characteristics listed in Table 180.9.14, with the references to 180.9 in that table replaced by the corresponding references in 182.9."				
Response	Response Status C			
ACCEPT IN PRINCIPLE. Resolved using the response to comment R1-89.				

CI 182	SC 182.12.4.1	P 561	L 9	# R1-253
Law, David		Hewlett Packard Enterprise		
Comment Type	T	Comment Status	A	PICS (B) (O)
PICS item F9 'Global Signal Detect function', referencing subclause 182.5.4, says 'Report to the PMD service interface the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' and item F10 'Global Signal Detect behavior', also referencing subclause 182.5.4, says SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes'.				
I don't think this is correct, as the Global_PMD_signal_detect variable defined in subclause 182.5.4 'PMD global signal detect function' is only used by the MDIO registers, with the PMD:IS_SIGNAL.indication primitive generated by the RTS function.				
In addition, doesn't 200GBASE-DR1-2 PMD have only one lane, a 400GBASE-DR2-2 PMD have only two lanes, but a 1.6TBASE-DR8-2 PMD have eight?				
SuggestedRemedy				
Delete PICS item F9 and change the Value/Comment text of PICS item F10 from 'SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes' to read 'Global_PMD_signal_detect is an indicator of the presence of an optical on all lanes, or lane 0 for a 200GBASE-DR1-2 PMD.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
The comment addresses an error in the draft. However, it is sufficient to use "all lanes" for any of the PMD types since all lanes of a single-lane PHY would be the one lane, designated lane 0.				
Delete PICS item F9				
Change the Value/Comment text of PICS item F10 from 'SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes' to 'Global_PMD_signal_detect is an indicator of the presence of an optical signal on all lanes'.				

CI/ 183	SC 183.4.1	P567	L22	# R1-54
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	delay (B) (O)
Clause 183.4.1 states the maximum delay is '59,392 bit times (96 pause_quanta or 74.24 ns)'. The value '96 pause_quanta' is arithmetically wrong: 59,392 bit times / 512 = 116 pause_quanta; 116 x 640 ps = 74.24 ns. With 96 pause_quanta: 96 x 640 ps = 61.44 ns, inconsistent with the stated 74.24 ns. All other 800GBASE 4-lane PMD delay subclauses (178.6, 179.6, 180.4.1, 181.4.1, 182.4.1) correctly state 116 pause_quanta. The error was copied from Table 169-4 which also has this bug.				
SuggestedRemedy				
Change '96 pause_quanta' to '116 pause_quanta' in 183.4.1.				
Response	Response Status W			
ACCEPT.				

CI 183	SC 183.7.1	P573	L39	# R1-88
Johnson, John		Broadcom Corporation		
Comment Type	TR	Comment Status	A	Tx Power Excursion (O)
Update the spec limits for Transmitter power excursion (max) to agree with the revised spec limit for Transmitter overshoot and undershoot.				
SuggestedRemedy				
Update the spec limit for Transmitter power excursion, each lane (max) in Table 183-6 from 2.9 dBm to 3.5 dBm for 800GBASE-FR4 and from 3.8 dBm to 4.4 dBm for 800GBASE-LR4. See slides in johnson_3dj_01_0726.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
The CRG reviewed the following presentation https://www.ieee802.org/3/dj/public/26_07/johnson_3dj_01_2607.pdf				
Implement slide 5 with editorial license.				

Ξ E P802.3dj D3.1 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet 1st SA recirculation ballot comment

CI 183 SC 183.8.2 P579 L52 # R1-194

Lewis, David SPECIPHY / Lumentum

Comment Type T Comment Status R channel IL (O)

The allocation of channel insertion loss to fiber cable attenuation and connection / splice loss does not account for cases where link distance is less than the maximum. For example an optical circuit switch (OCS) may be connected via short fiber cables and a number of connectors / patch panels. A presentation in support of this comment can be provided at the next Task Force meeting.

SuggestedRemedy

Change the second paragraph to: "The maximum link distance for 800GBASE-FR4 is based on an allocation of 1 dB total fiber cable attenuation, with the remainder of the maximum channel insertion loss specified in Table 183-11 being allocated to connection and splice loss. For example, with no discrete reflections greater than -55 dB, a total connection and splice loss allocation of 3 dB supports either six connections with an average insertion loss per connection of 0.5 dB, or alternatively an optical switch with insertion loss of 2 dB and two connections with average insertion loss of 0.5 dB."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 183 SC 183.9.6 P583 L49 # R1-157

Ghiasi, Ali Ghiasi Quantum LLC,Marvell Semiconductor, Inc.

Comment Type TR Comment Status R TDECQ/TFT AUI (O)

802.3dj has spend last 1.5 year to best capture jitter in TDECQ and FRx, unless xAUI-n (C2M) interface operate with jitter tolerance condition in 176D.8.14 TDECQ measurement can be too optimistic
see unsatisfied D3.0 comment 261

SuggestedRemedy

Add sentence: For those cases where there is an xAUI-n chip-to-chip (C2C) or chip-to-module (C2M) interface (see Table 18001 through Table 18004), the clock source for the test pattern is derived from the clock recovered from the xAUI-n input signal. The xAUI-n input signal is that described for the Jitter tolerance test in 176D.8.14. The pattern of the xAUI-n input signal may be PRBS31Q, scrambled idle, or a valid xBASE-R signal.
Suggest above text and a figure to be provided to move into a section called "Transmitter Under Test Requirements" Either locally create the section or just reference the section created in 180.9.6.x
See ghiasi_3dj_02_2605

Response Response Status U

REJECT.

Resolved using the response to comment R1-154

CI 183 SC 183.9.8 P585 L18 # R1-136

Huber, Thomas Nokia

Comment Type ER Comment Status A TFT delete FRx (O)

Defining "FRx" here adds no value since it is not used a second time.

SuggestedRemedy

Delete "(FRx)"

Response Response Status W

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-132

CI 183 SC 183.9.14 P587 L1 # R1-91

Johnson, John Broadcom Corporation

Comment Type E Comment Status A LRS wording (O) (B)

The sentence describing the application of the LRS TX exception table in 180.9.14 needs some additional clarification for better readability.

SuggestedRemedy

Replace "... meets the transmit characteristics given in Table 18306 except for characteristics in Table 180018 with references in 183.9."
with "... meets the transmit characteristics given in Table 18306 except for the characteristics listed in Table 180018, with the references to 180.9 in that table replaced by the corresponding references in 183.9."

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-89.

Cl 183	SC 183.12.4.1	P592	L7	# R1-254
Law, David		Hewlett Packard Enterprise		
Comment Type	T	Comment Status	A	PICS (B) (O)
PICS item F10 'Global Signal Detect function', referencing subclause 183.5.4, says 'Report to the PMD service interface the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' and item F11 'Global Signal Detect behavior', also referencing subclause 183.5.4, says SIGNAL_DETECT is a global indicator of the presence of optical signals on all four lanes'.				
I don't think this is correct, as the Global_PMD_signal_detect variable defined in subclause 183.5.4 'PMD global signal detect function' is only used by the MDIO registers, with the PMD:IS_SIGNAL.indication primitive generated by the RTS function.				
SuggestedRemedy				
Delete PICS item F10 and change the Value/Comment text of PICS item F11 from 'SIGNAL_DETECT is a global indicator ...' to read 'The Global_PMD_signal_detect variable is a global indicator ...'.				
Response	Response Status C			
ACCEPT IN PRINCIPLE.				
Implemented the suggested remedy with editorial license.				

Cl 184	SC 184.12.4.2	P619	L37	# R1-36
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	(B) (L)
Sec. 184.6.1 (Receive signal sampling) contains a normative 'shall': 'Each Rx_XI, Rx_XQ, Rx_YI, and Rx_YQ signal shall be sampled at a sampling rate equal to or greater than the 800GBASE-LR1 signaling rate' (p. 607, line 43). The Receive Function PICS table (Sec. 184.12.4.2) starts at RF1 for Sec. 184.6.2, skipping Sec. 184.6.1 entirely. This 'shall' requirement is not represented by any PICS conformance item.				
SuggestedRemedy				
Add a new PICS entry (e.g., RF0) to Sec. 184.12.4.2 for Sec. 184.6.1 'Receive signal sampling,' Status M, confirming that the ADC sampling rate meets the minimum 800GBASE-LR1 signaling rate requirement.				
Response	Response Status W			
ACCEPT IN PRINCIPLE.				
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.				
However the change proposed in the suggested remedy is an improvement to the draft.				
Implement the suggested remedy with editorial license.				

Cl 184	SC 184.12.4.3	P620	L 12	# R1-35
Bruckman, Leon		NVIDIA		
Comment Type	TR	Comment Status	A	(B) (L)
Sec. 184.12.4.3 and Sec. 184.12.4.6 are both sub-sections titled 'Management' under Sec. 184.12.4, each independently defining items M1 and M2 with the same feature names and the same Sec. 184.11 reference. The duplicate sections make the PICS proforma ambiguous -- a reader cannot determine which Status values are normative. The M1 Status values differ between the two sections ('O' vs '!MD:M'), and the M2 Value/Comment is present in one but absent in the other.				
SuggestedRemedy				
Remove Sec. 184.12.4.3 (p. 619, lines 12-20). Retain Sec. 184.12.4.6 (p. 620) as the sole Management sub-section, correcting its M2 Value/Comment field and renaming it Sec. 184.12.4.3 to restore sequential numbering.				
Add 'Table 184-6 and Table 184-7' to the Value/Comment field of M2 in Sec. 184.12.4.6.				
Response	Response Status W			
ACCEPT IN PRINCIPLE.				
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.				
However the change proposed in the suggested remedy is an improvement to the draft.				
Implement the suggested remedy with editorial license.				

Cl 185	SC 185.3.1.4	P626	L9	# R1-20
Hajduczenia, Marek		RG Nets		
Comment Type	E	Comment Status	A	(B) (O)
In the new subclause 185.3.1.4 "PMD:IS_SIGNAL.request(SIGNAL_OK)", the body text names the wrong primitive: "The PMD:IS_SIGNAL.indication primitive is generated by the 800GBASE-LR1 Inner FEC...". It should refer to the .request primitive, consistent with the heading and the 185.3.1.4.1 Semantics section. This is leftover copy from the .indication subclause (185.3.1.3).				
SuggestedRemedy				
Change "The PMD:IS_SIGNAL.indication primitive is generated..." to "The PMD:IS_SIGNAL.request primitive is generated by the 800GBASE-LR1 Inner FEC to indicate the status of the transmit function via the SIGNAL_OK parameter."				
Response	Response Status C			
ACCEPT IN PRINCIPLE. Implement the suggested remedy with editorial license.				

Cl 185 SC 185.3.1.4 P626 L11 # R1-56

Bruckman, Leon

NVIDIA

Comment Type T Comment Status A (B) (O)

Subclause title is 'PMD:IS_SIGNAL.request(SIGNAL_OK)' but the body text reads: 'The PMD:IS_SIGNAL.indication primitive is generated by the 800GBASE-LR1 Inner FEC to indicate the status of the transmit function via the SIGNAL_OK parameter.' The primitive name in the body is wrong -- it should be PMD:IS_SIGNAL.request (a request sent from the Inner FEC down to the PMD), not PMD:IS_SIGNAL.indication (which is the receive-direction primitive defined in the preceding subclause 185.3.1.3).

SuggestedRemedy

In the first sentence of 185.3.1.4, change 'The PMD:IS_SIGNAL.indication primitive' to 'The PMD:IS_SIGNAL.request primitive'.

Response Response Status C

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

Cl 185 SC 185.5.6 P629 L16 # R1-21

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A (B) (O)

The sentence newly added to 185.5.6 PMD reset function, "PMD reset shall reset the ILT and RTS functions associated with the PMD (see 185.5.11)," terminates with a comma and is immediately followed by the next subclause heading (185.5.7), leaving an incomplete/dangling sentence. The same trailing-comma pattern recurs in the parallel clauses at p469/508/540/570 (180/181/182/183.5.12).

SuggestedRemedy

Replace the trailing comma with a period: "...associated with the PMD (see 185.5.11)."
Apply the same fix to the parallel clauses on p469, p508, p540, and p570.

Response Response Status C

ACCEPT IN PRINCIPLE.
In 180.5.6, 181.5.6, 182.5.6, 183.5.6 and 185.5.6 change:
"PMD reset shall reset the ILT and RTS functions associated with the PMD (see 18x.5.12)," to
"PMD reset shall reset the ILT and RTS functions associated with the PMD (see 18x.5.12)."
With editorial license.

Cl 185 SC 185.5.11 P629 L49 # R1-22

Hajduczenia, Marek

RG Nets

Comment Type E Comment Status A (B) (O)

New subclause heading 185.5.11 reads "Autonomous path startup functions (APSU) functions" with "functions" duplicated. All parallel clauses (178.8.9, 179.8.9, 180.5.12, 181.5.12, 182.5.12, 183.5.12, 187.5.11) use "Autonomous path startup (APSU) functions".

SuggestedRemedy

Change heading 185.5.11 (and its ToC entry on p40) to "Autonomous path startup (APSU) functions".

Response Response Status C

ACCEPT.

Cl 185 SC 185.5.11 P629 L49 # R1-57

Bruckman, Leon

NVIDIA

Comment Type E Comment Status A (B) (O)

Subclause title duplicates the word 'functions': '185.5.11 Autonomous path startup functions (APSU) functions'. All other PMD clauses (187.5.11, 180.5.12, etc.) use the consistent title 'Autonomous path startup (APSU) functions' without the leading 'functions' word.

SuggestedRemedy

Change '185.5.11 Autonomous path startup functions (APSU) functions' to '185.5.11 Autonomous path startup (APSU) functions'.

Response Response Status C

ACCEPT.

Cl 185 SC 185.5.11 P629 L51 # R1-166

Mascitto, Marco

Infinera Canada Inc., Nokia

Comment Type TR Comment Status A coherent APSU (CA)

APSU functions for this PMD are supported but through mechanisms not originally intended for this purpose. Adopting the proposal in huber_178b_01_260603 provides a cleaner and more future-proof solution for supporting the RTS function in PMDs that do not require the ILT function.

SuggestedRemedy

Adopt a separate RTS state diagram for coherent PMDs, as proposed in huber_178b_01_260603.

Response Response Status C

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-140.

Cl **185** SC **185.5.11** P**630** L**1** # **R1-174**

Slavick, Jeff Broadcom Inc

Comment Type **T** Comment Status **A** (B) (O)

All other clauses specifying APSU functions use the form of tx_mode = local_pattern when specifying the state and not "is".

SuggestedRemedy

Change "tx_mode is local_pattern" to "tx_mode = local_pattern"

Response Response Status **C**

ACCEPT.

Cl **185** SC **185.11.4.1** P**644** L**46** # **R1-255**

Law, David Hewlett Packard Enterprise

Comment Type **E** Comment Status **A** (B) (O)

In PICS item F6, the reference to the SIGNAL_DETECT parameter should be to the SIGNAL_OK parameter (see subclause 185.3.1.3.1).

SuggestedRemedy

In PICS item F6, change the text '... the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' to read 'the message PMD:IS_SIGNAL.indication (SIGNAL_OK)'.

Response Response Status **C**

ACCEPT IN PRINCIPLE.
Implement the suggested remedy with editorial license.

Cl **186** SC **186.1.3.4** P**651** L**15** # **R1-23**

Hajduczenia, Marek RG Nets

Comment Type **ER** Comment Status **A** (B) (L)

Two unresolved "(XREF)" cross-reference placeholders shipped in new D3.1 text on p651: one in the sentence mapping FEC:IS_SIGNAL.request to the MNT bits (before Table 186-1) and one in the sentence about the SIL function reading the received MNT bits (before Table 186-2).

SuggestedRemedy

Replace each "(XREF)" with the correct cross-reference to the subclause that defines the MNT field/bits.

Response Response Status **W**

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-138 and using the response to comment R1-139

Cl **186** SC **186.2.1** P**649** L**1** # **R1-163**

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type **TR** Comment Status **A** (B) (L)

Regarding Figure 186-3 receive direction, the OH/FAM processing block should have an arrow pointing to the SIL block that indicates that MNT bits are used to determine the value of the SIGNAL_OK parameter for FEC:IS_SIGNAL.indication (see Table 186-2).

SuggestedRemedy

Add the arrow.

Response Response Status **W**

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-256.

Cl **186** SC **186.2.1** P**649** L**1** # **R1-164**

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type **TR** Comment Status **A** ER1 FEC SIL (L)

The FEC:IS_SIGNAL.indication primitive has only the SIGNAL_OK parameter explicitly associated with it. Looking at Figure 186-3, the FEC deinterleave and decode block has an arrow to the SIL, indicating that it provides some data this primitive uses when it invokes its functions. What data is that? Is it related to the FEC_degraded_SER bit specified in 186.2.4.2?

SuggestedRemedy

Please clarify.

Response Response Status **C**

ACCEPT IN PRINCIPLE.
The intent of this was that if the receiver cannot align to the OFEC frame, SIGNAL_OK would be set to FAIL. But if the DSP frame is aligned, which is the responsibility of the PMA, the FEC frame is aligned, so this line is not necessary.

Update Figure 186-3 to remove the line from FEC decoding to SIL.

CI 186 SC 186.2.1 P649 L31 # R1-256

Law, David Hewlett Packard Enterprise

Comment Type TR Comment Status A (B) (L)

Subclause 186.2.2 '800GBASE-ER1 FEC sublayer service interface' says, 'The SIL function uses information received from the 800GBASE-ER1 PMA via PMA:IS_SIGNAL.indication and information from the received MNT bits (XREF) to determine the status communicated to the SIGNAL_OK value communicated via FEC:IS_SIGNAL.indication. The mapping is as shown in Table 18602.'.

Since the SIL is using the received MNT bits, which are conveyed in the OH field (see Figure 18606), and I assume extracted by the 'OH/FAM processing' function in the 'Demapping' block in Figure 18603 '800GBASE-ER1 FEC functional block diagram', doesn't there need to be a connection from the 'OH/FAM processing' box to the IS_SIGNAL.indication SIL box in Figure 18603?

This seems to be confirmed by page 6 of the contribution 'A proposal to support APSU for 800GBASE-ER1/ER1-20' available at <https://www.ieee802.org/3/dj/public/adhoc/178b/26_0414/nicholl_178b_01_260414.pdf#page=6>, which shows a red line from the 'OH/FAM processing' box to the FEC:IS_SIGNAL.indication primitive.

SuggestedRemedy

Add an additional input from the 'OH/FAM processing' box to the SIL box in the IS_SIGNAL.indication primitive path in Figure 18603 '800GBASE-ER1 FEC functional block diagram'.

Response Response Status W

ACCEPT.

CI 186 SC 186.2.2 P651 L7 # R1-137

Huber, Thomas Nokia

Comment Type E Comment Status R (Bp) (L)

It is only the UNITDATA primitives that are defined for i=0 to 31; the SIGNAL primitive is not per-lane

SuggestedRemedy

Change "The primitives are defined for i = 0 to 31." to "The UNITDATA primitives are defined for i = 0 to 31."

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.

CI 186 SC 186.2.2 P651 L15 # R1-160

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type E Comment Status A (B) (L)

I assume the XREF was not updated and should point to 186.2.3.5.11.

SuggestedRemedy

Update the XREF

Response Response Status C

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-138.

CI 186 SC 186.2.2 P651 L15 # R1-58

Bruckman, Leon NVIDIA

Comment Type T Comment Status A (B) (L)

Two unfilled cross-reference placeholders remain in the normative text: (1) p. 650 line 15: 'FEC:IS_SIGNAL.request is mapped to the MNT bits (XREF) as shown in Table 186-1.' (2) p. 650 line 34: 'information from the received MNT bits (XREF) to determine the status...' Both placeholders should reference the subclause that defines the MNT bit mapping, which is 186.2.3.5.11.

SuggestedRemedy

Replace both '(XREF)' placeholders with '(see 186.2.3.5.11)'.

Response Response Status C

ACCEPT IN PRINCIPLE.
Resolved using the response to comment R1-138 (for xref on line 15) and using the response to comment R1-139 (for the other xref on line 34).

CI 186 SC 186.2.2 P651 L15 # R1-138

Huber, Thomas Nokia

Comment Type ER Comment Status A (B) (L)

Missing crossreference 'XREF'

SuggestedRemedy

This should be pointing to 186.2.3.5.11; add the cross-reference

Response Response Status W

ACCEPT.

CI 186 SC 186.2.2 P651 L33 # R1-162

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type ER Comment Status A (B) (L)

This sentence is difficult to read.

SuggestedRemedy

Replace:

The SIL function uses information received from the 800GBASE-ER1 PMA via PMA:IS_SIGNAL.indication and information from the received MNT bits (XREF) to determine the status communicated to the SIGNAL_OK value communicated via FEC:IS_SIGNAL.indication.

With:

The SIL function uses information received from the 800GBASE-ER1 PMA via PMA:IS_SIGNAL.indication and information from the received MNT bits (XREF) to determine the SIGNAL_OK value that is communicated via FEC:IS_SIGNAL.indication.

Response Response Status W

ACCEPT.

CI 186 SC 186.2.2 P651 L34 # R1-139

Huber, Thomas Nokia

Comment Type ER Comment Status A (B) (L)

Missing crossreference 'XREF'

SuggestedRemedy

This should be pointing to 186.2.4.7.6; add the cross-reference

Response Response Status W

ACCEPT.

CI 186 SC 186.2.2 P651 L34 # R1-161

Mascitto, Marco Infinera Canada Inc.,Nokia

Comment Type E Comment Status A (B) (L)

I assume the XREF was not updated and should point to 186.2.3.5.11.

SuggestedRemedy

Update the XREF

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolved using the response to comment R1-139.

CI 186 SC 186.2.2 P652 L1 # R1-68

Bruckman, Leon NVIDIA

Comment Type TR Comment Status A ER1 payload (L)

It is not clear what is the payload carried in the GMP frame when the MNT field is not OK and what will be the value of the 257-bit data blocks extracted at the receiver from the GMP payload in this case. Section 186.2.4.7.3 specifies the payload for the case of: "When any of the GID, IID, MAP, PT, or MSI values do not match the expected value, the payload area of the frame is replaced with local fault ordered sets." and section 186.2.4.8 specifies that: "If the payload type or multiplex structure identification overhead (see 186.2.4.7.3) did not match the expected value, the 257-bit data blocks extracted from the GMP frame are replaced with LF ordered sets."

SuggestedRemedy

Specify that in case of MNT # OK the GMP payload carries local faults, and the 257-bit blocks recovered are replaced with link fault ordered set.

Response Response Status C

ACCEPT IN PRINCIPLE.

The comment says it is not clear what is in the payload when the MNT bits are indicating a value other than OK (i.e., are indicating READY, IN_PROGRESS, or FAIL) and suggests insertion of LF in that situation based on how other OH mismatches are handled. However, those other OH mismatches are indicative of either the transmitter in the peer being implemented incorrectly, or possibly that the interface is connected to an OIF 800ZR Interface rather than another 800GBASE-ER1 PMD; in those conditions, replacement with LF is appropriate. In contrast, the MNT bits not being OK doesn't indicate a problem with the GMP frame itself; those bits are carrying the RTS status for the path. As such, it is not appropriate to insert LF in all of those conditions. When SIGNAL_OK is IN_PROGRESS or READY, a pattern should be mapped (Idle or a test pattern) instead.

It is however necessary to specify what gets mapped into the ER1 frame during the APSU process. When MNT is indicating FAIL, LF should be mapped; when MNT indicates IN_PROGRESS or READY, a scrambled Idle pattern is mapped; when MNT indicates OK, the data from the AUI is mapped.

Add a column to Table 186-1 to indicate this and/or add the appropriate text to the Flow Merging block of the Inverse RS-FEC function or the GMP mapping function. Update other subclauses as needed.

Implement with editorial license.

CI 186 SC 186.2.2 P652 L1 # R1-24

Hajduczenia, Marek RG Nets

Comment Type E Comment Status A (B) (L)

In new D3.1 text on p652, the primitive name is written "FEC:IS_SIGNALindication(SIGNAL_OK)", missing the period between SIGNAL and indication. The correct primitive name (used 165 times elsewhere in D3.1) is FEC:IS_SIGNAL.indication.

SuggestedRemedy

Change "FEC:IS_SIGNALindication(SIGNAL_OK)" to "FEC:IS_SIGNAL.indication(SIGNAL_OK)".

Response Response Status C

ACCEPT IN PRINCIPLE.
[Editor's note: changed subclause to 186.2.2]
Implement the suggested remedy.

CI 186 SC 186.2.3.2 P652 L51 # R1-82

He, Xiang Huawei Technologies Co., Ltd

Comment Type T Comment Status A (B) (L)

"The stream of 257-bit blocks shall be distributed to 8 FEC flows in a round-robin manner" is not clear.
1. It only stated there are 8 FEC flows but not the numbering of them.
2. It should clearly state where the 257-bit blocks from flow 0 and flow 1 go to, in order to match the statement on the Rx side (186.2.4.10.1)

SuggestedRemedy

Change:
"The stream of 257-bit blocks shall be distributed to 8 FEC flows in a round-robin manner" to:
"The stream of 257-bit blocks shall be distributed to 8 FEC flows (0 through 7) in a round-robin manner, with 257-bit blocks from flow 0 distributed to even numbered FEC flows and 257-bit blocks from flow 1 distributed to odd numbered FEC flows."

Response Response Status C

ACCEPT IN PRINCIPLE.
This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot. However, the proposed change improves the clarity of the draft.

Implement the suggested remedy with editorial license.

CI 186 SC 186.2.3.5 P656 L22 # R1-257

Law, David Hewlett Packard Enterprise

Comment Type E Comment Status A (B) (L)

In Figure 18606 'Contents of the OH field in eight frame multi-frame', shouldn't the LSB of the CSTAT field be bit 0, not bit 5?

SuggestedRemedy

Change '5' above the LSB of the CSTAT field in Figure 18606 to read '0'.

Response Response Status C

ACCEPT.

CI 186 SC 186.2.3.5.11 P660 L1 # R1-59

Bruckman, Leon NVIDIA

Comment Type TR Comment Status A (B) (L)

Off-by-one error in CSTAT bit range: the text states 'Bits 5-7 are used to convey the information encoded in am_sf<2:0>' but the code table immediately below assigns CSTAT<6>, CSTAT<7>, and CSTAT<8> to am_sf<0>, am_sf<2>, and am_sf<1> respectively. If CSTAT uses 1-based indexing (bits 1-8), then 'bits 5-7' corresponds to CSTAT<5>, <6>, <7> -- not <6>, <7>, <8>. The same error appears in 186.2.4.7.6.

SuggestedRemedy

In both 186.2.3.5.11 and 186.2.4.7.6, change CSTAT<6>, <7>, <8> to: CSTAT<5>, <6>, <7>.

Response Response Status W

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the comment identifies an error in the text that should be corrected.

Figure 186-6 is 0-based, so align the text in the bullet list with that. Change the last 3 bullets to read:

- ù The resulting am_sf<0> signal is placed in CSTAT<5>
- ù The resulting am_sf<1> signal is logically ORed with IFEC_degraded_SER and placed in CSTAT<7>
- ù The resulting am_sf<2> signal is placed in CSTAT<6>.

CI 186 SC 186.2.3.5.11 P660 L10 # R1-258

Law, David Hewlett Packard Enterprise

Comment Type T Comment Status A ER1 MNT bits (L)

Subclause 186.2.2 '800GBASE-ER1 FEC sublayer service interface' says 'The signal status received via FEC:IS_SIGNAL.request is mapped to the MNT bits (XREF) as shown in Table 186.01.' and 'The SIL function uses ... from the received MNT bits (XREF) to determine the status communicated ...'.

Further, while the MNT bits are illustrated as part of the CSTAT field in Figure 186.06 'Contents of the OH field in eight frame multi-frame', this is no reference to the 'MNT bits' in subclause 186.2.3.5.11 'Client status (CSTAT)' which just says, 'Bits 1-3 are used to convey the SIGNAL_OK status as described in Table 186.01.'.

Suggested Remedy

Assuming that MNT means Maintenance:

[1] Change the two instances of '... MNT bits (XREF) ...' is subclause 186.2.2 to read '... MNT bits (186.2.3.5.11) ...'

[2] Change the text 'Bits 1-3 are used to convey the SIGNAL_OK status as described in Table 186.01.' to read 'Bits 1-3 are the Maintenance (MNT) bits and are used to convey the SIGNAL_OK status as described in Table 186.01.'.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve Item [1] of the suggested remedy using the responses to comment R1-138 and comment R1-139.

In 186.2.3.5.11, page 660, line 10:

Change the text:

"Bits 1-3 are used to convey the SIGNAL_OK status as described in Table 186-1."

To:

"Bits 1-3 are the Maintenance (MNT) bits and are used to convey the SIGNAL_OK status as described in Table 186-1 across the 800GBASE-ER1 or 800GBASE-ER1-20 link."

CI 186 SC 186.2.4.10.1 P666 L48 # R1-81

He, Xiang Huawei Technologies Co., Ltd

Comment Type T Comment Status A ER1 FEC flows (L)

The requirement δThe first 257-bit block of each 800GBASE-ER1 tributary multi-frame is distributed to flow 0δ is incorrect.

For the eight 800GBASE-ER1 FEC flows, each FEC flow has its own frame and multi-frame. With the round-robin distribution on Tx side (186.2.3.2), blocks from PCS flow 0 always go to FEC flow <0,2,4,6> (assuming we are numbering the eight FEC flows as 0 to 7 - this is also a problem that we only stated there are 8 FEC flows but not the numbering/index of them), and blocks from PCS flow 1 always go to FEC flow <1,3,5,7>. It is a valid requirement to align the first block from each multi-frame to the correct PCS flow. We should use more precise description on the Rx side.

Suggested Remedy

Change

δThe first 257-bit block of each 800GBASE-ER1 tributary multi-frame is distributed to flow 0δ

to:

δThe first 257-bit block of each 800GBASE-ER1 tributary multi-frame from even FEC flow is distributed to flow 0, and the first 257-bit block of each 800GBASE-ER1 tributary multi-frame from odd FEC flows is distributed to flow 1 δ.

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3dj D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, the proposed change would improve the clarity of the text.

Per 186.2.4.9, the 8 FEC flows are interleaved to form a single stream of blocks, which is then distributed to two flows per the rules in 186.2.4.10. That text should be clarified. In 186.2.4.10.1, the text should be revised to say that the first blocks from instances 0,2,4,6 go to flow 0, and blocks from instances 1,3,5,7 go to flow 1.

In 186.2.4.9, change:

"The 257-bit blocks from each of the eight FEC flows are interleaved to form a single stream of 257-bit blocks."

To:

"The 257-bit blocks from each of the eight FEC flows are interleaved, starting with FEC flow 0 and ending with FEC flow 7, to form a single stream of 257-bit blocks."

In 186.2.4.10.1, change:

"The stream of 257-bit blocks is distributed to two flows on a round-robin basis. The first 257-bit block of each 800GBASE-ER1 tributary multi-frame is distributed to flow 0."

To:

"The stream of 257-bit blocks is distributed to two flows in a round-robin manner, with blocks from FEC flow 0, 2, 4, and 6 distributed to flow 0, and blocks from FEC flow 1, 3, 5, and 7

distributed to flow 1."

Implement with editorial license.

Cl 186 SC 186.6.1 P690 L21 # R1-25
Hajduczenia, Marek RG Nets
Comment Type E Comment Status A (B) (L)
PHY name typo in the rewritten path-delay paragraph: "800BASE-ER1 FEC sublayer" is missing the 'G'; the correct name "800GBASE-ER1" is used consistently elsewhere in the same paragraph on p690.

SuggestedRemedy

Change "800BASE-ER1 FEC sublayer" to "800GBASE-ER1 FEC sublayer".

Response Response Status C
ACCEPT.

Cl 186 SC 186.7.2 P693 L26 # R1-26
Hajduczenia, Marek RG Nets
Comment Type TR Comment Status A (B) (L)
The newly added IFEC_hi_ser row in Table 186-10 has an empty Variable reference (subclause) cell, unlike all adjacent rows which cite 186.2.3.1.3 or 186.2.3.5.2. The variable IFEC_hi_ser is not defined or referenced anywhere in Clause 186. A new Editor's note on the same page acknowledges this gap (added per comment I-11, no reference in clause). This unresolved open item/placeholder should not remain in a recirculation draft.

SuggestedRemedy

Resolve before publication: either add a Clause 186 definition/usage of IFEC_hi_ser and populate its subclause reference in Table 186-10, or remove the IFEC_hi_ser row; then delete the Editor's note.

Response Response Status W
ACCEPT IN PRINCIPLE.

Clause 186.2.3.1.3 points back to 172.2.5.3, which explains that hi_ser for each flow are mapped to separate variables that are later ORed into a hi_ser_combined variable and mapped to the MDIO register. All the other IFEC-related variables refer to 186.2.3.1.3, so that can be done with this one also.
Update Table 186-10 to indicate 186.2.3.1.3 as the reference for IFEC_hi_ser.
[Editors note: changed clause/subclause from 0/0 to 186/186.7.2]

Cl 186A SC 186A.3 P963 L25 # R1-34
Hajduczenia, Marek RG Nets
Comment Type E Comment Status A (B) (L)
Missing word "in" in newly inserted test-vector sentence: "for the 800GBASE-ER1 FEC sublayer as shown Figure 186A-1." The parallel VP4 sentence on p966 correctly reads "as shown in Figure 186A-2."

SuggestedRemedy

Change "as shown Figure 186A-1" to "as shown in Figure 186A-1".

Response Response Status C
ACCEPT.

Cl 187 SC 187.3.1 P703 L17 # R1-60
Bruckman, Leon NVIDIA
Comment Type T Comment Status A (B) (O)
Three instances of incorrect primitive names in Clause 187.3.1 service interface semantics: (1) 187.3.1.1.2 'When generated' (p. 703 I.17): 'The 800GBASE-ER1 PMA generates PMA_UNITDATA.request continuously.' (2) 187.3.1.1.3 'Effect of receipt' (p. 702 I.21): 'The effect of receipt of the PMA_UNITDATA.request by the PMD...' (3) 187.3.1.2.2 'When generated' (p. 702 I.38): 'The PMD generates PMA_UNITDATA.indication continuously.' The correct primitive name at the PMD service interface is 'PMD:IS_UNITDATA.request/.indication', not 'PMA_UNITDATA.request/.indication'.

SuggestedRemedy

(1) Change 'PMA generates PMA_UNITDATA.request' to 'PMA generates PMD:IS_UNITDATA.request'. (2) Change 'receipt of the PMA_UNITDATA.request' to 'receipt of the PMD:IS_UNITDATA.request'. (3) Change 'PMD generates PMA_UNITDATA.indication' to 'PMD generates PMD:IS_UNITDATA.indication'.

Response Response Status C
ACCEPT IN PRINCIPLE.

Make the following changes.
On page 703 line 11 change "PMD:UNITDATA.request" to "PMD:IS_UNITDATA.request"
On page 703 line 17 change "PMA_UNITDATA" to "PMD:IS_UNITDATA.request"
On page 703 line 21 change "PMA_UNITDATA" to "PMD:IS_UNITDATA.request"
On page 703 line 32 change "PMD:UNITDATA.indication" to "PMD:IS_UNITDATA.indication"
On page 703 line 38 change "PMA_UNITDATA" to "PMD:IS_UNITDATA.indication"
Correct any other similar errors in the draft.
Implement with editorial license.

Cl 187	SC 187.5.11	P707	L 50	# R1-165
Mascitto, Marco		Infinera Canada Inc.,Nokia		
Comment Type	TR	Comment Status	A	<i>coherent APSU (CA)</i>
APSU functions for this PMD are supported by Cl186 but through mechanisms not originally intended for this purpose. Adopting the proposal in huber_178b_01_260603 provides a cleaner and more future-proof solution for supporting the RTS function in PMDs that do not require the ILT function.				
<i>SuggestedRemedy</i>				
Adopt a separate RTS state diagram for coherent PMDs, as proposed in huber_178b_01_260603.				
Response	Response Status C			
ACCEPT IN PRINCIPLE. Resolved using the response to comment R1-140.				

Cl 187	SC 187.5.11	P707	L 50	# R1-175
Slavick, Jeff		Broadcom Inc		
Comment Type	TR	Comment Status	A	<i>(B) (CA)</i>
800G-ER1 is not really using the APSU functions. And it's better to point to the service interface where the mapping of signal_ok is done.				
<i>SuggestedRemedy</i>				
Change "APSU functions are supported by 800GBASE-ER1 FEC sublayer (see 186.2)." To: "The 800GBASE-ER1 PMD supports APSU via the 800GBASE-ER1 FEC sublayer (see 186.2.2).				
Response	Response Status W			
ACCEPT IN PRINCIPLE. Implement the suggested remedy with editorial license.				

Cl 187	SC 187.11.4.1	P720	L 46	# R1-259
Law, David		Hewlett Packard Enterprise		
Comment Type	E	Comment Status	A	<i>(B) (O)</i>
In PICS item F6, the reference to the SIGNAL_DETECT parameter should be to the SIGNAL_OK parameter (see subclause 185.3.1.3.1).				
<i>SuggestedRemedy</i>				
In PICS item F6, change the text '... the message PMD:IS_SIGNAL.indication (SIGNAL_DETECT)' to read 'the message PMD:IS_SIGNAL.indication (SIGNAL_OK)'.				
Response	Response Status C			
ACCEPT IN PRINCIPLE. Implement the suggested remedy with editorial license.				