

802.3dj D1.0 Comment Resolution Logic Track

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Introduction

- This slide package was assembled by the 802.3dj editorial team to provide background and detailed resolutions to aid in comment resolution.

Clause 175

Clause 177, Inner FEC Sync Comment # 505

Cl 177	SC 177.6	P 262	L 5	# 505
Ren, Hao		Huawei		
<i>Comment Type</i>	TR	<i>Comment Status</i>	D	<i>Inner FEC Sync</i>
In Figure 177—8, the input variable of state FS_LOCK_INIT is not correct. It would cause a FS lock error.				
<i>Suggested Remedy</i>				
FS_LOCK_INIT state should be entered after all the 8 flows obtain their inner FEC codeword boundaries and inner FEC flow 0 is identified, when fs_lock is false.				
Propose change: Change the input variable from '!all_synced' to 'all_synced * !fs_lock'.				
Change the definition of all_synced from 'A Boolean variable that is set to true when sync_flow<x> is true for all eight flows and is set to false when sync_flow<x> is false for any x.' to 'A Boolean variable that is set to true when inner FEC flow 0 is identified and is set to false when sync_flow<x> is false for any x.' (in page 258 line 48-50)				
<i>Proposed Response</i>		<i>Response Status</i>	W	
PROPOSED ACCEPT IN PRINCIPLE.				
Change the condition for FS_LOCK_INIT state from "!all_synced" to "all_synced*!fs_lock"				
Change the definition of variable "all_synced" from: "A Boolean variable that is set to true when sync_flow<x> is true for all eight flows and is set to false when sync_flow<x> is false for any x." to: " A Boolean variable that is set to true when sync_flow<x> is true for all eight flows AND inner FEC flow 0 is identified, and is set to false when sync_flow<x> is false for any x."				

Clause 177, Inner FEC Sync

Comment # 505

Change the definition of variable “all_synced” in 177.6.2.1 as follows:

all_synced

A Boolean variable that is set to true when sync_flow<x> is true for all eight flows AND inner FEC flow 0 is identified, and is set to false when sync_flow<x> is false for any x.

Change Figure 177-8 as follows:

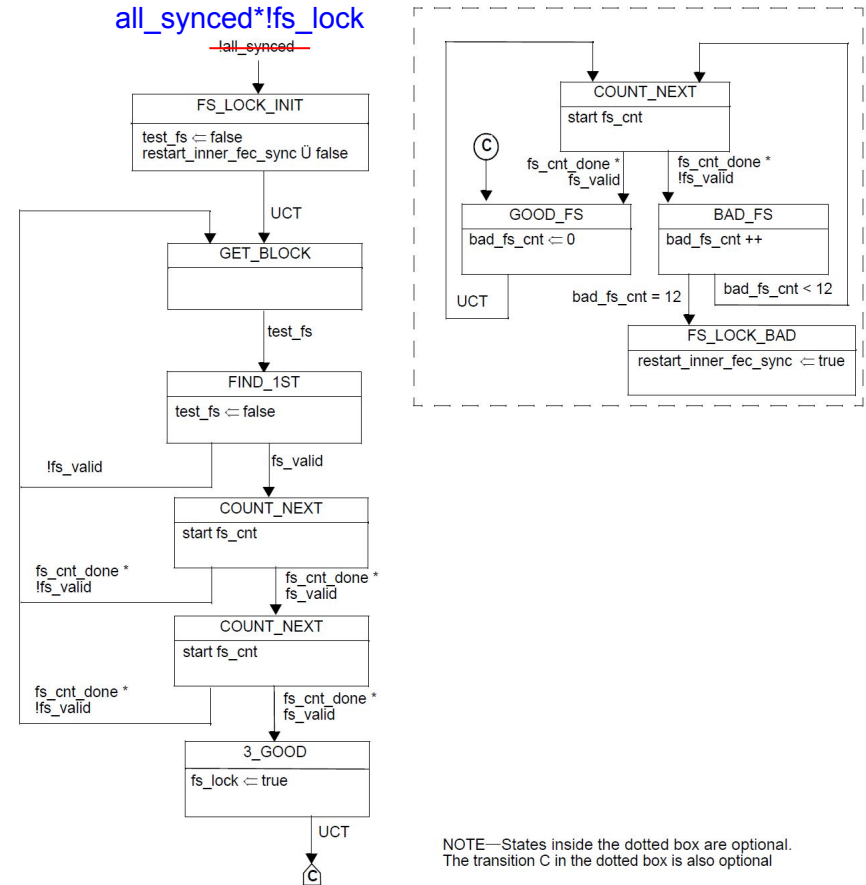


Figure 177–8—Inner FEC pad detection state diagram