

**P802.3dj D1.3**  
**Comment Resolution Agenda**  
**Thursday 23 January 2025**

Matt Brown, Alphawave, P802.3dj Editor-in-Chief  
Kent Lusted, Synopsys, Electrical Track Chair

# Thursday Plan #1

Topic	Clause/Annex	Comments
AN/ILT: time-out [TBD]	73, 178B	131, 184, 543, 545, <del>slaviek_01</del> , 544
R_peak [TBD]	179, 176D	[303, 185, <del>ran_02</del> ], [350, 351, <del>ran_02</del> ], 206, 207
Test fixtures ICN	179B	52, 53, 54, 55, 56, 522, 523, 524
Error ratio budget	174A	151
Partial channel model	179, 176D, 178A	[393, 466, kocsis_01], [391, 392], 389, 390, [387, 388]
KER	174A	150, <del>he_3dj_01</del>
KER for PHY	174A	376
KER stress	174A	167, brown_03
KER, all-lanes	174A	[384, 166]
KER, per-lane	174A	180
Bucket pulls	174A	164, 430
Annex 176B	176B	424
ILT: Management variables	178B	170
PHY TX KER	174A, 178-183	8, brown_04

Note that comment resolution order may be readjusted.

Cyan highlight: pulled from bucket #1

# Thursday Plan #2

Topic	Clause/Annex	Comments
ILT: TX Presets	178B	512
Error ratio budget	174A	435
KER, per-lane	174A	[107, mi_01], 183
ILT: State diagram	178B	356
Jitter tolerance	176C, 176D	253, 260,
Test fixtures COM (1)	179B	[48 mellitz_01]
frequency range	178A, 179B	[47 mellitz_02], 535, 529, 526
Introduction	174	155
References	181, 183	263, 264
Bucket pulls	179A, 179B, 174, 174A, 120B	266, 267, 525, 526, 527, 530, 431
AN DME swing	73	547
<p>Note that comment resolution order may be readjusted.  Cyan highlight: pulled from bucket #1</p>		