

# IEEE P802.3dj Joint Tracks Ad Hoc Report

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# Report

- Since Nov 2024:
  - 1 joint optics/logic ad hoc call (Dec 19<sup>th</sup>)
  - 1 joint optics/logic/electrical (Jan 9<sup>th</sup>)
  - 50+ attendee
  - 10 contributions in total
- Minutes for these past ad hocs:
  - [Dec 19th](#) & [Jan 9th](#)
- Next meetings will occur between Jan and March TF meetings

# Dec 19<sup>th</sup> Joint optics/logic Ad hoc

## Dec 19<sup>th</sup>

- “How to derive the receiver sensitivity from waterfall curves for 802.3dj applications” presented by Peter Stassar, Huawei
- “Improving TDECQ Test Definition”, presented by Ali Ghiasi, Ghiasi Quantum/Marvell
- “800GBASE-LR1 Permutation function Figure 184-3 update”, presented by Leon Bruckman, Nvidia
- “800G-ER1 AML field specification improvements”, presented by Jeff Slavick, Broadcom

# Jan 9<sup>th</sup> Joint optics/logic/electrical Ad hoc

## Jan 9<sup>th</sup>

- “Open TBDs in D1.3”, presented by Kent Lusted, Synopsys
- “Block error ratio measurements for PMD receive lane measurements” presented by Richard Barrie, Alphawave Semiconductor
- “Proposed update of Coefficient initial conditions (Table 179-8 and Table 176D-8)” presented by Bill Simms, Nvidia
- “Proposed update of electrical parameters of DME”, presented by Bill Simms, Nvidia
- “Rethinking the 800GBASE-ER1 model”, presented by Tom Huber, Nokia
- “Improvements to 12 Edge Jitter operations,  $JH_{rms}$  and  $J4_u$  with attention to making these measurements less dependent on channel induced amplification”, presented by David Gines, Keysight

**THANKS!**