

**IEEE 802.3 September 2025 Interim
IEEE P802.3dj Task Force**

IEEE P802.3dj Annex 178B Ad Hoc Report

**John D'Ambrosia, Futurewei, U.S. Subsidiary of Huawei
Annex 178B Ad Hoc Chair**

■ Two ad hoc calls since July 2025 Plenary

– 27 Aug 2025

- Webpage: https://www.ieee802.org/3/dj/public/adhoc/178b/25_0827/index.html
- “High-Level Concepts & Terminology”, John D’Ambrosia

– 10 Sept 2025

- Webpage : https://www.ieee802.org/3/dj/public/adhoc/178b/25_0910/index.html
- “Annex 178B link timer proposal”, Kent Lusted
 - Strawpolls 1 – 3 explored ad hoc interest in max_wait_timer_duration value of 1) KR/CR PMDs; 2) for the AUIs; and 3) for the optical PMDs (see slide 6, lusted_178b_01_250910).
- “IEEE P802.3dj Scope and Annex 178B”, John D’Ambrosia
 - Straw Poll #4 - I support leaving the existing IEEE P802.3dj PAR as is
 - Path Startup Process is only specified for 200 Gb/s per lane AUIs
i.e. - no additional features to <= 100 Gb/s per lane AUIs, including for 1.6TAUI-16
 - Results Y:32 N: 0 A: 4

■ Future meetings to be announced