Considerations for Removal of Editor's Notes related to SFP224 and SFP-DD224

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Related to Clause 179, Annex(s) 179B, 179C, 179D

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Background

- 802.3dj D3P0 has MDI connectors SFP224 and SFP-DD224, along with their normative specifications that do not exist yet as real, citable documents
- The 802.3dj TF submitted liaison letters in July 2025 to the relevant standards organizations to provide a summary of the situation
  - SNIA / SFF (Letter related to SFP224)
  - SFP-DD MSA (Letter related to SFP-DD224)
- As of March 2, 2026 there has been no response to either liaison
  - No contribution of status or copy of the required reference specification for either SFP224 or SFP-DD224

Further Context

- The term “SFP224” appears (32) times in the D3P0 document
  - Clause 179, Annex(s) 179B, 179C, 179D
- The term “SFP-DD224” appears (31) times in the D3P0 document
  - Clause 179, Annex(s) 179B, 179C, 179D
- The specification and reference to either of these MDI connector types is not a requirement for 802.3dj to meet PAR objectives
- We need consider the options to remove the editorial notes and complete the 802.3dj specification absent of these references

Further Context

- The reference to SFP224 offers a convenient example of a host with a 200GBASE-CR1 physical layer and/or breakout configurations
- The reference to SFP-DD224 offers a convenient example of a host with a 200GBASE-CR2 physical layer and/or breakout configurations

For 200GBASE-CR1, the mechanical interface between the PMD and the cable assembly is a mated pair of connectors meeting the requirements of 179C.2.1 (SFP224), 179C.2.2 (SFP-DD224), 179C.2.3 (QSFP224), 179C.2.4 (QSFP-DD1600), or 179C.2.4 (OSFP1600).

For 400GBASE-CR2, the mechanical interface between the PMD and the cable assembly is a mated pair of connectors meeting the requirements of 179C.2.2 (SFP-DD224), 179C.2.3 (QSFP224), 179C.2.4 (QSFP-DD1600), or 179C.2.5 (OSFP1600).

For 800GBASE-CR4, the mechanical interface between the PMD and the cable assembly is a mated pair of connectors meeting the requirements of 179C.2.3 (QSFP224), 179C.2.4 (QSFP-DD1600), or 179C.2.5 (OSFP1600).

For 1.6TBASE-CR8, the mechanical interface between the PMD and the cable assembly is a mated pair of connectors meeting the requirements of 179C.2.4 (QSFP-DD1600) or 179C.2.5 (OSFP1600).

The MDI connector electrical performance is consistent with the signal quality and electrical requirements of 179.9 and 179.11.

Proposed Resolution(s)

In D3P0, 179C.1

Current → *Proposed*

Table 179C–3—MDI connector contact mapping for SFP224, SFD-DD224

SFP224	SFP-DD224	Connector signal name	Description
11	11	GND	Ground
12	12	DL0n	Receiver inverted data output
13	13	DL0p	Receiver non-inverted data output
14	14	GND	Ground
17	17	GND	Ground
18	18	SL0p	Transmitter non-inverted data input
19	19	SL0n	Transmitter inverted data input
20	20	GND	Ground
—	31	GND	Ground
—	32	DL1n	Receiver inverted data output
—	33	DL1p	Receiver non-inverted data output
—	34	GND	Ground
—	37	GND	Ground
—	38	SL1p	Transmitter non-inverted data input
—	39	SL1n	Transmitter inverted data input
—	40	GND	Ground

REMOVED

Proposed Resolution(s)

In D3P0, 179C.2.1

Current → *Proposed*

179C.2 MDI connector types

179C.2.1 SFP224

For 200GBASE-CR1, the mechanical interface between the PMD and the cable assembly may be a mated pair of connectors meeting the requirements of SFF-TA-1031, Rev 1.0. SFP224 supports one lane.

Editor's note: When this draft was published the current version of reference SFF-TA-1031 (Rev 1.0) included no specifications for SFP224. If this reference, with specifications for SFP224, is not available for review by the P802.3dj Task Force prior to the January 2026 IEEE 802.3 interim meeting then the reference will be deleted and related MDI specifications will be deleted or appropriately modified (proposal required).

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The SFP224 cable assembly connector is the plug illustrated in Figure 179C–1. The SFP224 PMD connector is the receptacle with the mechanical mating interface as illustrated in Figure 179C–2. The connectors have data signal and signal ground contact assignments as specified in Table 179C–3. The pin definition for both the plug and receptacle remains unchanged from Annex 162C.

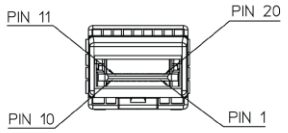

Figure 179C–1—SFP224 cable assembly plug

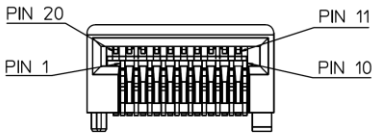

Figure 179C–2—SFP224 PMD receptacle

Proposed Resolution(s)

In D3P0, 179C.2.2

Current → *Proposed*

179C.2.2 SFP-DD224

For 200GBASE-CR1 or 400GBASE-CR2, the mechanical interface between the PMD and the cable assembly may be a mated pair of connectors as defined in the SFP-DD MSA. SFP-DD224 supports up to two lanes.

Editor's note: When this draft was published the SFP-DD MSA had not defined specifications for SFP-DD224. If the SFP-DD MSA specifications for SFP-DD224 are not available for review by the P802.3dj Task Force prior to the January 2026 IEEE 802.3 interim meeting then the reference will be deleted and related MDI specifications will be deleted or appropriately modified (proposal required).

The SFP-DD224 cable assembly connector is the plug illustrated in Figure 179C–3. The SFP-DD224 PMD connector is the receptacle with the mechanical mating interface as illustrated in Figure 179C–4. The connectors have data signal and signal ground contact assignments as specified in Table 179C–3. The pin definition for both the plug and receptacle remains unchanged from Annex 162C.

Figure 179C–3—SFP-DD224 cable assembly plug

Figure 179C–4—SFP-DD224 PMD receptacle

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Proposed Resolution(s)

In D3P0, 179.11.6.2.2

Current → *Proposed*

Table 179–22—Number of crosstalk paths used in calculation of COM

Victim (one end)	NEXT	FEXT (other end)			
		SFP224	SFP-DD224	QSFP224	QSFP-DD1600 or OSFP1600
SFP224	1	0	1	3	7
SFP-DD224	2	1	1	3	7
QSFP224	4	3	3	3	7
QSFP-DD1600 or OSFP1600	8	7	7	7	7

Table 179–22—Number of crosstalk paths used in calculation of COM

Victim (one end)	NEXT	FEXT (other end)	
		QSFP224	QSFP-DD1600 or OSFP1600
QSFP224	4	3	7
QSFP-DD1600 or OSFP1600	8	7	7

References would update, as necessary

Proposed Resolution(s)

In D3P0, 179C.1

Current → *Proposed*

Table 179C–1—Number of PMDs supportable for each connector type

MDI types	200GBASE-CR1	400GBASE-CR2	800GBASE-CR4	1.6TBASE-CR8	Reference
SFP224	1	—	—	—	179C.2.1
SFP-DD224	1,2	1	—	—	179C.2.2
QSFP224	1, 2, 4	1, 2	1	—	179C.2.3
QSFP-DD1600	1, 2, 4, 8	1, 2, 4	1, 2	1	179C.2.4
OSFP1600	1, 2, 4, 8	1, 2, 4	1, 2	1	179C.2.5

Table 179C–1—Number of PMDs supportable for each connector type

MDI types	200GBASE-CR1	400GBASE-CR2	800GBASE-CR4	1.6TBASE-CR8	Reference
QSFP224	1, 2, 4	1, 2	1	—	179C.2.3
QSFP-DD1600	1, 2, 4, 8	1, 2, 4	1, 2	1	179C.2.4
OSFP1600	1, 2, 4, 8	1, 2, 4	1, 2	1	179C.2.5

References would update, as necessary

Proposed Resolution(s)

In D3P0, 179D.1

Current → *Proposed*

Table 179D–1—Host receptacles and cable assembly plugs

Receptacle/Plugs	Reference
SFP224	179C.2.1
SFP-DD224	179C.2.2
QSFP224	179C.2.3
QSFP-DD1600	179C.2.4
OSFP1600	179C.2.5

Table 179D–1—Host receptacles and cable assembly plugs

Receptacle/Plugs	Reference
QSFP224	179C.2.3
QSFP-DD1600	179C.2.4
OSFP1600	179C.2.5

References would update, as necessary

Proposed Resolution(s)

In D3P0, 179D.1.1

Current → *Proposed*

Table 179D–2—200GBASE-CR1 cable assembly types and supportable number of PMDs

One end		Other end(s)		Supportable PMDs
Receptacle/Plug	Number	Receptacle/Plug	Number	Number
SFP224	1	SFP224	1	1
SFP-DD224	1	SFP224	2	2
QSFP224	1	SFP224	4	4
QSFP-DD1600	1	SFP224	8	8

Table 179D–2—200GBASE-CR1 cable assembly types and supportable number of PMDs *(continued)*

One end		Other end(s)		Supportable PMDs
Receptacle/Plug	Number	Receptacle/Plug	Number	Number
OSFP1600	1	SFP224	8	8
SFP-DD224	1	SFP-DD224	1	2
QSFP224	1	QSFP224	1	4
QSFP-DD1600	1	QSFP-DD1600	1	8
QSFP-DD1600	1	OSFP1600	1	8
OSFP1600	1	OSFP1600	1	8

Table 179D–2—200GBASE-CR1 cable assembly types and supportable number of PMDs

One end		Other end(s)		Supportable PMDs
Receptacle/Plug	Number	Receptacle/Plug	Number	Number
QSFP224	1	QSFP224	1	4
QSFP-DD1600	1	QSFP-DD1600	1	8
QSFP-DD1600	1	OSFP1600	1	8
OSFP1600	1	OSFP1600	1	8

Proposed Resolution(s)

In D3P0, 179B.4.6

Current → *Proposed*

The SFP224 mated test fixtures integrated near-end crosstalk noise voltage for the disturber near-end crosstalk loss is determined according to the method in 110B.1.3.6, given the disturber near-end crosstalk loss *NEXT_loss*(*f*). The mated test fixtures integrated near-end crosstalk noise voltage shall meet the specification in Table 179B–3.

Table 179B–3—SFP224 mated test fixtures integrated near-end crosstalk noise voltage

Parameter	Value	Units
Integrated near-end crosstalk noise voltage (max)	1.6	mV

REMOVED

Summary

- 802.3dj D3P0 has MDI connectors SFP224 and SFP-DD224, along with their normative specifications that do not exist yet as real, citable documents
- As the TF has communicated in the liaison letters to other in the industry, we need consider the options to remove the editorial notes and complete the 802.3dj specification absent of references to SFP224 and SFP-DD224
- The proposed resolution on slides 6-13 illustrates what the specific changes would look like given the information we have today