

# Proposed Figure 178B-9 Optimization

Addresses comment #206 against D3.0

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# Description

- Further to [slavick\\_3dj\\_03\\_2603](#), presented at the IEEE 802 February 2026 Plenary, an additional optimization is proposed here.
- “Empty” states (other than IDLE, WAIT, FAIL, END, etc.,) may cause the reader to wonder if there are variables set or functions called within the state that are not explicitly shown.
- The CLOCK\_MODE state does not update variables, nor call functions, so it can be removed.

# Proposed update to Figure 178B-9

