

# TDD Baseline Proposal for 802.3dm (updated)

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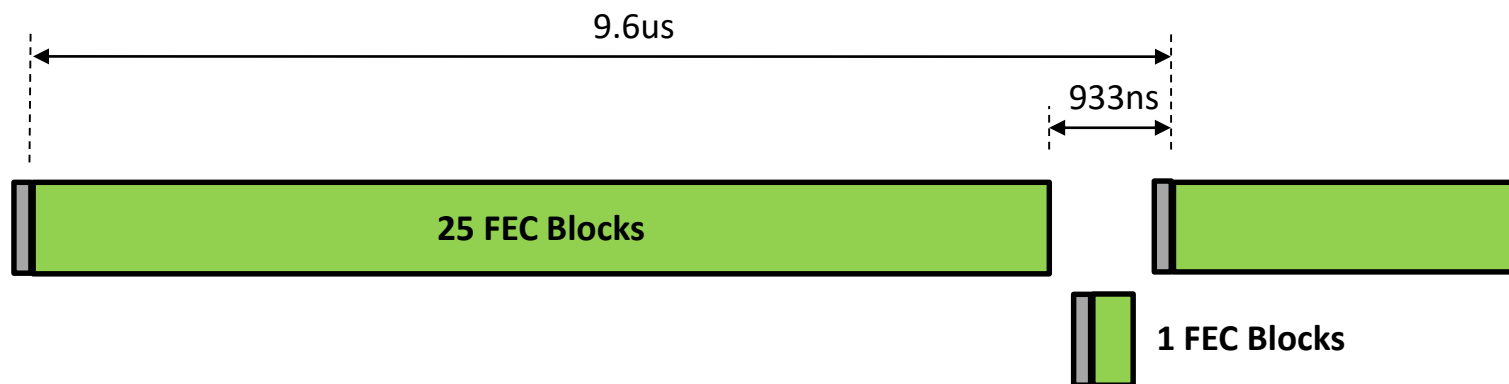
# Comments

- Multiple TDD based options for asymmetric Ethernet link was previously presented to the 802.3dm task force<sup>1,2</sup>.
- Many participants and attendees have since shown interest to use the 64/65 encoding and stay with 3GHz/6GHz baud rates and lower complexity FEC.
- This joint presentation consolidates the proposal into a single TDD based solution with a note that the authors are open to further constructive comments from the task force members.
- The parameters are also optimized for ease of implementations.

1- [https://www.ieee802.org/3/dm/public/1124/Chini\\_3dm\\_01a\\_1124.pdf](https://www.ieee802.org/3/dm/public/1124/Chini_3dm_01a_1124.pdf)

2- [https://www.ieee802.org/3/dm/public/1124/Dalmia\\_Goel\\_3dm\\_01a\\_11112024.pdf](https://www.ieee802.org/3/dm/public/1124/Dalmia_Goel_3dm_01a_11112024.pdf)

# TDD Baseline



<b>Ethernet Packets Boundary</b>	64b/65b encoding
<b>Speed Grades (@ xMII)</b>	<b>Forward Link:</b> 2.5Gbps, 5Gbps & 10Gbps <b>Reverse Link:</b> 100Mbps (PAM2, 2.5Gbps line rate )
<b>Modulation</b>	<b>PAM2</b> for 2.5Gbps and 5Gbps <b>PAM4</b> for 10Gbps
<b>Baud Rate</b>	3.0Gsps for 2.5Gbps (forward and reverse link) 6.0Gsps for 5Gbps and 10Gbps (forward link)
<b>TDD Cycle</b>	9.6μs for ALL speed grades
<b>FEC Block Period</b>	346.66ns for ALL speed grades

# TDD Baseline (cntd.)

<b>FEC Type</b>	<b>S=8bit Reed-Solomon Code</b> RS(130S, 122S), L=1 for 2.5Gbps (120Byte at XGMII, 15 x 65b+1b OAM) RS(130S, 122S), L=2 for 5.0Gbps (240Byte at XGMII, 30 x 65b+2b OAM) RS(130S, 122S), L=4 for 10Gbps (480Byte at XGMII, 60 x 65b+4b OAM) L= 2 and 4 shows number of interleaved RS codes
<b>IBG (Inter Burst Gap)</b> normal mode transmit gap	2 x 106.66ns
<b>Total Refresh (Resync) Sequence</b>	1120b PAM2 at 3Gbps 2240b PAM2 at 6Gbps
<b>FIFO</b>	<b>Forward link :</b> 292-Byte at 2.5Gbps 584-Byte at 5.0Gbps 1168-Byte at 10Gbps <b>Reverse link :</b> 116-Byte at 100Mbps

Let's define  $T = \text{One TDD Cycle} / 360$

Then note that:

One FEC Block Period = 13T

IBG = 4T

Total Refresh Period = 14T

Thank you for your attention!

Questions?