Updated ACT Text Proposal

Contribution to 802.3dm Task Force

September 15, 2025

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Introduction

- This presentation describes updates to the ACT text presented in the July plenary meeting in Madrid: jonsson_etal_3dm_01a_07_28_25.pdf
- The 802.3dm editor, Natalie Wienckowski, has provided editorial support in updating the text proposal
- This time there is only on significant update to the text, that is related to Link sync state machine

- The updated text is available at:
  https://ieee802.org/3/dm/public/0925/8023-200_ACT_D0p7a.pdf

- The current ACT text proposal is mostly complete
- The few remaining items are listed on slide 4

Updates to PHY Link Synchronization

What:

- The PHY Link Synchronization has been updated and described in more detail

Why:

- Provided more detailed description of the PHY Link Synchronization

Where:

- The updates are reflected in Clause 200.8.3

Key Open Items in the Draft Text Proposal

- Additional test mode description
- Close remaining TBD

TBD that need to be closed

Text	Page	Line
Service primitives and interfaces in the high speed direction are as described in TBD.	46	51
The PMA generates this primitive to indicate a change in link_status in compliance with the state diagram given in Figure 200–TBD	48	35
PCS Transmit shall generate a sequence (Tn) defined in TBD to the PMA via the PMA_UNITDATA.request primitive.	58	15
By this mechanism, a PHY indicates the status of its own receiver to the link partner and makes requests for remote transmitter settings. (See TBD.)	58	19
This mode is further described as test mode 7 in TBD	66	24
The 100M+MultiGBASE-T1/V1 PMA takes no longer than TBD(100) ms to enter the PCS_DATA state after exiting from reset or low power mode (see Figure 149-32).	69	11
If the PMA_CONFIG.indication parameter config is SLAVE, the PMA Transmit function shall source TX_TCLK from the recovered clock of 149.4.2.8 while meeting the jitter requirements of TBD.	69	28
Table 200–5—Startup timing maximums for LEADER	70	43
Table 200–6—Startup timing maximums for FOLLOWER	71	1
The ideal PAM2 level of TBD should be used for effective symbol levels of ES1 and ES2.	77	46
When transmitting at a data rate of 100M and measured with 100 W termination, the transmit differential signal at the MDI shall be less than TBD V peak-to-peak	81	46
When transmitting at a data rate of 100M and measured with 50 Ω termination, the transmit differential signal at the MDI shall be less than TBD V peak-to-peak.	87	44
The PMA provides the Receive function specified in TBD in accordance with the electrical specifications of this clause using cabling that is within the limits specified in 200.13.	88	3
External noise rejection is TBD.	88	16
The maximum link delay of each MultiG+100M/100M+MultiGBASE-T1 link shall be TBD.	91	8
Where coaxial cabling is used, the minimum screening attenuation for a link segment is TBD dB for all frequencies between 30 MHz and Fmax MHz.	92	3
The maximum link delay of each MultiG+100M/100M+MultiGBASE-V1 link shall be TBD.	92	10
The power sum ANEXT loss between a disturbed link segment and the disturbing link segment shall meet the values determined using Equation (TBD).	92	16
The power sum AACRF loss between a disturbed link segment and the disturbing link segment shall meet the values determined using Equation (TBD).	92	31
The delay limits for 100Mb/s low speed direction are TBD.	95	30

Summary

- This presentation reflects updates to the unified ACT proposal
- The updated text proposal is made in the hope that it may help build consensus within the 802.3dm Task Force

- Our goal is to have the text proposal completed by the November meeting

Comments and Collaborators Wanted

Thank You