

Meeting Minutes: IEEE P802.3dq Pin-Optimized PHY Interface (POPI) Task Force

October 28, 2025
Electronic Ad Hoc Meeting
(all times are in PDT)

Prepared by George Zimmerman

See Appendix A for Attendance

IEEE P802.3dq Task Force meeting convened at **8:05 AM PDT, Monday, October 28, 2025**, by Jason Potterf, IEEE P802.3dq Task Force Chair.

Presentation: https://www.ieee802.org/3/dq/public/2025-10-28/POPI_Agenda_2025-10-28_v01.pdf

Presenter: Jason Potterf, Cisco, Chair

Mr. Potterf turned to presentation above and reviewed the agenda for the meeting on slide 2. He noted that as this was the first meeting, there were no previous minutes or liaisons to review.

Approval of Agenda: Mr. Potterf asked if there were any additions or corrections to the agenda on slide 2, there were none. He then asked if there was any objection to approving the agenda there were none.
(AGENDA APPROVED 8:06 AM)

The Chair then resumed the review of the agenda deck

Mr. Potterf noted that there should be no recording or photography without permission.

The chair discussed decorum and processes for joining the queue using the conference tool.

The chair discussed the goals of the meeting and noted that the big ticket item was to finalize the objectives.

Mr. Potterf then continued review of the presentation, reviewing information for the reflector, private area, and ground rules.

The Chair announced this meeting was being conducted as an electronic ad hoc meeting. There would be no motions of business.

Attendance

The chair noted that the attendance would be taken from Webex. He noted that because the meeting was being conducted as a separate electronic ad hoc, participants would not be receiving credit for 802.3 voting rights. He reminded the group that attendance credit at 802.3 plenaries and interim meeting series was the way to achieve voting status.

IEEE Structure, Policies

Mr. Potterf continued review of the IEEE SA structure, where to find the bylaws, policies & procedures documents, and operations manuals which hierarchically govern IEEE SA, IEEE 802 LMSC, and IEEE 802.3 activities.

He also asked whether anyone in the room or online had not seen the various policy slides. There were no responses. He therefore announced that he would show the slides.

IEEE SA Patent Policy, Mr. Potterf reviewed the IEEE SA Patent Policy (slides labeled as 15 – 19 (actual PDF deck pages 13-17) in the agenda deck). During this, he showed and read aloud slides 1 & 2 of the IEEE SA patent policy from the agenda deck (slides labeled 16-17 and made the call for patents on the slide labeled “Ways to Inform IEEE” **(8:07AM)**).

There was no response to the call for patents at **8:07 AM**.

He then showed and read aloud slide 3 of the patent policy and showed slide 4 of the patent policy.

Other IEEE Policies

Mr. Potterf showed and read aloud the slides on the IEEE SA copyright, Participant behavior (ethics), IEEE individual participation, and fair and equitable consideration policies as shown in the agenda deck.

Mr. Potterf asked whether anyone was attending from the press including those who would run a public blog on this meeting – there were no indications from the group. **(8:08 AM)**

There were no questions.

Mr. Potterf reviewed the standards development process for IEEE 802.3 and where this Task Force is in that process.

LIAISONS

The chair noted that the Task Force had received no liaisons.

Task Force Documents

The Chair reviewed that the group had a newly approved PAR. The PAR and CSDs could be found posted on the Task Force website <https://www.ieee802.org/3/dq/> .

Objectives

The chair then moved to a presentation to guide discussion on the adoption of objectives.

https://www.ieee802.org/3/dq/public/2025-10-28/POPI_DRAFT_Objectives_2025-10-21_v02.pdf

The presentation discussed several potential objectives for the Task Force to consider forwarding to the working group.

There was discussion of the wording of the proposed objectives and edits were made based on the discussion. The result of the edits is posted at:

https://www.ieee802.org/3/dq/public/2025-10-28/POPI_DRAFT_Objectives_2025-10-21_v03.pdf

During the discussion participants offered that more detail might be needed in:

- The extent and requirement of the set of PLS Service Primitives and other primitives that the new interface might serve.
- The potential structure of the specification of the interface to various PHYs, both served with the logical interface and with the electrical interface
- The speeds of PHYs served with an electrical interface – there was general agreement to specify up to 100 Mb/s with an electrical interface, but not preclude scaling to GMII or XGMII PHY types.
 - o How architecturally to specify the interface to be scalable to PHYs using MII, GMII, and XGMII, as well as PHY connecting to more than just the PLS service interface.
 - o Focusing the objectives on an efficient 100 Mb/s interface, while providing extensibility to higher speeds.
 - o Provide a basis model for providing control and out-of-band signaling in the logical interface (as well as the packet data)
- A review of the deficiencies of existing interfaces might be useful.

Future Meetings & Adjournment

The next meeting was announced for: **Tuesday November 25, 2025, with a submission deadline by Sunday November 23, 2025, and a request deadline of Thursday November 20, 2025.**

The chair then asked regarding topics of discussion for the next meeting, and the list of topics discussed during objectives was reviewed.

Having exhausted the agenda, the meeting was adjourned at **9:24 AM**.

Appendix A: Attendees at the IEEE P802.3dq Pin-Optimized PHY Interface (POPI) Task Force Meeting, October 28, 2025

Taken from WebEx.

Name	Affiliation
Antonio Orzelli	Canova Tech
Brian Mowad	Cisco
Chad Jones	Cisco
Daniel Hopf	AUMOVIO
George Zimmerman	CME Consulting/ADI, APL Gp, Cisco, Infineon, OnSemi, Sony
Gian Marco Bo	CanovaTech
Jason Potterf	Cisco
Jason Rock	Dell
Jörg Kock	NXP
Lokesh Kabra	Synopsys
Peter Jones	Cisco
Thorsten Hoffleit	Renesas
Tim Baggett	Microchip
Valerie Maguire	Copperopolis, affl w/ CME Consulting and Cisco
Xiang He	Huawei
Xuebo Wang	Huawei